

An ad-hoc implementation of a remote laboratory

Azad, Siavoosh Payandeh; Kinks, Hannes; Tajammul, Muhammad Adeel; Ellervee, Peeter 2015 International Conference on Microelectronic Systems Education : MSE '15 : Pittsburgh, PA, May 20-21, 2015 / p. 48-51 : ill <http://dx.doi.org/10.1109/MSE.2015.7160015>

Code compaction within CGRAs

Tajammul, Muhammad Adeel; Jafri, Syed Mohammad Asad Hassan; **Ellervee, Peeter** Proceedings of the 8th Annual Conference of the Estonian National Doctoral School in Information and Communication Technologies : December 5-6, 2014, Rakvere 2014 / p. 133-136 : ill

Digital system modeling and synthesis as an introduction to computer systems engineering

Tajammul, Muhammad Adeel; Azad, Siavoosh Payandeh; Ellervee, Peeter 2015 International Conference on Microelectronic Systems Education : MSE '15 : Pittsburgh, PA, May 20-21, 2015 / p. 52-55 : ill <http://dx.doi.org/10.1109/MSE.2015.7160016>

DyMeP : an infrastructure to support dynamic memory binding for runtime mapping in CGRAs

Tajammul, Muhammad Adeel; Jafri, Syed Mohammad Asad Hassan; Ellervee, Peeter; Hemani, Ahmed; Tenhunen, Hannu; Plosila, Juha 28th International Conference on VLSI Design : held concurrently with 14th International Conference on Embedded Systems : 3-7 January 2015, Bangalore, India : proceedings 2015 / p. 547-552 : ill <https://doi.org/10.1109/VLSID.2015.98> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

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Polymorphic configuration architecture for CGRAs

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TransMem : a memory architecture to support dynamic remapping and parallelism in low power high performance CGRAs

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