

Automated minimization of concurrent online checkers for network-on-chips

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AWAIT : an ultra-lightweight soft-error mitigation mechanism for network-on-chip links

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Comprehensive performance and robustness analysis of 2D turn models for network-on-chips

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Design concept and microarchitecture of network-on-chip with best-effort and guaranteed-throughput services

Samman, Faizal; Hollstein, Thomas International journal of innovative computing, information and control 2019 / p. 305-319 : ill <https://doi.org/10.24507/ijicic.15.01.305> <http://www.ijicic.net/> <http://www.ijicic.net/servlet/Download?contentID=CONT5b67c7cbc23142feade914ceeabbd4d> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

DoS attack detection and path collision localization in NoC-based MPSoC architectures

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Dynamic and distributed security management for NoC Based MPSoCs

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Network-on-Chip with equality-of-service : a local fair runtime arbitration method for global fair bandwidth share

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QoSInNoC: analysis of QoS-aware NoC architectures for mixed-criticality applications

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