Extended checkers for control part of routers in network-on-chips

Hariharan, Ranganathan; Niazmand, Behrad; Hollstein, Thomas; Raik, Jaan; Jervan, Gert MEDIAN 2015: the 4th Workshop on Manufacturable and Dependable Multicore Architectures at Nanoscale: March 13, 2015, Grenoble, France 2015 / p. 36-39: ill