

### **Evaluating NTT/INTT implementation styles for post-quantum cryptography**

**Imran, Malik**; Khan, Safiullah; Khalid, Ayesha; Rafferty, Ciara; Ali Shah, Yasir; Pagliarini, Samuel; Rashid, Muhammad; O'Neill, Maire  
IEEE Embedded Systems Letters 2024 / p. 485 - 488 <https://doi.org/10.1109/LES.2024.3410516> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

### **Fast iterative circuits and RAM-based mergers to accelerate data sort in software/hardware systems**

Sklyarov, Valery; Skliarova, Iuliia; **Rjabov, Artjom; Sudnitsõn, Aleksander** Proceedings of the Estonian Academy of Sciences  
2017 / p. 323-335 : ill <https://doi.org/10.3176/proc.2017.3.07> [http://www.eester.ee/record=b2355998\\*est](http://www.eester.ee/record=b2355998*est) [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)