

Bandwidth Reduction DoS attacks in Multi-Tenant NoC-based MPSoCs : detection and avoidance strategies = Ribalaiuse vähendamise DoS-rünnakud mitme rentnikuga NoC-põhiste MPSoC-de puhul : tuvastamise ja vältimise strateegiad
Chaves Arroyave, Cesar Giovanni 2023 <https://doi.org/10.23658/taltech.8/2023> <https://digikogu.taltech.ee/et/Item/60b3f5ee-3a53-43c6-b79e-d6a8cbbc3489> https://www.estel.ee/record=b5548907*est

CAESAR-MPSoC : dynamic and efficient MPSoC security zones

Azad, Siavoosh Payandeh; Tempelmeier, Michael; Jervan, Gert; Sepulveda, Johanna 2019 IEEE Computer Society Annual Symposium on VLSI : ISVLSI 2019 : proceedings 2019 / p. 477-482 : ill <https://doi.org/10.1109/ISVLSI.2019.00092>

Design and verification of secure cache wrapper against access-driven side-channel attacks

Niazmand, Behrad; Azad, Siavoosh Payandeh; Jervan, Gert; Sepulveda, Johanna Euromicro Conference on Digital System Design : DSD 2019 : 28 - 30 August 2019 Kallithea, Chalkidiki, Greece : proceedings 2019 / p. 672-676 : ill <https://doi.org/10.1109/DSD.2019.00108>

Detecting and mitigating Low-and-Slow DoS attacks in NoC-based MPSoCs

Chaves Arroyave, Cesar Giovanni; Azad, Siavoosh Payandeh; Sepulveda, Johanna; Hollstein, Thomas 2019 14th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC) : July 1-3 2019, York - United Kingdom : proceedings 2019 / p. 82-89 : ill <https://doi.org/10.1109/ReCoSoC48741.2019.9034934>

Diagnosing DoS attacks in NoC-based MPSoCs

Chaves Arroyave, Cesar Giovanni; Azad, Siavoosh Payandeh; Hollstein, Thomas; Sepulveda, Johanna Testmethoden und Zuverlässigkeit von Schaltungen und Systemen, TUZ 2019 2019 / p. [39-41] <https://www.researchgate.net/publication/333756736>

A Distributed DoS detection scheme for NoC-based MPSoCs

Chaves Arroyave, Cesar Giovanni; Azad, Siavoosh Payandeh; Hollstein, Thomas; Sepulveda, Johanna 2018 IEEE Nordic Circuits and Systems Conference (NORCAS) : NORCHIP and International Symposium of System-on-Chip (SoC) : 30-31 October 2018, Tallinn, Estonia : proceedings in IEEE Xplore 2018 / 6 p. : ill <https://doi.org/10.1109/NORCHIP.2018.8573524>

DoS attack detection and path collision localization in NoC-based MPSoC architectures

Chaves Arroyave, Cesar Giovanni; Azad, Siavoosh Payandeh; Hollstein, Thomas; Sepulveda, Johanna Journal of low power electronics and applications 2019 / art. 7, 20 p. : ill <https://doi.org/10.3390/jlpea9010007> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Dynamic and distributed security management for NoC Based MPSoCs

Azad, Siavoosh Payandeh; Jervan, Gert; Sepulveda, Johanna Computational Science - ICCS 2019 : 19th International Conference, Faro, Portugal, June 12-14, 2019 : Proceedings, Part II 2019 / p. 649-662 : ill https://doi.org/10.1007/978-3-030-22741-8_4 [Conference proceeding at Scopus](#) [Article at Scopus](#) [Conference proceeding at WOS](#) [Article at WOS](#)

Enabling secure MPSoC dynamic operation through protected communication

Azad, Siavoosh Payandeh; Niazmand, Behrad; Jervan, Gert; Sepulveda, Johanna 2018 25th IEEE International Conference on Electronics Circuits and Systems (ICECS), Bordeaux, France, December 9-12, 2018 2018 / p. 481-484 : ill <https://doi.org/10.1109/ICECS.2018.8617940>

LiD-CAT: A lightweight detector for cache ATtacks

Reinbrecht, Cezar; Hamdioui, Said; Taouil, Mottaqiallah; Niazmand, Behrad; Ghasempouri, Tara; Raik, Jaan; Sepulveda, Johanna 2020 IEEE European Test Symposium (ETS) : ETS 2020, May 25-29, 2020 Tallinn, Estonia : proceedings 2020 / 6 p. : ill <https://doi.org/10.1109/ETS48528.2020.9131603>

Lightweight monitoring scheme for flooding DoS Attack detection in multi-tenant MPSoCs

Chaves Arroyave, Cesar Giovanni; Sepulveda, Johanna; Hollstein, Thomas 2021 IEEE International Symposium on Circuits and Systems (ISCAS), Daegu, Korea May 22-28, 2021 : proceedings 2021 / 5 p <https://doi.org/10.1109/ISCAS51556.2021.9401153> [Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Towards formal verification of cache access-based side-channel attacks

Niazmand, Behrad; Reinbrecht, Cezar; Raik, Jaan; Jervan, Gert; Sepulveda, Johanna Testmethoden und Zuverlässigkeit von Schaltungen und Systemen, TUZ 2019 2019 / 2 p. : tab <http://www.informatik.uni-bremen.de/tuz/2019>

Understanding MPSoCs : exploiting memory microarchitectural vulnerabilities of high performance NoC-based MPSoCs

Sepulveda, Johanna; Azad, Siavoosh Payandeh; Niazmand, Behrad; Jervan, Gert SAMOS '18 : Proceedings of the 18th International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation, Pythagorion, Greece, July 15-19, 2018 2018 / p. 162-166 <https://doi.org/10.1145/3229631.3239367> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)