

Beta assertive graphs : proofs of assertions with quantification

Bellucci, Francesco; **Chiffi, Daniele; Pietarinen, Ahti-Veikko** Journal of applied logics : IfCoLog journal of logics and their applications 2021 / 23 p. : ill <https://www.collegepublications.co.uk/downloads/ifcolog00044.pdf> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Design understanding : from logic to specification

Fey, Goerschwin; **Ghasempouri, Tara**; Jacobs, Swen; **Raik, Jaan** Proceedings of the 2018 IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC) : October 8-10, 2018, Verona, Italy 2018 / p. 172–175 : ill <https://doi.org/10.1109/VLSI-SoC.2018.8644732>

Icons, interrogations, and graphs: On Peirce's integrated notion of abduction

Pietarinen, Ahti-Veikko; Bellucci, Francesco Transactions of the Charles S. Peirce Society 2020 / p. 43-61
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On the logical philosophy of assertive graphs

Chiffi, Daniele; Pietarinen, Ahti-Veikko Journal of logic, language and information 2020 / 21 p <https://doi.org/10.1007/s10849-020-09315-6> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

SCARF : securing chips with a robust framework against fabrication-time hardware trojans

Eslami, Mohammad; Ghasempouri, Tara; Pagliarini, Samuel Nascimento IEEE Transactions on Computers 2024 / p. 2761-2775 <https://doi.org/10.1109/TC.2024.3449082> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

SCARF : securing chips with a robust framework against fabrication-time hardware Trojans : preprint

Eslami, Mohammad; Ghasempouri, Tara; Pagliarini, Samuel Nascimento arXiv.org 2024 / 14 p. : ill
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