

Network-based hardware accelerators for parallel data processing = Võrgupõhised riistvarakiirendid paralleelseks andmetöötluseks

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Parallel processing in FPGA-based digital circuits and systems

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Pipelined execution of data-parallel algorithms

Gorev, Maksim; Ubar, Raimund-Johannes BEC 2014 : 2014 14th Biennial Baltic Electronics Conference : proceedings of the 14th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 6-8, 2014, Tallinn, Estonia 2014 / p. 109-112 : ill

Relation learning with bar charts

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See mitmetahuline rööpsus

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3D parallel fault simulation

Gorev, Maksim; Ubar, Raimund-Johannes Proceedings of the 8th Annual Conference of the Estonian National Doctoral School in Information and Communication Technologies : December 5-6, 2014, Rakvere 2014 / p. 39-42 : ill

Uus sõna arvutusmaailma kiiruses, kvaliteedis ja energia kokkuhoius - TTÜ avas arvutusfarmi

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Об одном методе разработки параллельных алгоритмов

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