

Analysis and comparison of attainable hardware acceleration in all programmable systems-on-chip

Sklyarov, Valery; Skliarova, Ioulia; Silva, João; **Sudnitsõn, Aleksander** Euromicro Conference on Digital System Design : DSD 2015 : 26-28 August 2015, Funchal, Madeira, Portugal : proceedings 2015 / p. 345-352 : ill <http://dx.doi.org/10.1109/DSD.2015.45>

Application of extensible processing platforms for experiments with FPGA-based circuits

Sklyarov, Valery; Skliarova, Ioulia; Silva, João; **Rjabov, Artjom; Sudnitsõn, Aleksander** MELECON 2014 : 2014 17th IEEE Mediterranean Electrotechnical Conference : 13-16 April 2014, Beirut, Lebanon 2014 / p. 467-471 : ill <https://doi.org/10.1109/MELCON.2014.6820579> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Design space exploration in multi-level computing systems

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Hardware accelerators for information retrieval and data mining

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Hardware/software co-design for programmable systems-on-chip

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