

Application specific processor synthesis from assembler code using VHDL

Udre, Jüri Asian-Pacific Conference on Hardware Description Languages, Standards and Applications CAPCHDLA-93, Australia 1993

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A CAD system for decomposition oriented synthesis

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Decomposition - oriented synthesis of finite state machines

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Decomposition-oriented extraction of the control path from high-level behavioral descriptions

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Informational modelling of FSM networks

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Transforming VHDL descriptions from behavior to structure

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