

ASIC prototyping for ALTERA devices on ICUBE programmable interconnect

Krupnova, Helena European Design and Test Conference - EDAMPTC'96, Paris, March 11-14, 1996 : proceedings 1996 / p. 223-227

ASIC prototyping for reprogrammable implementations of large ASICs

Krupnova, Helena ACM/SIGDA 4th International Symposium on FPGAs, Monterey, USA, February 11-13, 1996 : proceedings 1996

ASIC prototyping with area oriented mapping for ALTERA/FLEX devices

Krupnova, Helena Synthesis and System Integration of Mixed Technologies - SASMI'95, Nara, Japan, August 25-26, 1995 : proceedings 1996

Automatic circuit partitioning for FPGAs

Krupnova, Helena; Abbara, A.; Brasen, D.; Saucier, Gabriele BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 389-392: ill

Circuit partitioning method for FPGAs

Krupnova, Helena; Saucier, Gabriele Proceedings of the Estonian Academy of Sciences. Engineering 1997 / 4, p. 283-292

A constraint-driven gate-level test generator

Raik, Jaan; Ubar, Raimund-Johannes; Jervan, Gert; Krupnova, Helena BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 237-240: ill

Constraints analysis in hierarchical test generation for digital systems

Ubar, Raimund-Johannes; Krupnova, Helena BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 1 1994 / p. 313-318: ill

Functional test program generation for digital systems

Ubar, Raimund-Johannes; Dušina, Julia; Krupnova, Helena; Storožev, Sergei; Zaugarov, Viktor Testmethoden und Zuverlässigkeit von Schaltungen und Systemen : proceedings of the 6th workshop, Vaals (Niederlande), March 6-8, 1994 1994 / p. 14-18: ill

Methode et outil de prototypage des systemes integres sur FPGAs : [doktoriväitekiri]

Krupnova, Helena 1999

Model synthesis from VHDL for the functional test generation system

Krupnova, Helena 1993 https://www.ester.ee/record=b2090509*est

User-guided hierarchical FPGA partitioning

Krupnova, Helena; Saucier, Gabriele BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 187-190: ill