

Latch-Based logic locking

Sweeney, J.; Mohammed Zackriya, V.; **Pagliarini, Samuel Nascimento**; Pileggi, Larry Proceedings of the IEEE International Symposium on Hardware Oriented Security and Trust, HOST 2020 2020 / p. 132–141 : ill
<https://doi.org/10.1109/HOST45689.2020.9300256>

Minimal realizations of nonlinear time-delay systems

Kaldmäe, Arvo; Zbigniew, Bartosiewicz; **Kotta, Ülle**; Wyrwas, Malgorzata IEEE transactions on automatic control 2023 / p. 6766 - 6772 <https://doi.org/10.1109/TAC.2023.3240124> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)