

From FPGAs to obfuscated eASICs : design and security trade-offs

Abideen, Zain Ul; Perez, Tiago Diadami; Pagliarini, Samuel Nascimento IEEE Asian Hardware-Oriented Security and Trust (AsianHOST) 2021 / p. 1-4 <https://doi.org/10.1109/AsianHOST53231.2021.9699758>

G-GPU : a fully-automated generator of GPU-like ASIC accelerators

Perez, Tiago Diadami; Gonçalves, Marcio M.; Gobatto, Leonardo; Brandalero, Marcelo; Azambuja, Jose Rodrigo; Pagliarini, Samuel Nascimento 2022 Design, Automation & Test in Europe Conference & Exhibition (DATE 2022) : proceedings 2022 / p. 544 - 547 <https://doi.org/10.23919/DATE54114.2022.9774758>

Hardware trojan insertion in finalized layouts : from methodology to a silicon demonstration

Perez, Tiago Diadami; Pagliarini, Samuel Nascimento IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems 2023 / p. 2094-2107 <https://doi.org/10.1109/TCAD.2022.3223846> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Impact of orientation on the bias of SRAM-based PUFs

Abideen, Zain Ul; Wang, Rui; Perez, Tiago Diadami; Schrijen, Geert-Jan; Pagliarini, Samuel Nascimento arXiv.org 2023 / 7 p. : ill <https://doi.org/10.48550/arXiv.2308.06730>

Impact of orientation on the bias of SRAM-based PUFs

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A pragmatic methodology for blind hardware trojan insertion in finalized layouts

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SALSy : security-aware layout synthesis

Eslami, Mohammad; Perez, Tiago Diadami; Pagliarini, Samuel Nascimento arXiv.org 2024 / 13 p. : ill <https://doi.org/10.48550/arXiv.2308.06201>

A security-aware and LUT-based CAD flow for the physical synthesis of hASICs

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Security-aware physical synthesis of integrated circuits = Integraallülituste turvateadlik füüsiline süntees

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A side-channel hardware trojan in 65nm CMOS with 2 μ W precision and multi-bit leakage capability

Perez, Tiago Diadami; Pagliarini, Samuel Nascimento 2022 27th Asia and South Pacific Design Automation Conference (ASP-DAC) : 17-20 January 2022 : Taipei, Taiwan 2022 / p. 9-10 : ill <https://doi.org/10.1109/ASP-DAC52403.2022.9712490>

Side-channel Trojan insertion - a practical foundry-side attack via ECO

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A survey on split manufacturing : attacks, defenses, and challenges

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