From FPGAs to obfuscated eASICs: design and security trade-offs

Abideen, Zain UI; Perez, Tiago Diadami; Pagliarini, Samuel Nascimento IEEE Asian Hardware-Oriented Security and Trust (AsianHOST) 2021 / p. 1-4 https://doi.org/10.1109/AsianHOST53231.2021.9699758

A security-aware and LUT-based CAD flow for the physical synthesis of hASICs

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