

### **Benchmarking advanced security closure of physical layouts**

**Eslami, Mohammad**; Knechtel, Johann; Sinanoglu, Ozgur; Karri, Ramesh; **Pagliarini, Samuel Nascimento** ISPD '23 : proceedings of the 2023 International Symposium on Physical Design 2023 / p. 256-264 <https://doi.org/10.1145/3569052.3578924>  
<https://dl.acm.org/doi/pdf/10.1145/3569052.3578924>

### **On the use of defensive schemes for hardware security = Kaitseskeemid riistvara turvalisuse tagamiseks**

**Eslami, Mohammad** 2024 [https://www.eester.ee/record=b5701420\\*est](https://www.eester.ee/record=b5701420*est) <https://doi.org/10.23658/taltech.53/2024>  
<https://digikogu.taltech.ee/et/Item/068530be-4810-4489-9604-fb838d298b45>

### **Reusing verification assertions as security checkers for Hardware Trojan detection**

**Eslami, Mohammad**; **Ghasempouri, Tara**; **Pagliarini, Samuel Nascimento** 2022 23rd International Symposium on Quality Electronic Design (ISQED), Santa Clara, CA, USA : 06-07 April 2022 2022 / p. 1-6 : ill  
<https://doi.org/10.1109/ISQED54688.2022.9806292>

### **SALSy : security-aware layout synthesis**

**Eslami, Mohammad**; **Perez, Tiago Diadami**; **Pagliarini, Samuel Nascimento** arXiv.org 2024 / 13 p. : ill  
<https://doi.org/10.48550/arXiv.2308.06201>

### **SCARF : securing chips with a robust framework against fabrication-time hardware trojans**

**Eslami, Mohammad**; **Ghasempouri, Tara**; **Pagliarini, Samuel Nascimento** IEEE Transactions on Computers 2024 / p. 2761-2775 <https://doi.org/10.1109/TC.2024.3449082> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

### **SCARF : securing chips with a robust framework against fabrication-time hardware Trojans : preprint**

**Eslami, Mohammad**; **Ghasempouri, Tara**; **Pagliarini, Samuel Nascimento** arXiv.org 2024 / 14 p. : ill  
<https://doi.org/10.48550/arXiv.2402.12162>