

Distributed approach for parallel exact critical path tracing fault simulation

Ivask, Eero; Devadze, Sergei; Ubar, Raimund-Johannes International journal of microelectronics and computer science 2010 / p. 165-174 : ill

Distributed fault simulation with collaborative load balancing for VLSI circuits

Ivask, Eero; Devadze, Sergei; Ubar, Raimund-Johannes Scalable computing : practice and experience 2011 / p. 153-163 : ill

Exact parallel critical path fault tracing to speed-up fault simulation in sequential circuits

Kõusaar, Jaak; Ubar, Raimund-Johannes; Kostin, Sergei; Devadze, Sergei; Raik, Jaan International journal of microelectronics and computer science 2018 / p. 9–18 <https://ijmcs.dmcs.pl/web/guest/vol.-9-no.-1>
https://ijmcs.dmcs.pl/documents/10630/345460/IJMCS_1_2018_2.pdf