

**Quasistatic methods for measuring the emitter, collector and base resistances in advanced bipolar transistors**

Bubennikov, Alexander N.; Kobosev, Gennady BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 117-120: ill

**Two dimensional modeling of the shunted emitter area for power shepistors**

Rang, Toomas; Saul, Indrek Modeling, Simulation and Control 1989 / p. 29-39