

Five-Stage, Power Efficient, Dual Rail, 100MHz, 10dB Programmable Gain Amplifier with Down-Stepping Functions in 28nm CMOS

Kampus, Vahur; Teschner, Robert; Gaier, Ulrich; Linder, Thomas; Nössing, Gerhard; Trojer, Martin IEEE International Symposium on Circuits and Systems (ISCAS), Sapporo, Japan, 26-29 May 2019 : proceedings 2019 / p. 1-5
<https://doi.org/10.1109/ISCAS.2019.8702077>