

A scalable static test set compaction method for sequential circuits

Aleksejev, Igor; Raik, Jaan; Jutman, Artur; Ubar, Raimund-Johannes Proceedings of the 9th IEEE Latin-American Test Workshop : LATW2008 : February 17-20, 2008, Puebla, Mexico 2008 / p. 87-92 : ill

Action and reaction in electrical circuits

Järvik, Jaan 5th International Symposium "Topical Problems in the Field of Electrical and Power Engineering". Doctoral School of Energy and Geotechnology : Kuressaare, January 14-19, 2008 2008 / p. 208-215 : ill

Action and reaction in electrical circuits. Part I

Järvik, Jaan EPE 2010 : Electric Power Engineering 11th International Scientific Conference : Brno, 2010 : proceedings 2010 / p. 43-48

Action and reaction in electrical equipment. Part II

Järvik, Jaan EPE 2010 : Electric Power Engineering 11th International Scientific Conference : Brno, 2010 : proceedings 2010 / p. 49-54

Active filters based on GIC-array

Kukk, Vello ECCTD '95 : proceedings of the 12th European Conference on Circuit Theory and Design : Istanbul, Turkey, 27-31 August 1995. Volume 1 of 2 1995 / p. 615-618

AdAM: Adaptive Approximate Multiplier for Fault Tolerance in DNN Accelerators

Taheri, Mahdi; Cherezova, Natalia; Nazari, Samira; Azarpeyvand, Ali; Ghasempouri, Tara; Daneshtalab, Masoud; Raik, Jaan; Jenihhin, Maksim IEEE transactions on device and materials reliability 2024 / p. 66-75 : ill <https://doi.org/10.1109/TDMR.2024.3523386>

AdAM: adaptive fault-tolerant approximate multiplier for edge DNN accelerators

Taheri, Mahdi; Cherezova, Natalia; Nazari, Samira; Rafiq, Ahsan; Azarpeyvand, Ali; Ghasempouri, Tara; Daneshtalab, Masoud; Raik, Jaan; Jenihhin, Maksim 2024 IEEE European Test Symposium (ETS): ETS 2024 : May 20-24, 2024, The Hague, Netherlands : proceedings 2024 <https://doi.org/10.1109/ETS61313.2024.10567161> [Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Adjusted electrical equivalent circuit model of induction motor with broken rotor bars and eccentricity faults

Petrov, Aleksei; Plokhov, Igor; Rassõlkin, Anton; Vaimann, Toomas; Kallaste, Ants; Belahcen, Anouar Proceeding of the 2017 IEEE 11th International Symposium on Diagnostics for Electric Machines, Power Electronics and Drives (SDEMPED) : [Tinos (Greece), August 29 - September 01, 2017] 2017 / p. 58-64 : ill <https://doi.org/10.1109/DEMPED.2017.8062334>

Ahelate teooria : laboratoorsed tööd

Kangur, Oleg; Ratassepp, Jaak 1988 https://www.ester.ee/record=b1224914*est

Ahelate teooria : laboratoorsed tööd. II

1986 https://www.ester.ee/record=b1262233*est

Ahelate teooria : laboratoorsed tööd. 2

1989 https://www.ester.ee/record=b1216636*est

Ahelate teooria : laboratoorsed tööd. 3

1986 https://www.ester.ee/record=b4138082*est

Ahelate teooria alused : kursusetöö koostamise metoodiline juhend erialale 0701

1984 https://www.ester.ee/record=b1245465*est

Algorithm for restructuring of structurally synthesized BDDs

Jürimägi, Lembit; Ubar, Raimund-Johannes 2019 IEEE 31st International Conference on Microelectronics : Niš, Serbia September 16th-18th, 2019 : proceedings 2019 / p. 239-242 : ill <https://doi.org/10.1109/MIEL.2019.8889578>

An improved test generation approach for sequential circuits using decision diagrams

Brik, Marina; Ubar, Raimund-Johannes BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 155-158: ill

Analysis of experience : fully web based introductory course in electrical engineering

Kukk, Vello Proceedings of the 1st International Workshop on e-learning and Virtual and Remote Laboratories : Setubal, Portugal, August 24-25, 2004 2004 / p. 111-118 <https://www.scitepress.org/PublishedPapers/2004/11505/11505.pdf>

Application of an advanced repetitive controller to mitigate harmonics in MMC with APD scheme

Madichetty, Sreedhar; Dasgupta, Abhijit; Mishra, Sambeet; Panigrahi, Chinmoy Kumar; Basha, Ghouse IEEE transactions on power electronics 2016 / p. 6112-6121 : ill <https://doi.org/10.1109/TPEL.2015.2501314> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal](#)

Characteristics of output circuit with feedback containing transmission lines

Kängsep, Eiko Analysis and synthesis of complicated systems and circuits with the aid of computers 1989 / p. 27-46

Circuit complexity problems of integrated programmable cellular neural networks

Porra, Veikko; Paasio, Ari; Halonen, Kari BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 411-416: ill https://www.ester.ee/record=b2150914*est

CMOS SP chip for resistor type semiconductor gas sensors

Kasemaa, Argo; Rang, Toomas The 7th Biennial Conference on Electronics and Microsystem Technology "Baltic Electronics Conference" : BEC 2000 : October 8 - 11, 2000, Tallinn, Estonia : conference proceedings 2000 / p. 255-256 : ill

Colored circuits

Sabolotny, Rein BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 579-582: ill https://www.ester.ee/record=b2150914*est

Comparative study of rectifier topologies for quasi-Z-source derived push-pull converter

Chub, Andrii; Husev, Oleksandr; Vinnikov, Dmitri Elektronika ir elektrotehnika = Electronics and electrical engineering 2014 / p. 29-34 : ill <https://doi.org/10.5755/j01.eee.20.6.7264> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Compensation and minimization of higher harmonics in power saturable reactors

Tellinen, Juhan International Conference Quality of Power and Standardization, June 4...7, 1996, Lohusalu, Estonia 1996 / p. 48-57: ill

Complementary Darlington circuit

Männama, Vello BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 1 1994 / p. 223-228: ill

Computer models for simulation and control of a traction supply system [Electronic resource]

Lehtla, Madis; Laugis, Juhan EPE-PEMC 2006 : 12th International Power Electronics and Motion Control Conference : Portorož, Slovenia, August 30 - September 1, 2006 : proceedings 2006 / p. 1372-1377 : ill. [CD-ROM]
https://www.researchgate.net/publication/224382843_Computer_Models_for_Simulation_and_Control_of_a_Traction_Supply_System

Constraint-based test pattern generation at the register-transfer level

Viilukas, Taavi; Raik, Jaan; Jenihhin, Maksim; Ubar, Raimund-Johannes; Krivenko, Anna Proceedings of the 13th IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 14-16, 2010, Vienna, Austria 2010 / p. 352-357 : ill
<http://dx.doi.org/10.1109/DDECS.2010.5491752>

Control and protection of a traction supply system

Lehtla, Madis 3rd International Symposium "Topical Problems of Education in the Field of Electrical and Power Engineering" : Doctoral School of Energy and Geotechnology : Kuressaare, Estonia, January 16-21, 2006 2006 / p. 93-98 : ill

Controller synthesis in control and memory centric high level synthesis system

Ellervee, Peeter; Hemani, Ahmed; Kumar, Anshul; Svantesson, Bengt; Öberg, Johnny; Tenhunen, Hannu BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 393-396: ill

Current sensing methods for portable power circuits : working out an accurate flexible integrated solution

Mihhailov, Juri 2013 https://www.ester.ee/record=b4533635*est

DC voltage sensorless predictive control of a high-efficiency PFC single-phase rectifier based on the versatile buck-boost converter

González-Castaño, Catalina; Restrepo, Carlos; Sanz, Fredy; **Chub, Andrii**; Giral, Roberto Sensors 2021 / art. 5107
<https://doi.org/10.3390/s21155107> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Design and analysis of a DC solid-state circuit breaker for residential energy router application

Rahimpour, Saeed; Husev, Oleksandr; Vinnikov, Dmitri Energies 2022 / art. 9434 <https://doi.org/10.3390/en15249434> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Determination of leakage inductances of three-winding transformer for HF-PSA resonant circuit

Šklovski, Jevgeni; Janson, Kuno; Niilo, Helar 11th International Symposium "Topical Problems in the Field of Electrical and Power Engineering." Doctoral School of Energy and Geotechnology II : Pärnu, Estonia, January 16-21, 2012 2012 / p. 113-116 : ill

Determination of the nonfaulty limits of circuit characteristics in the fault identification of discrete-type circuits

Gadzheva, Elissaveta BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 97-100: ill

A dual-buck-boost DC–DC/AC universal converter

Ong, Yao Rong; Cao, Shuyu; Lee, Sze Sing; Lim, Chee Shen; Chen, Max M.; **Vosoughi Kurdkandi, Naser**; Barzegarkhoo, Reza; Siwakoti, Yam P. Electronics (Switzerland) 2022 / art. 1973 <https://doi.org/10.3390/electronics11131973> [Journal metrics at scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Electrical and control engineering

Kalm, Evald 1990 https://www.ester.ee/record=b1233571*est

Electrical engineering and automatics

Kull, Arvo; Kiitam, Andres; Bachverk, Aleksander; Kängsep, Eiko; Jöers, Rein; Peterson, Jaak; Toomet, Madis 1989

https://www.ester.ee/record=b1212924*est

Elektrist, energiast ja füüsikast üldiselt

Kalda, Jaan Elektriala 2019 / lk. 26-27 : ill http://www.ester.ee/record=b1240496*est

Evaluation of dual-active bridge converter for DC energy buildings

Carvalho da Silva, Edivan Laercio; Blinov, Andrei; Sidorova, Aleksandra; Chub, Andrii; Vinnikov, Dmitri 2023 IEEE 17th

International Conference on Compatibility, Power Electronics and Power Engineering (CPE-POWERENG) 2023 / 6 p

<https://doi.org/10.1109/CPE-POWERENG58103.2023.10227460>

Evaluation of equivalent circuit diagrams and transfer functions for modeling of lithium-ion batteries

Rahmoun, Ahmad; Biechl, Helmut; **Rosin, Argo** Scientific Journal of Riga Technical University. Electrical, control and communication engineering 2013 / p. 34-39 : ill

Event driven method for transient simulation of high frequency digital circuits

Popescu, Gabriel-Stefan; Tarniceriu, Daniela BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 253-256: ill

Exact static compaction of independent test sequences

Raik, Jaan; Jutman, Artur; Ubar, Raimund-Johannes BEC 2002 : proceedings of the 8th Biennial Baltic Electronics Conference :

October 6-9, 2002, Tallinn, Estonia 2002 / p. 315-318 : ill

Exploiting high-level descriptions for circuits fault tolerance assessments

Benso, A.; Prinetto, Paolo; Rebaudengo, M.; Sonza Reorda, Matteo; **Raik, Jaan; Ubar, Raimund-Johannes** 1997 IEEE

International Symposium on Defect and Fault Tolerance in VLSI Systems, Paris, October 20-22, 1997 1997 / p. 212-216

<https://ieeexplore.ieee.org/document/628327>

Fast static compaction of tests composed of independent sequences : basic properties and comparison of methods

Raik, Jaan; Jutman, Artur; Ubar, Raimund-Johannes The 9th IEEE International Conference on Electronics, Circuits and Systems

: ICECS 2002 : September 15-18, 2002, Dubrovnik, Croatia. Volume II 2002 / p. 445-448 : ill

<http://dx.doi.org/10.1109/ICECS.2002.1046190> <https://ieeexplore.ieee.org/document/1046190>

Fast test pattern generation for sequential circuits using decision diagram representations

Raik, Jaan; Ubar, Raimund-Johannes Journal of electronic testing : theory and applications (JETTA) 2000 / 3, p. 213-226 : ill

<https://link.springer.com/article/10.1023/A:1008335130158>

Fault oriented test pattern generation for sequential circuits using genetic algorithms

Ivask, Eero; Raik, Jaan; Ubar, Raimund-Johannes IEEE European Test Workshop 2000 / p. 319-320

Formalization of finite state machines with data path for the verification of high-level synthesis

Borrione, Dominique; **Dušina, Julia**; Pierre, Laurence XI Brazilian Symposium on Integrated Circuit Design, September 30 - October

3, 1998, Rio de Janeiro, Brazil : proceedings 1998 / p. 99-102: ill <https://ieeexplore.ieee.org/document/715419>

Fourier' rida : metoodiline juhend raadiotehniliste ahelate ja signaalide laboratoorse töö sooritamiseks

1984 https://www.ester.ee/record=b1245512*est

Fractional order model identification of receptor-ligand complexes formation by equivalent electrical circuit modeling

Ates, Abdullah; Alagoz, Baris Baykant; **Tepljakov, Aleksei; Petlenkov, Eduard** 2019 International Artificial Intelligence and Data

Processing Symposium (IDAP) 2019 / 5 p. : ill <https://doi.org/10.1109/IDAP.2019.8875913>

Generation of tests for the localization of single gate design errors in combinational circuits using the stuck-at fault model

Ubar, Raimund-Johannes; Borrione, Dominique XI Brazilian Symposium on Integrated Circuit Design, September 30 - October 3,

1998, Rio de Janeiro, Brazil : proceedings 1998 / p. 51-54 <https://ieeexplore.ieee.org/document/715409>

Genetic optimization in circuit simulation

Haikarainen, Sakari; Jokinen, Hannu; Valtonen, Martti BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 381-384: ill

Greedy alternative for the static compaction of sequential circuit test sequences

Raik, Jaan The 7th Biennial Conference on Electronics and Microsystem Technology "Baltic Electronics Conference" : BEC 2000 : October 8 - 11, 2000, Tallinn, Estonia : conference proceedings 2000 / p. 133-136 : ill

Harmonic suppression in high power DC controlled reactors for AC power transmission lines

Tellinen, Juhan; Järvik, Jaan 8th International Conference on Harmonics and Quality of Power, October 14-16, 1998, Athens, Greece : proceedings. Vol. 2 1998 / p. 844-847: ill <https://ieeexplore.ieee.org/document/760152>

Heartwork analysis using bioimpedance method

Gordon, Rauno Telekommunikatsioon 2002 : IX rahvusvahelise telekommunikatsioonipäeva materjalid 2002 / lk. 8-14 : ill

Hierarchical defect-oriented fault simulation for digital circuits

Blyzniuk, M.; Cibakova, Tatiana; Gramatova, Elena; Kuzmich, W.; Lobur, M.; Pleskacz, Witold A.; **Raik, Jaan; Ubar, Raimund-Johannes** IEEE European Test Workshop 2000 / p. 151-156 <https://ieeexplore.ieee.org/document/873781>

Hierarchical physical defect reasoning in digital circuits

Kostin, Sergei; Ubar, Raimund-Johannes; Raik, Jaan; Brik, Marina Estonian journal of engineering 2011 / 3, p. 185-200

Hierarchical test generation for combinational circuits with real defects coverage

Cibakova, Tatiana; Fischerova, Maria; Gramatova, Elena; Kuzmich, W.; Pleskacz, Witold A.; **Raik, Jaan; Ubar, Raimund-Johannes** Microelectronics reliability 2002 / p. 1141-1149 : ill <https://www.sciencedirect.com/science/article/pii/S002627140200080X>

Hierarchical timing-critical paths analysis in sequential circuits

Jürimägi, Lembit; Ubar, Raimund-Johannes; Jenihhin, Maksim; Raik, Jaan; Devadze, Sergei; Kostin, Sergei 2018 IEEE 28th International Symposium on Power and Timing Modeling, Optimization and Simulation (PATMOS 2018) : 2 – 4 July 2018, Spain 2018 / 6 p. : ill <https://doi.org/10.1109/PATMOS.2018.8464176>

High voltage circuit breaker charging motor's condition monitoring - importance, possibilities, and challenges

Asad, Bilal; Sardar, Muhammad Usman; Vaimann, Toomas; Kallaste, Ants; Asefi, Sajjad; Kilter, Jako 2024 International Conference on Diagnostics in Electrical Engineering (Diagnostika) 2024 / 7 p <https://doi.org/10.1109/Diagnostika61830.2024.10693906>

High-level path activation technique to speed up sequential circuit test generation

Raik, Jaan; Ubar, Raimund-Johannes European Test Workshop 1999 : proceedings, May 25-28, 1999, Constance, Germany 1999 / p. 84-89 : ill <https://ieeexplore.ieee.org/document/804289>

Homological modelling of the electric circuits

Paal, Eugen; Umbleja, Märt Moduli Operads Dynamics II 2014

How to prove that a circuit is fault-free?

Ubar, Raimund-Johannes; Kostin, Sergei; Raik, Jaan Proceedings : 15th Euromicro Conference on Digital System Design DSD 2012 : 5-8 September 2012, Cesme, Izmir, Turkey 2012 / p. 427-430 : ill https://www.researchgate.net/publication/262271409_How_to_Prove_that_a_Circuit_is_Fault-Free

Identifying NBTI-critical paths in nanoscale logic

Ubar, Raimund-Johannes; Vargas, Fabian; Jenihhin, Maksim; Raik, Jaan; Kostin, Sergei; Bolzani Poehls, Leticia 16th Euromicro Conference series on Digital System Design : DSD 2013 : proceedings : 4-6 September 2013, Santander, Spain 2013 / p. 136-141 : ill

Identifying untestable faults in sequential circuits using test path constraints

Viilukas, Taavi; Karputkin, Anton; Raik, Jaan; Jenihhin, Maksim; Ubar, Raimund-Johannes; Fujiwara, Hideo Journal of electronic testing : theory and applications (JETTA) 2012 / p. 511-521 : ill <https://link.springer.com/article/10.1007/s10836-012-5312-5>

Impedance network impact on the controller design of the QZSI for PV applications

Liu, Wenjie; Yang, Yongheng; Kerekes, Tamas; **Liivik, Elizaveta**; Blaabjerg, Frede 2020 IEEE 21st Workshop on Control and Modeling for Power Electronics (COMPEL), Aalborg, Denmark, November 9-12, 2020 2020 / 6 p <https://doi.org/10.1109/COMPEL49091.2020.9265708>

Improved biasing error in current mode absolute value circuits

Männama, Vello; Wilson, B. Proceedings of the European Conference on Circuit Theory and Design ECCTD'99 : August 29-September 2, 1999, Stresa, Italy 1999 / p. 131-132

Improved fractional open circuit voltage MPPT methods for PV systems

Baimel, Dmitry; Tapuchi, Saad; Levron, Yoash; **Belikov, Juri** Electronics 2019 / art. 321 <https://doi.org/10.3390/electronics8030321>

Interaction in power source, load and electrical circuits. Part I

Järvik, Jaan Journal of international scientific publications : materials, methods & technologies 2010 / 2, p. 512-521 : ill

Interactive circuit synthesiser

Koort, Marko; Kukk, Vello BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 245-248: ill

ISTOC-2D: system of mixed device-circuit modeling for personal computer

Philatov, N.I.; Nakropin, B.O.; Yakovlev, D.G. Automation, simulation & measurement : ASM'91 : 3rd biennial conference, Tallinn, October 7-11, 1991. Section S / Tallinn Technical University 1992 / p. 95-100: ill

Kolmas vaatus - elektronide gümastika "vedrudel" : ehitame arvutisse sobiva retrohõngulise nivooindikaatori

Sinivee, Veljo Horisont 2008 / 3, lk. 52-53 : ill <http://parsek.yf.ttu.ee/~felc/horisont/elektro3.pdf>

Large time-step integration method for modeling of oscillating circuits

Kukk, Vello Proc. IEEE International Symposium on Circuits and Systems 2000 / [4] p. [CD-ROM]
<https://ieeexplore.ieee.org/document/857379>

LC circuit with parallel and series resonance alternation in switch-mode converters = Paralleel- ja järjestikresonantsi vaheldumisega LC ahel lülitussageduslikes muundurites

Šklovski, Jevgeni 2007 https://www.ester.ee/record=b2238343*est

Madalpingelised elektripaigaldised. Osa 5-557, Elektriseadmete valik ja paigaldamine. Abiahelad [Võrguteavik] = Low-voltage electrical installations. Part 5-557, Selection and erection of electrical equipment. Auxiliary circuits (IEC 60364-5-55:2011/A1:2012 (Clause 557))

2016 http://www.ester.ee/record=b4579708*est

Madalpingelised elektripaigaldised. Osa 5-557, Elektriseadmete valik ja paigaldamine. Abiahelad [Võrguteavik] = Low-voltage electrical installations. Part 5-557, Selection and erection of electrical equipment. Auxiliary circuits (IEC 60364-5-55:2011/A1:2012 (Clause 557))

2015

Method of the electrical network macromodeling

Dmitriyev-Zdorov, V.B.; Dudka, V.B.; Popov, V.P. Automation, simulation & measurement : ASM'91 : 3rd biennial conference, Tallinn, October 7-11, 1991. Section A. Section M / Tallinn Technical University 1992 / p. 54-59: ill

Mixed-level defect simulation in data-paths of digital systems

Ubar, Raimund-Johannes; Raik, Jaan; Ivask, Eero; Brik, Marina 23rd International Conference on Microelectronics : MIEL 2002, Niš, Yugoslavia, 12-15 May 2002 : proceedings. Volume 2 2002 / p. 617-620 : ill <https://ieeexplore.ieee.org/document/1003333>

Model reduction in VLSI circuit design

Rogoza, V.S. Automation, simulation & measurement : ASM'91 : 3rd biennial conference, Tallinn, October 7-11, 1991. Section S / Tallinn Technical University 1992 / p. 113-119: ill

Moduleeritud signaalide spektraalanalüüs : metoodiline juhend raadiotehniliste ahelate ja signaalide laboratoorse töö sooritamiseks

1988 https://www.ester.ee/record=b1231481*est

Multi-level test generation for digital systems at system, circuit and defect levels

Ubar, Raimund-Johannes Proceedings of the 7th International Scientific Conference "Theory and Technique of Information Transmission, Reception and Processing" : Tuapse, October 1-4, 2001 2001 / p. 286-288

Multiple-objective backtrace for solving test generation constraints

Mekler, A.; **Raik, Jaan** International Symposium on System-on-Chip : November 19-21, 2003, Tampere, Finland : proceedings 2003 / p. 123-126 : ill <https://ieeexplore.ieee.org/document/1267732>

Mõõteseadmete kasutamishand : metoodiline juhend raadiotehniliste ahelate ja signaalide laboratoorsete tööde sooritamiseks

1984 https://www.ester.ee/record=b1245516*est

New active clamp circuit for current-fed galvanically isolated DC/DC converters [Electronic resource]

Zakis, Janis; Vinnikov, Dmitri; Kolosov, Valery; Vasechko, Evgen CPE 2013 : 2013 International Conference on Compatibility and Power Electronics (CPE) : June 5-7, 2013, Ljubljana, Slovenia : conference proceedings 2013 / p. 353-358 : ill [CD-ROM]

Nonequilibrium transport phenomena in a tilted ratchet model subjected to a multiplicative trichotomous noise

Sauga, Ako; Mankin, Romi Book of abstracts : Fifth International Conference on Application of Mathematics in Technical and Natural Sciences, 24–29 June 2013, Albena, Bulgaria 2013 / p. 60

Note on homological modeling of the electric circuits

Paal, Eugen; Umbleja, Märt 3Quantum : Algebra Geometry Information (QQQ Conference 2012) : 10-13 July 2012, Tallinn, Estonia : dedicated to memory of Professor Jean-Louis Loday (1936-2012) 2014 / p. 1-7 <https://doi.org/10.1088/1742-6596/532/1/012022>
[Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Novel family of single-stage buck-boost inverters based on unfolding circuit

Husev, Oleksandr 18th International Symposium "Topical Problems in the Field of Electrical and Power Engineering". Doctoral School of Energy and Geotechnology III : Toila, Estonia, January 14-19, 2019 : [proceedings] 2019 / p. 39-40 : ill https://www.ester.ee/record=b5183874*est

Novel family of single-stage buck-boost inverters based on unfolding circuit

Husev, Oleksandr; Matiushkin, Oleksandr; Roncero-Clemente, Carlos; Blaabjerg, Frede; **Vinnikov, Dmitri** IEEE transactions on power electronics 2019 / p. 7662-7676 : ill <https://doi.org/10.1109/TPEL.2018.2879776> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Novel family of single-stage buck-boost inverters based on unfolding circuit : [conference paper]

Matiushkin, Oleksandr; Husev, Oleksandr 17th International Symposium "Topical Problems in the Field of Electrical and Power Engineering". Doctoral school of energy and geotechnology. III : Kuressaare, Estonia, January 15-20, 2018 2018 / p. 128-129 : ill http://ise.elnet.ee/record=b2950043~S2*est

On efficient logic-level simulation of digital circuits represented by the SSBDD model

Jutman, Artur; Raik, Jaan; Ubar, Raimund-Johannes 23rd International Conference on Microelectronics : MIEL 2002, Niš, Yugoslavia, 12-15 May 2002 : proceedings. Volume 2 2002 / p. 621-624 : ill <https://ieeexplore.ieee.org/document/1003334>

On robust stability of uncertain control systems with time delay : an approach based on the overlap of value sets

Ghorbani, Majid; Tepļakov, Aleksei; Petlenkov, Eduard IEEE Transactions on Automatic Control 2024 / p. 6349 - 6356 <https://doi.org/10.1109/TAC.2024.3381912> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

On the sensitivity analysis of linear active networks

Laksberg, Edgar Electronics letters : an international publication 1978 / p.221-223 https://www.ester.ee/record=b2180432*est

Optimal coupling coefficient calculation for inductances in interleaved bidirectional DC-DC converters

Tytelmaier, Kostiantyn; Husev, Oleksandr; Veligorskyi, Oleksandr; Khomenko, Maksym; Maladyka, D. Technical Electrodynamics 2018 / p. 41-46 <https://doi.org/10.15407/techned2018.04.041> [Journal metrics at Scopus](#) [Article at Scopus](#)

Optimization of recursive sorting algorithms for implementation in hardware

Mihhailov, Dmitri; Sklyarov, Valery; Skliarova, Iuliia; **Sudnitsõn, Aleksander** Proceedings of 22nd International Conference on Microelectronics (ICM 2010) : Cairo, Egypt, Dec. 19-22, 2010 2010 / p. 471-474 : ill https://www.researchgate.net/publication/224213497_Optimization_of_recursive_sorting_algorithms_for_implementation_in_hardware

Optimized modulation scheme for four-leg quasi Z-source inverter : reducing power loss and improving output quality

Abid, Abderahmane; **Bakeer, Abualkasim Ahmed Ali;** Albalawi, Hani; Zellouma, Laid; Bouzidi, Mansour; Lashab, Abderezak; Rabhi, Boualaga; **Chub, Andrii** IEEE Access 2023 / p. 94125-94137 <https://doi.org/10.1109/ACCESS.2023.3305263> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Parallel exact critical path tracing fault simulation with reduced memory requirements

Devadze, Sergei; Ubar, Raimund-Johannes; Raik, Jaan; Jutman, Artur 4th International Conference on Design and Technology of Integrated Systems in Nanoscale Era : DTIS'09 : Cairo, Egypt, April 6-9, 2009 2009 / p. 155-160 : ill <https://ieeexplore.ieee.org/document/4938046>

Parallel FPGA-based implementation of recursive sorting algorithms

Mihhailov, Dmitri; Sklyarov, Valery; Skliarova, Iuliia; **Sudnitsõn, Aleksander** 2010 International Conference on Reconfigurable Computing and FPGAs (ReConFig 2010) : Cancun, Mexico, December 13-15, 2010 2010 / p. 121-126 : ill https://www.researchgate.net/publication/221437230_Parallel_FPGA-Based_Implementation_of_Recursive_Sorting_Algorithms

Parasiitmahtuvuse mõju induktiivsele sidestusele

Eiskop, Ilmar Side. Raadio. Televisioon : infoseeria 10 1981 / lk. 13-17 : joon https://www.ester.ee/record=b1232303*est

Performance analysis of protection methods in residential DC microgrids

Jalakas, Tanel; Banavath, Satish Naik; **Chub, Andrii; Roasto, Indrek; Vinnikov, Dmitri** 2023 IEEE 17th International Conference on Compatibility, Power Electronics and Power Engineering (CPE-POWERENG) 2023 / 6 p <https://doi.org/10.1109/CPE-POWERENG58103.2023.10227388>

Proceedings of the 2011 IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 13-15, 2011, Gottbus, Germany

2011 <https://www.computer.org/csdl/proceedings/ddecS/2011/12OmNvTBbbs>

Properties of nonlinear power system models that are symmetrically configured in the narrow sense

Segev, Elior; Ofir, Ron; Levron, Yoash; **Belikov, Yuri** 2023 IEEE Power & Energy Society General Meeting (PESGM) : 16-20 July 2023 (2023) 2023 <https://doi.org/10.1109/PESGM52003.2023.10252373>

Raadioelektroonikaahelate müraparametrite arvutamine : elektronahelate mürad : mürade masinarvutus : õppevahend
Ratassepp, Jaak 1978 https://www.ester.ee/record=b1281539*est

Raadiotehnilised ahelad : õppeabimaterjal. 2, Harmooniliste signaalide genereerimine

Heinrichsen, Vladimir 1974 https://www.ester.ee/record=b1294101*est

Raadiotehnilised ahelad ja signaalid

Heinrichsen, Vladimir 1991 https://www.ester.ee/record=b1450227*est

Raadiotehnilised ahelad ja signaalid

Heinrichsen, Vladimir 1991 https://www.ester.ee/record=b1191459*est

Raadiotehnilised ahelad ja signaalid

Heinrichsen, Vladimir; Ratassepp, Jaak 1991 https://www.ester.ee/record=b1164574*est

Raadiotehniliste ahelate ja signaalide kursuse põhijooni

Heinrichsen, Vladimir; Martverk, Peep Õppemetoodika küsimusi ; 12 1975 / lk. 40-44 https://www.ester.ee/record=b1346720*est

RC-ostsillaator : metoodiline juhend raadiotehniliste ahelate ja signaalide laboratoorse töö sooritamiseks

1984 https://www.ester.ee/record=b1245517*est

RC-осциллятор : методическое пособие к лабораторной работе по курсу "Радиотехнические цепи и сигналы"

1988 https://www.ester.ee/record=b1232560*est

Reactive power injection capability of buck-boost inverter with unfolding circuit

Roncero-Clemente, Carlos; **Husev, Oleksandr; Matiushkin, Oleksandr; Vinnikov, Dmitri**; Blaabjerg, Frede IEEE transactions on power electronics 2022 / p. 11876-11886 <https://doi.org/10.1109/TPEL.2022.3179784> [Journal metrics at Scopus](#) [Article at Scopus](#)
[Journal metrics at WOS](#) [Article at WOS](#)

Removing design errors from digital circuits

Ubar, Raimund-Johannes Proc. of the 4th International Conference on New Information Technologies. Vol. 1 2000 / p. 118-125

Research and training environment for digital design and test

Ubar, Raimund-Johannes; Wuttke, Heinz-Dietrich Proceedings of the Eighth IASTED International Conference on Computers and Advanced Technology in Education : August 29-31, 2005, Oranjestad, Aruba 2005 / p. 232-237 : ill <https://ieeexplore.ieee.org/document/1408779>

RT-level test point insertion for sequential circuits

Raik, Jaan; Govind, Vineeth; Ubar, Raimund-Johannes IWoTA 2004 : IEEE 1st International Workshop on Testability Assessment : November 2, 2004, Rennes, France : proceedings 2004 / p. 34-40 : ill <https://ieeexplore.ieee.org/document/1428412>

Sequential circuit test generation using decision diagram models

Raik, Jaan; Ubar, Raimund-Johannes Design, Automation and Test in Europe : DATE : Conference and Exhibition 1999 : Munich, Germany, March 9-12, 1999 : proceedings 1999 / p. 736-740 : ill https://www.cs.york.ac.uk/rts/docs/SIGDA-Compendium-1994-2004/papers/1999/date99/pdffiles/11e_1.pdf

Sequential circuits BIST synthesis from signal specifications

Raik, Jaan; Jenihhin, Maksim; Adelbert, Rain Proceedings 23rd NORCHIP Conference : Oulu, Finland, 21-22 November 2005 2005 / p. 196-199 : ill <https://ieeexplore.ieee.org/document/1597023>

Sequential circuits BIST with status bit control

Raik, Jaan; Orasson, Elmet; Ubar, Raimund-Johannes Proceedings of the 11th International Conference : Mixed Design of Integrated Circuits and Systems : MIXDES 2004 : Szczecin, Poland, 24-26 June 2004 2004 / p. 507-510 : ill https://pld.ttu.ee/~raiub/files/aaaaa_pulk/MIXDES/jaan.pdf

A set of tools for estimating quality of built-in self-test in digital circuits

Jervan, Gert; Markus, Antti; Paomets, Priidu; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the International

Sisemise tagasisidega ostsillaator : metoodiline juhend raadiotehniliste ahelate ja signaalide laboratoorse töö sooritamiseks

1984 https://www.ester.ee/record=b1245521*est

SPADE как инструментальная система для разработки пакетов программ проектирования электронных схем
Vendelin, Jelena Анализ и синтез сложных систем и цепей с помощью ЭВМ 1985 / с. 69-77

SSBDDs and double topology for multiple fault reasoning

Ubar, Raimund-Johannes; Kostin, Sergei; Raik, Jaan Proceedings of IEEE East-West Design & Test Symposium

(EWDTS'2012) : Kharkov, Ukraine, September 14–17, 2012 2012 / p. 23-28 <https://www.semanticscholar.org/paper/SSBDDs-and-Double-Topology-for-Multiple-Fault-Ubar-Kostin/8ba04611a41768f0c277a4ba46ca666132fc2f65>

Structurally synthesized multiple input BDDs for speeding up logic-level simulation of digital circuits

Mironov, Dmitri; Ubar, Raimund-Johannes; Devadze, Sergei; Raik, Jaan; Jutman, Artur 13th Euromicro Conference on Digital System Design : Architectures, Methods and Tools : DSD 2010 : Lille, France, 1-3 September 2010 : proceedings 2010 / p. 658-663 : ill <https://ieeexplore.ieee.org/document/5615526>

Survey of harmonic emission of electrical vehicle chargers in the European market

Collin, A. J.; Xu, Xiao; Djokic, S. Z.; Möller, F.; Meyer, Jan; **Kütt, Lauri**; Lehtonen, Matti SPEEDAM 2016 - proceedings : International Symposium on Power Electronics, Electrical Drives, Automation and Motion : Capri (Italy), 22nd-24th June 2016 2016 / p. 1208-1213 : ill <https://doi.org/10.1109/SPEEDAM.2016.7526005>

Synthesis and implementation of hierarchical finite state machines with implicit modules

Sklyarov, Valery; Skliarova, Iuliia; **Mihhailov, Dmitri; Sudnitsõn, Aleksander** 2010 International Conference on Reconfigurable Computing and FPGAs (ReConFig 2010) : Cancun, Mexico, December 13-15, 2010 2010 / p. 436-441 : ill

https://www.researchgate.net/publication/221437255_Synthesis_and_Implementation_of_Hierarchical_Finite_State_Machines_with_Implicit_Modules

Test pattern generation at the behavioral level from VHDL circuit description containing several processes

Gramatova, Elena; Bezakova, Jana; Cibakova, Tatiana BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 145-148

Towards artificial intelligence based automatic adaptive response analyzer for high frequency analog BIST

Petlenkov, Eduard; Jutman, Artur; Nõmm, Sven; Ubar, Raimund-Johannes CIMSA 2008 : IEEE International Conference on Computational Intelligence for Measurement Systems and Applications : Istanbul, Turkey, July 14-16, 2008 2008 / p. 99-104 : ill <https://ieeexplore.ieee.org/document/4595841>

Схемы замещения индуктивно связанных цепей и их параметры

Voldek, Aleksander 1952 https://www.ester.ee/record=b1397542*est <https://digikogu.taltech.ee/et/Item/348d194d-2234-4202-830c-e6e6f8bc89b8>

2023 IEEE Nordic Circuits and Systems Conference (NORCAS) : 31 October-November 1, 2023 Aalborg, Denmark : proceedings in IEEE Xplore

2023 <https://researchr.org/publication/norcass-2023>

Uninterruptible power supply in distribution substations auxiliary circuits

Hõimoja, Hardi 4th International Symposium Topical Problems of Education in the Field of Electrical and Power Engineering. Doctoral School of Energy and Geotechnology : Kuressaare, Estonia, January 15-20, 2007 2007 / p. 78-82

Untestable fault identification in sequential circuits using model-checking

Raik, Jaan; Fujiwara, Hideo; Ubar, Raimund-Johannes; Krivenko, Anna 2002-2011 : 20th Anniversary compendium of papers from Asian Test Symposium 2011 / p. 257-262 : ill <https://ieeexplore.ieee.org/document/4711554>

Walshi signaalid : metoodiline juhend raadiotehniliste ahelate ja signaalide laboratoorse töö sooritamiseks

1984 https://www.ester.ee/record=b1245522*est

Very high-speed signal rectification using current conveyors

Wilson, Brett; Dodd, Alan J.; **Männama, Vello** BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 421-424: ill

Yield optimization of circuits based on simulated annealing and gravity centers methods

Zebbach, Ahmed; Siarry, Patrick BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 371-372: ill

Алгоритм частичного решения уравнений больших цепей

Ronk, Ants Труды по электротехнике и автоматике : сборник статей. 14 1976 / с. 11-20 : илл

https://www.ester.ee/record=b2190768*est <https://digikogu.taltech.ee/et/Item/aa35e320-87b1-405b-9cac-3b90c51867d1>

Анализ электронных цепей в системе SPADE

Vijar, Merike; Kurm, Matti; Sildaru, Kalev Анализ и синтез сложных систем и цепей с помощью ЭВМ 1985 / с. 93-101

Вопросы реализуемости RC-трехполюсников по трем независимым параметрам

Männama, Vello Теоретическая электротехника : республиканский межведомственный научно-технический сборник 1976 / с. 30-39 : ил https://www.ester.ee/record=b1895084*est

Вопросы синтеза RC-трехполюсников по трем параметрам : автореферат ... кандидата технических наук

Männama, Vello 1977 https://www.ester.ee/record=b1519261*est

Входной язык системы SPADE

Vendelin, Jelena Расчет и проектирование приборов, устройств и систем технической кибернетики 1980 / с. 81-90 : илл

https://www.ester.ee/record=b1264145*est <https://digikogu.taltech.ee/et/Item/81bf2178-a9f8-417d-86c7-2000cca6a01e>

Диагностика кратных неисправностей в комбинационных схемах

Viilur, Agu; Ubar, Raimund-Johannes; Heiter, U. Труды по электротехнике и автоматике : сборник статей. 11 1973 / с. 89-94 :

илл https://www.ester.ee/record=b2190624*est <https://digikogu.taltech.ee/et/Item/d6e57925-e104-44e1-a218-c5b3110d9996>

Инструкция по измерительным устройствам : методическое пособие к лабораторным работам по курсу "Радиотехнические цепи и сигналы"

1988 https://www.ester.ee/record=b1224921*est

К вопросу устойчивости активных RC-цепей, содержащих конвертор отрицательного сопротивления

Schiff, Gunnar Тезисы докладов республиканской научно-технической конференции, посвященной 80-летию со дня изобретения радио А. С. Поповым 1975 / с. 61 https://www.ester.ee/record=b1322122*est

Локализация дефектов в цифровых схемах

Evertson, Teet Машинное проектирование электронных устройств и систем 1986 / с. 110-119

Локализация неисправностей в линейных аналоговых цепях

Kangur, Oleg; Slowig, P. Теоретические основы методов и приборов измерения параметров слабых сигналов 1979 / с. 17-23

: илл https://www.ester.ee/record=b1267554*est <https://digikogu.taltech.ee/et/Item/323c0193-b2ea-454b-8578-33f3da07fa9f>

Математическая модель нелинейной безреактивной электрической цепи

Laksberg, Edgar Труды по радиотехнике : сборник статей. 2 1975 / с. 53-59 : илл https://www.ester.ee/record=b2190715*est

<https://digikogu.taltech.ee/et/Item/7c870ec1-0e49-4f2b-a354-8e2133afce48>

Машинное моделирование цепи питания, обеспечивающей инжекторный ток для И²Л схем

Rang, Toomas Расчет и проектирование приборов, устройств и систем технической кибернетики 1981 / с. 15-19 : илл

https://www.ester.ee/record=b1319172*est <https://digikogu.taltech.ee/et/Item/4c9b1559-97b9-4d9b-bac8-6c40840ba442>

Метод расчета на ЭМВ электромагнитных предходных процессов в ферромагнитных устройствах с произвольной структурой магнитной и электрической цепи

Sepping, Eino; Евдокунин Г.А.; Коршунов Е.В. Электротехника 1991 / 2, с. 56-59: ил

Метод синтеза сбалансированных цепей

Männama, Vello Расчет и проектирование приборов, устройств и систем технической кибернетики 1980 / с. 129-132 : илл

https://www.ester.ee/record=b1264145*est <https://digikogu.taltech.ee/et/Item/81bf2178-a9f8-417d-86c7-2000cca6a01e>

Моделирование частичного LU-разложения разреженной матрицы при решении задач анализа цепей

Ronk, Ants Труды по электротехнике и автоматике : сборник статей. 13 1975 / с. 55-66 : илл

https://www.ester.ee/record=b2190710*est <https://digikogu.taltech.ee/et/Item/ffbd63ed-06d6-4bbb-9468-118f743cc87f>

Нахождение передаточной функции по заданной функции группового запаздывания

Kukk, Vello; Rüstern, Ennu Труды по электротехнике и автоматике : сборник статей. 11 1973 / с. 19-26 : илл

https://www.ester.ee/record=b2190624*est <https://digikogu.taltech.ee/et/Item/d6e57925-e104-44e1-a218-c5b3110d9996>

О компактности RC - трехполюсников. I. Свойства компактности Y(Z)-параметров

Männama, Vello Труды по электротехнике и автоматике : сборник статей. 10 1972 / с. 23-30

https://www.ester.ee/record=b2190520*est <https://digikogu.taltech.ee/et/Item/49449c41-8f8a-4846-93fd-8f149889ec73>

О компактности RC - трехполюсников. II. Связи компактности между Y- и Z-параметрами

Männama, Vello Труды по электротехнике и автоматике : сборник статей. 10 1972 / с. 31-45

https://www.ester.ee/record=b2190520*est <https://digikogu.taltech.ee/et/Item/49449c41-8f8a-4846-93fd-8f149889ec73>

О компенсированности неидеальных конверторных двухпортов

Schiff, Gunnar Труды по электротехнике и автоматике : сборник статей. 10 1972 / с. 47-62 : илл

https://www.ester.ee/record=b2190520*est <https://digikogu.taltech.ee/et/Item/49449c41-8f8a-4846-93fd-8f149889ec73>

О понятии напряжения на участке электрической цепи в курсе общей физики

Gavrilov, Aleksei; Kukk, Peeter-Enn Методика преподавания физики в вузе 1986 / с. 21-26

https://www.ester.ee/record=b1206046*est

О реализации идеальных трансформаторов в виде +- R-цепей

Männama, Vello Тезисы докладов республиканской научно-технической конференции, посвященной 80-летию со дня изобретения радио А. С. Поповым 1975 / с. 62 https://www.ester.ee/record=b1322122*est

О точности электрической измерительной цепочки

Randmer, Uudus-Gerhard Труды по электротехнике и автоматике : сборник статей. [1] 1963 / с. 35-42 : илл

https://www.ester.ee/record=b2181953*est <https://digikogu.taltech.ee/et/Item/c0cba674-7147-4659-abad-39f8419dd45e>

Об общем определении цепи

Sillamaa, Hanno Тезисы Четвертой Всесоюзной межвузовской конференции по теории и методам расчета нелинейных электрических цепей и систем. (Октябрь 1971 г.) 1971 / с. 3-4

Основы синтеза схемных структур соединения многополюсных цепей : теория эквивалентных цепей

Sillamaa, Hanno 1983 https://www.ester.ee/record=b1291510*est

Основы теории цепей : методическое руководство к составлению курсовой работы для специальности 0701

Kangur, Oleg; Ratassepp, Jaak 1985 https://www.ester.ee/record=b1227175*est

Осциллятор с внутренней обратной связью : методическое пособие для лабораторных работ по курсу

"Радиотехнические цепи и сигналы"

1988 https://www.ester.ee/record=b1222305*est

Преобразование звезда-сеть для расчета активных адмитансных цепей

Kukk, Vello; Ronk, Ants Расчет и проектирование приборов, устройств и систем технической кибернетики 1980 / с. 121-128 :

илл https://www.ester.ee/record=b1264145*est <https://digikogu.taltech.ee/et/Item/81bf2178-a9f8-417d-86c7-2000cca6a01e>

Преобразование спектров в нелинейных цепях : методическое руководство для лабораторных работ по курсу

"Радиотехнические цепи и сигналы"

1988 https://www.ester.ee/record=b1222345*est

Применение электронной вычислительной машины для определения рациональных параметров последовательно-параллельной цепи термокомпенсации

Randmer, Uudus-Gerhard Труды по электротехнике и автоматике : сборник статей. 3 1965 / с. 123-143 : илл

https://www.ester.ee/record=b2181992*est <https://digikogu.taltech.ee/et/Item/ac7c42ac-6151-4dbe-bbe3-9dfe68adbce1>

Свойства и взаимосвязи адмитансных многополюсных элементов

Sillamaa, Hanno Труды по электротехнике и автоматике : сборник статей. 9 1971 / с. 3-20 : илл

https://www.ester.ee/record=b2190156*est <https://digikogu.taltech.ee/et/Item/945ec07e-d7b0-466e-bc24-6ad904a829e9/>

Спектральный анализ в нелинейных цепях : лабораторная работа 5 : методическое пособие по курсу

"Радиотехнические цепи и сигналы" для спец. 2103

1990 https://www.ester.ee/record=b1248911*est

Структура и свойства элементор-пространства представления многополюсных цепей

Sillamaa, Hanno Труды по электротехнике и автоматике : сборник статей. 10 1972 / с. 3-15 : илл

https://www.ester.ee/record=b2190520*est <https://digikogu.taltech.ee/et/Item/49449c41-8f8a-4846-93fd-8f149889ec73>

Теория линейных цепей : лабораторные работы

1986 https://www.ester.ee/record=b1353954*est

Теория цепей : лабораторные работы

1989 https://www.ester.ee/record=b1272281*est

Теория цепей : лабораторные работы

1986 https://www.ester.ee/record=b1262243*est

Теория цепей : лабораторные работы

1986 https://www.ester.ee/record=b1217035*est

Численное моделирование цепей, содержащих компоненты с распределенными параметрами

Kängsep, Eiko Численные методы и средства проектирования и испытания элементов РЭА : тезисы докладов. Том II 1978 / с. 79-80 https://www.ester.ee/record=b1273989*est

Эквивалентное преобразование для одного класса RC-цепей

Männama, Vello Расчет и проектирование приборов, устройств и систем технической кибернетики 1980 / с. 133-138 : илл https://www.ester.ee/record=b1281890*est <https://digikogu.taltech.ee/et/Item/8e0abfe2-9020-4ebd-85d1-fd67de0d1b30>

Эквивалентное преобразование для уменьшения суммарной емкости RC-цепи

Männama, Vello Тезисы докладов республиканской научно-технической конференции, посвященной 80-летию со дня изобретения радио А. С. Поповым 1975 / с. 62-63 https://www.ester.ee/record=b1322122*est

Электротехника и автоматика

Seppel, Simmu; Sillamaa, Hanno; Aarna, Olav; Raiend, Kullo; Kukk, Vello; Rüstern, Ennu; Vendelin, Jelena; Leppikson, Viktor; Vijar, Merike; Kurm, Matti; Min, Mart; Parve, Toomas; Väljamäe, Gunnar; Proode, Ülo; Tilk, Johan; Gordon, Boris; Einer, Lauri; Remmel, Ülo; Sepp, Ülo; Uutma, Toomas; Annus, Arno; Bachverk, Aleksander; Kiitam, Andres; Saks, Eva; Sildaru, Kalev 1985 https://www.ester.ee/record=b1302188*est

Электротехника и автоматика

Mägi, Harri; Rüstern, Ennu; Aarna, Olav; Kiitam, Andres; Land, Raul; Min, Mart; Ronk, Ants; Parve, Toomas; Rang, Toomas; Mahhitko, V.P.; Šendrik, M.G.; Tamm, Boris, inform. 1986 https://www.ester.ee/record=b1296468*est

Электротехника и автоматика

Pikkov, Otto; Uutma, Toomas; Seppel, Simmu; Umbleja, Uko; Väljamäe, Gunnar; Gordon, Boris; Proode, Ülo; Raiend, Kullo; Aarna, Olav; Metsanurm, Margus; Land, Raul; Lipping, Kalle; Ronk, Ants; Saar, R. 1988 https://www.ester.ee/record=b1288214*est