

**Deadlock-free generic routing algorithms for 3-dimensional Networks-on-Chip with reduced vertical link density topologies**

Ying, Haoyuan; Jaiswal, Ashok; **Hollstein, Thomas**; Hofmann, Klaus Journal of systems architecture 2013 / p. 528-542 : ill  
<https://doi.org/10.1016/j.sysarc.2013.03.005> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

**Dynamic quadrant partitioning adaptive routing algorithm for irregular reduced vertical link density topology 3-dimensional network-on-chips**

Ying, Haoyuan; Hofmann, Klaus; **Hollstein, Thomas** Proceedings of the 2014 International Conference on High Performance Computing & Simulation (HPCS 2014) : July 21-25, 2014, Bologna, Italy 2014 / p. 516-522 : ill

**A simulation framework for 3-dimension networks-on-chip with different vertical channel density configurations**

Ying, Haoyuan; Jaiswal, Ashok; Abd El Ghany, Mohamed A; **Hollstein, Thomas**; Hofmann, Klaus Proceedings of the 2012 IEEE 15th International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS) : April 18-20, 2012 Tallinn, Estonia 2012 / p. 83-88 : ill