

### **Accurate NBTI-induced gate delay modeling based on intensive SPICE simulations**

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### **BASTION : board and SoC test instrumentation for ageing and no failure found**

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### **Gate-level modelling of NBTI-induced delays under process variations**

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### **Health index prediction of overhead transmission lines : a machine learning approach**

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### **Hierarchical identification of NBTI-critical gates in nanoscale logic**

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### **Human centenarian-associated SIRT6 mutants modulate hepatocyte metabolism and collagen deposition in multilineage hepatic 3D spheroids**

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### **Identification and rejuvenation of NBTI-critical logic paths in nanoscale circuits**

**Jenihhin, Maksim; Squillero, Giovanni; Tihomirov, Valentin; Kostin, Sergei; Raik, Jaan; Ubar, Raimund-Johannes** Journal of electronic testing : theory and applications (JETTA) 2016 / p. 273-289 : ill <https://doi.org/10.1007/s10836-016-5589-x> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

### **Identifying NBTI-critical paths in nanoscale logic**

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### **On BTI aging rejuvenation in memory address decoders**

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