

Actual research topics in mechatronics system design

Glesner, M.; Herpel, H.-J.; Kirschbaum, A.; Windirsch, P. BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 1 1994 / p. 85-91: ill

Application specific processor synthesis from assembler code using VHDL

Tammemäe, Kalle; Udre, Jüri; Wehn, N.; Gasteier, M.; Glesner, M. APCHDLISA-93 Dec. 6-9, 1993 1993

DICE - an interactive approach to hardware/software co-design of heterogeneous real-time systems

Hollstein, Thomas; Becker, J.; Kirschbaum, A.; Renner, F.-M.; Glesner, M. BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 97-100

FPGA netlist partitioning onto heterogeneous FPGA boards

Zahm, U.; Glesner, M. BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 397-404: ill https://www.ester.ee/record=b2150914*est

High-level synthesis and test in the MOSCITO-based virtual laboratory

Schneider, Andre; Diener, Karl-Heinz; Jervan, Gert; Peng, Z.; Raik, Jaan; Ubar, Raimund-Johannes; Hollstein, Thomas; Glesner, M. BEC 2002 : proceedings of the 8th Biennial Baltic Electronics Conference : October 6-9, 2002, Tallinn, Estonia 2002 / p. 287-290 : ill

A new concept for a self-learning kit for FPGA design

Hollstein, Thomas; Zahm, U.; Wehn, N.; Herpel, H.-J.; Glesner, M. BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 405-410: ill https://www.ester.ee/record=b2150914*est

New curricula and a competence centre through TEMPUS at the Technical University of Tallinn

Glesner, M.; Hollstein, Thomas; Courtois, B.; Amblar, P.; Ubar, Raimund-Johannes; Vainomaa, Kaido Workshop on Design Methodologies for Microelectronics, Smolenice castle, Slovakia, September 11-13, 1995 : proceedings 1995 / p. 347-353