

Automatic synthesis of asynchronous circuits from synchronous RTL descriptions

Öberg, Johnny; Plosila, Juha; **Ellervee, Peeter** Proceedings 23rd NORCHIP Conference : Oulu, Finland, 21-22 November 2005 2005 / p. 200-205 : ill <https://ieeexplore.ieee.org/document/1597024/keywords#keywords>

A comparison of six languages for system level description of telecom applications

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Controller synthesis in control and memory centric high level synthesis system

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A HW/SW partitioning technique based on hierarchical canitate selection scheme

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NESTIMATOR - a neural network based estimator

Ellervee, Peeter; Jantsch, Axel; Öberg, Johnny; Hemani, Ahmed 7th International Workshop on High-Level Synthesis, Niagara-on-the Lake, Ontario, Canada, May 18-20, 1994 1994

Neural network based estimator to explore the design space at system level

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