

A HW/SW partitioning technique based on hierarchical canitate selection scheme

Jantsch, Axel; **Ellervee, Peeter**; Öberg, Johnny; Hemani, Ahmed 7th International Workshop on High-level Synthesis, Niagara-on-the Lake, Ontario, Canada, May 18-20, 1994 1994

Automatic synthesis of asynchronous circuits from synchronous RTL descriptions

Öberg, Johnny; Plosila, Juha; **Ellervee, Peeter** Proceedings 23rd NORCHIP Conference : Oulu, Finland, 21-22 November 2005 2005 / p. 200-205 : ill

A comparison of six languages for system level description of telecom applications

Jantsch, Axel; Kumar, Shashi; Sander, Ingo; Svantesson, Bengt; **Öberg, Johnny**; Hemani, Ahmed; **Ellervee, Peeter**; O'Nils, Mattias Electronic chips & systems design languages 2001 / p. 181-192 : ill

Controller synthesis in control and memory centric high level synthesis system

Ellervee, Peeter; Hemani, Ahmed; Kumar, Anshul; Svantesson, Bengt; Öberg, Johnny; Tenhunen, Hannu BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 393-396: ill

NESTIMATOR - a neural network based estimator

Ellervee, Peeter; Jantsch, Axel; Öberg, Johnny; Hemani, Ahmed 7th International Workshop on High-Level Synthesis, Niagara-on-the Lake, Ontario, Canada, May 18-20, 1994 1994

Neural network based estimator to explore the design space at system level

Ellervee, Peeter; Öberg, Johnny; Jantsch, Axel; Hemani, Ahmed BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 391-396: ill