

A model for the superconducting microstrip line

Costa, Luis R.J.; Linna, Riku; Seppä, Heikki; **Valtonen, Martti** BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 439-442: ill

A new modified berger code for concurrent error detection

Maamar, Ali H.; Russel, Gordon BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 187-190: ill

A testing strategy for interacting finite state machines

Yevtushenko, Nina; Petrenko, Alexandre; Trenkaev, Vadim BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 137-140: ill

AG-model for design of testable controllers

Kasirova, Lilia BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 303-306: ill

An efficient systolic array algorithm for memory-based VLSI array implementation of DST

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An implementation of simulated annealing for analog circuit optimization

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An improved programmed PWM method using walsh functions

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An spaceborne analog IF narrowband 16*16 switch matrix for personal communications satellites

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Analog interface for electrical bio-impedance measurement unit

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Automatic system for testing of dynamic quality of PC plug-in A/D boards

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A BIST scheme for testing mixed analogue and digital circuits

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Block diagram modeling and simulation in a bond graph framework

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Bringing the Sallen-Key biquad back to work

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Determination of the nonfaulty limits of circuit characteristics in the fault identification of discrete-type circuits

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Dynamic voltage controlled HF and microwave band oscillator frequency stabilizing method

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Implementation-independent macro-library for telecommunication in VHDL

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Pulse generation in a superconducting microstrip line

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