## A 48 by 48 CMOS cellular neural network chip for image processing

Paasio, Ari; Kananen, Asko; Halonen, Kari; Porra, Veikko BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 87-88