

Applying FPGA partial reconfiguration for digital system simulation

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Architectural exploration tasks for on-chip embedded systems

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Clock manipulation for heterogeneous emulation environment

Ellervee, Peeter; Arhipov, Anton; Tammemäe, Kalle Proceedings [of] 24th IEEE Norchip Conference : Linköping, Sweden, 20-21 November 2006 2006 / p. 213-216 : ill <https://ieeexplore.ieee.org/abstract/document/4126984>

FPGA based emulation environment

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HDL-s for students with different background

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Teaching HDL for IT-students

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Using emulation for system model analysis

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Using soft-core processors and FPGA development boards for hardware emulation

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