

Engineering of an effective automatic dynamic assertion mining platform

Ghasempouri, Tara; Malburg, Jan; Danese, Alessandro; Pravadelli, Graziano; Fey, Goerschwin; **Raik, Jaan** VLSI-SoC 2019 : 27th IFIP/IEEE International Conference on Very Large Scale Integration : [proceedings] 2019 / p. 111-116 : ill <https://doi.org/10.1109/VLSI-SoC.2019.8920331>

Mutation analysis for systemC designs at TLM

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On the combined use of HLDDs and EFSMs for functional ATPG

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RTL assertion mining with automated RTL-to-TLM abstraction

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