## Bottlenecks in hardware design and design automation [Electronic resource]: [slides]

**Ellervee, Peeter** Design and Test Technology for Dependable Hardware/Software Systems: DEDIS/DAAD Summer Academy: BTU Cottbus, Sept. 1st-12th, 2008 2008 / [24] p.: ill. [CD-ROM]

## Faults and fault models for integrated circuits and systems [Electronic resource]: [slides]

**Ubar, Raimund-Johannes** Design and Test Technology for Dependable Hardware/Software Systems: DEDIS/DAAD Summer Academy: BTU Cottbus, Sept. 1st-12th, 2008 2008 / [64] p.: ill. [CD-ROM]

## SoC and NoC test technology [Electronic resource] : [PowerPoint presentation]

Raik, Jaan Design and Test Technology for Dependable Hardware/Software Systems: DEDIS/DAAD Summer Academy: BTU Cottbus, Sept. 1st-12th, 2008 2008 / [41] p.:ill. [CD-ROM]