

**FSM decomposition with application to FPGA synthesis**

**Sudnitsõn, Aleksander; Mihailov, Dmitri; Kruus, Margus;** Tarletski, Konstantin International Conference on Computer Systems and Technologies - CompSysTech'09 : Ruse, Bulgaria 2009 / p. IV.4.1-IV.4.6 <https://dl.acm.org/doi/10.1145/1731740.1731820>

**Web-based tool for FSM encoding targeting low-power FPGA implementation**

**Mihailov, Dmitri; Sudnitsõn, Aleksander;** Tarletski, Konstantin 2010 27th International Conference on Microelectronics : MIEL 2010 : Niš, Serbia, 16-19 May 2010 : proceedings 2010 / p. 349-352 <https://ieeexplore.ieee.org/document/5490468>