

### **Address-based data processing over N-ary trees**

Sklyarov, Valery; Skliarova, Ioulia; Kruus, Margus; Mihailov, Dmitri; Sudnitsõn, Aleksander EuroCon 2013 : 01-04 July 2013, Zagreb, Croatia 2013 / p. 1790-1797 : ill

### **Architectural solutions for high-speed data processing demands of CERN LHC detectors with FPGA and high-level synthesis**

Devadze, Sergei; Nielsen, Christine Elizabeth; Mihailov, Dmitri; Ellervee, Peeter 2024 IEEE Nordic Circuits and Systems Conference (NorCAS) 2024 [https://doi.org/10.1109/NorCAS64408.2024.10752490 Article at Scopus](https://doi.org/10.1109/NorCAS64408.2024.10752490)

### **Development of process optimization model for autonomous mobile robot used in production logistics**

Majak, Jüri; Karjust, Kristo; Mahmood, Kashif; Hermaste, Aigar; Raamets, Tõnis AIP conference proceedings 2024 / art. 020008 [https://doi.org/10.1063/5.0189299 Conference proceedings at Scopus Article at Scopus](https://doi.org/10.1063/5.0189299)

### **Multilevel models for data processing**

Sklyarov, Valery; Skliarova, Ioulia; Mihailov, Dmitri; Sudnitsõn, Aleksander 2011 IEEE GCC Conference and Exhibition (GCC) : February 19-22, 2011, Dubai, United Arab Emirates 2011 / p. 136-139

### **Optimization of address-based data sorting unit with external memory support**

Mihailov, Dmitri; Rjabov, Artjom; Sklyarov, Valery; Skliarova, Ioulia; Sudnitsõn, Aleksander CompSysTech'13 : proceedings of the 14th International Conference on Computer Systems and Technologies 2013 / p. 83-90 : ill [https://doi.org/10.1145/2516775.2516807 Conference Proceedings at Scopus Article at Scopus](https://doi.org/10.1145/2516775.2516807)

### **Processing N-ary trees in reconfigurable hardware**

Sklyarov, Valery; Skliarova, Ioulia; Sudnitsõn, Aleksander 2013 25th International Conference on Microelectronics (ICM) : 15–18 December 2013, Beirut-Lebanon 2013 / p. 13-16 : ill