Implementation in FPGA of address-based data sorting

Sklyarov, Valery; Skliarova, Iouliia; **Mihhailov, Dmitri**; **Sudnitsõn, Aleksander** 21st International Conference on Field Programmable Logic and Applications: FPL 2011: Chania, Crete, Greece, 5-7 September 2011 2011 / p. 405-410: ill https://www.researchgate.net/publication/220760392 Implementation in FPGA of Address-Based Data Sorting