

Actual research topics in mechatronics system design

Glesner, M.; Herpel, H.-J.; Kirschbaum, A.; Windirsch, P. BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 1 1994 / p. 85-91: ill

Comprehensive abstraction of VHDL RTL cores to ESL SystemC = Register-siirde taseme VHDL kirjelduste kompleksne abstraherimine süsteemitaseme SystemC mudeliteks

Abrar, Syed Saif 2016 http://www.ester.ee/record=b4564850*est

CREDES Summer School : Dependable Systems Design : handouts

2011 http://www.ester.ee/record=b2891192*est

Dependability improvements of NoC-based systems = Töökindluse parandamine kiipvõrkudel põhinevates süsteemides

Niazmand, Behrad 2018 <https://digi.lib.ttu.ee/ii/?9879> https://www.ester.ee/record=b4907650*est

Design and test technology for dependable systems-on-chip

2011 https://www.ester.ee/record=b4467408*est

Design space exploration and optimisation for NoC-based timing sensitive systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert BEC 2010 : 2010 12th Biennial Baltic Electronics Conference : proceedings of the 12th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 4-6, 2010, Tallinn, Estonia 2010 / p. 177-180 : ill

Design space exploration and optimisation for NoC-based timing sensitive systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK neljanda aastakonverentsi artiklite kogumik : 26.-27. novembril 2010, Essu mõis 2010 / lk. 117-120 : ill

FP7 DIAMOND : design error diagnosis and correction success stories

Raik, Jaan; Jenihhin, Maksim; Könighofer, Robert European Test Symposium (ETS), 2013, Avignon, France 2013 / p. 1-6

From checklists to design process support systems: Initial framing

Pikas, Ergo; Koskela, Lauri; Oehmen, Josef; Dave, Bhargav Proceedings of the 27th Annual Conference of the International Group for Lean Construction (IGLC), IGLC 2019, 1 July 2019 - 7 July, 2019, Dublin, Ireland 2019 / p. 83-96 <https://doi.org/10.24928/2019/0173>

FSMD RTL design manipulation for clock interface abstraction

Abrar, Syed Saif; Jenihhin, Maksim; Raik, Jaan 2015 International Conference on Advances in Computing, Communications and Informatics (ICACCI) : 10-13 August 2015, Kerala, India 2015 / p. 463-468 : ill <http://dx.doi.org/10.1109/ICACCI.2015.7275652>

Hardware/software co-design for programmable systems-on-chip

Sklyarov, Valery; Skliarova, Iuliia; Silva, João; Rjabov, Artjom; Sudnitsõn, Aleksander; Cardoso, Cláudia 2014 http://www.ester.ee/record=b3087107*est

Internet-basierter Systementwurf mit MOSCITO

Schneider, Andre; Schneider, Peter; Gramatova, Elena; Ivask, Eero Entwurf Integrierter Schaltungen : 10. E.I.S.-Workshop : Präsentationen der ITG-Fachtagung : vom 3. bis 5. April 2001 in Dresden 2001 / S. 295-296 : ill

Kasutusjuhend ja eesmärgimudelid

Taveter, Kuldar A & A 2010 / 1, lk. 6-8 : ill https://artiklid.elnet.ee/record=b1965430*est

Logic and system design of digital systems

Baranov, Samary; Keevallik, Andres 2008 https://www.ester.ee/record=b2358322*est

Mechatronics system design in conceptual stage

Sell, Raivo Proceedings of the 4th International Conference Industrial Engineering - New Challenges to SME : 29-30 April 2004, Tallinn, Estonia 2004 / p. 82-85 : ill

Mikroprotsessorsüsteemid : kursuseprojekti juhendmaterjal

Toomsalu, Arvo 1998 https://www.ester.ee/record=b1061287*est

Model based mechatronic systems modeling methodology in conceptual design stage

Sell, Raivo 2007 https://www.ester.ee/record=b2298261*est

Situation awareness of computing agents

Preden, Jürjo-Sören Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK kolmanda aastakonverentsi artiklite kogumik : 25.-26. aprill 2008, Voore külalistemaja 2008 / p. 11-13

SysML in systems engineering course

Kruus, Helena; Robal, Tarmo; Jervan, Gert 10th European Workshop on Microelectronics Education : EWME 2014 : May 14-16, 2014, Tallinn, Estonia 2014 / p. 177-181 : ill

System-level communication synthesis and dependability improvements for Network-on-Chip based systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert Estonian journal of engineering 2010 / 1, p. 23-38 : ill
https://artiklid.elnet.ee/record=b1964968*est

System-level design of timing-sensitive network-on-chip based dependable systems = Kiipvõrkudel põhinevate ajakriitiliste ja töökindlate süsteemide kõrgtaseme disain

Tagel, Mihkel 2012 https://www.eester.ee/record=b2778263*est

The Crisis Management System - a case study on aspect-oriented modelling with UPPAAL

Iqbal, Junaid; Truscan, Dragos; Porres, Ivan; Tsiopoulos, Leonidas; Vain, Jüri 2016 http://tucs.fi/publications/view/?pub_id=tlqTsTrVaPo16a

Стохастическая модель задачи проектирования оптимальных систем

Jõgi, Emma [Проблемы оптимизации : тезисы докладов всесоюзной конференции "Проблемы оптимизации и надежности в строительной механике"] 1979 / с. 17-18