

Bringing research issues into lab scenarios on the example of SoC testing [Electronic resource]

Ubar, Raimund-Johannes; Jutman, Artur; Devadze, Sergei; Wuttke, Heinz-Dietrich International Conference on Engineering Education - ICEE 2007 : September 3-7, 2007, Coimbra, Portugal 2007 / [7] p. : ill. [CD-ROM]
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Designing reliable cyber-physical systems

Aleksandrowicz, Gadi; Arbel, Eli; Bloem, Roderick; **Devadze, Sergei; Jenihhin, Maksim; Jutman, Artur; Raik, Jaan; Shibin, Konstantin** Languages, design methods, and tools for electronic system design : selected contributions from FDL 2016 2018 / p. 15-38 : ill https://doi.org/10.1007/978-3-319-62920-9_2 [Conference Proceedings at Scopus](#) [Article at Scopus](#)

Fast data sort based on searching networks with ring pipeline

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Fast matrix covering in all programmable systems-on-chip

Sklyarov, Valery; Skliarova, Ioulia; **Rjabov, Artjom; Sudnitsõn, Aleksander** Elektronika ir elektrotehnika = Electronics and electrical engineering 2014 / p. 150-153 : ill <https://doi.org/10.5755/j01.eee.20.5.7116> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

G-GPU : a fully-automated generator of GPU-like ASIC accelerators

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Interactions of Zynq-7000 devices with general purpose computers through PCI-express : a case study [Electronic resource]

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LiD-CAT: A lightweight detector for cache ATtacks

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Split-chip design to prevent IP reverse engineering

Pagliarini, Samuel Nascimento; Sweeney, Joseph; Mai, Ken; Blanton, Shawn; Mitra, Subhasish; Pileggi, Larry IEEE Design and Test 2020 / p. 109-118 <https://doi.org/10.1109/MDAT.2020.3033255> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Zynq-based system for extracting sorted subsets from large data sets

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