

Combinational fault simulation in sequential circuits

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Exact parallel critical path fault tracing to speed-up fault simulation in sequential circuits

Kõusaar, Jaak; Ubar, Raimund-Johannes; Kostin, Sergei; Devadze, Sergei; Raik, Jaan International journal of microelectronics and computer science 2018 / p. 9–18 <https://ijmcs.dmc.s.pl/web/guest/vol.-9-no.-1>
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Modeling sequential circuits with shared structurally synthesized BDDs

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Parallel critical path tracing fault simulation in sequential circuits

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