

Comprehensive performance and robustness analysis of 2D turn models for network-on-chips

Azad, Siavoosh Payandeh; Niazmand, Behrad; Janson, Karl; Kogge, Thilo; Raik, Jaan; Jervan, Gert; Hollstein, Thomas

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Dynamic quadrant partitioning adaptive routing algorithm for irregular reduced vertical link density topology 3-dimensional network-on-chips

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Logic-based implementation of fault-tolerant routing in 3D Network-on-Chips

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Review, analysis, and implementation of path selection strategies for 2D NoCs

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