

### **Checking sequence design for FSM**

**Keevallik, Andres; Kruus, Margus; Lensen, Harri** Proceedings of the 5th International Conference on Mixed Design of Integrated Circuits and Systems, Lodz, Poland, June 18-20, 1998 1998 / p. 325-330

### **Decompositional design of testable Decompositional design of testable FSM networks**

**Keevallik, Andres; Kruus, Margus; Lensen, Harri** Proceedings of the 4th International Workshop Mixed Design of Integrated Circuits and Systems : MIXDES'97 : Poznan, Poland, 12-14 June 1997 1997 / p. 431-436

### **Diagnostic modelling of digital systems with binary and high-level decision diagrams**

**Ubar, Raimund-Johannes; Raik, Jaan; Kruus, Helena; Lensen, Harri; Evertson, Teet** Progress in industrial mathematics at ECMI 2006 2008 / p. 902-907 : ill [https://link.springer.com/chapter/10.1007/978-3-540-71992-2\\_158](https://link.springer.com/chapter/10.1007/978-3-540-71992-2_158)

### **Diagnostic modelling of digital systems with multi-level decision diagrams**

**Ubar, Raimund-Johannes; Raik, Jaan; Evertson, Teet; Kruus, Margus; Lensen, Harri** Proceedings of the 17th IASTED International Conference on Modelling and Simulation : May 24-26, 2006, Montreal, Quebec, Canada 2006 / p. 207-212 : ill

### **Digital design at microarch[itectural] level based on quality relationship measures**

**Kruus, Margus; Lensen, Harri; Sudnitsõn, Aleksander** Успехи современного естествознания 2004 / 5, приложение 1, Материалы XXXI международной конференции и дискуссионного научного клуба : информационные технологии в науке, образовании, телекоммуникации и бизнесе : IT+SE'2004 : Украина, Крым, Ялта-Гурзуф, 18-27 мая 2004 года, с. 29-30

### **Diskreetne matemaatika**

**Lensen, Harri** 2002 [http://www.ester.ee/record=b1715399\\*est](http://www.ester.ee/record=b1715399*est)

### **Diskreetne matemaatika**

**Lensen, Harri; Kruus, Margus** 2002 [http://www.ester.ee/record=b1850141\\*est](http://www.ester.ee/record=b1850141*est)

### **Diskreetne matemaatika**

**Lensen, Harri; Kruus, Margus** 2006 [http://www.ester.ee/record=b2223858\\*est](http://www.ester.ee/record=b2223858*est)

### **Diskreetne matemaatika**

**Lensen, Harri; Kruus, Margus** 2012 [https://www.ester.ee/record=b2855919\\*est](https://www.ester.ee/record=b2855919*est)

### **Fault diagnosis in integrated circuits with BIST**

**Ubar, Raimund-Johannes; Kostin, Sergei; Raik, Jaan; Evertson, Teet; Lensen, Harri** 10th Euromicro Conference on Digital System Design Architectures, Methods and Tools, DSD 2007 : 29-31 August 2007, Lübeck, Germany : proceedings 2007 / p. 604-610 : ill <http://dx.doi.org/10.1109/DSD.2007.4341530>

### **FSM decomposition using shift registers**

**Kruus, Margus; Lensen, Harri** The 7th Biennial Conference on Electronics and Microsystem Technology "Baltic Electronics Conference" : BEC 2000 : October 8 - 11, 2000, Tallinn, Estonia : conference proceedings 2000 / p. 141-144 : ill

### **Hierarchical fault diagnosis in embedded digital systems with multi-level decision diagrams [Electronic resource]**

**Ubar, Raimund-Johannes; Evertson, Teet; Lensen, Harri; Aarna, Margit** 5th International Conference on Industrial Automation = Cinquieme Conference Internationale sur l'Automatisation Industrielle : June 11-13, 2007, Montreal, Canada 2007 / [6] p. [CD-ROM]

### **Microprogram automation : design for testability**

**Keevallik, Andres; Kruus, Margus; Lensen, Harri** BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 171-174

### **Synthesis of sequential circuits with dynamic power management**

**Lensen, Harri; Kruus, Margus; Sudnitsõn, Aleksander** Proc. of 42nd International Scientific Conference of Riga Technical University : RTUCET'01 2001 / p. 81-86

### **Synthesis of sequential circuits with dynamic power management**

**Lensen, Harri; Kruus, Margus; Sudnitsõn, Aleksander** Scientific proceedings of Riga Technical University. 7.seriija, Telecommunications and electronics 2001 / p. 81-86

### **Web-based tools for decomposition-oriented digital design**

**Kruus, Margus; Lensen, Harri; Sudnitsõn, Aleksander** MEET/MARIND'2002 : proceedings of First International Congress on Mechanical and Electrical Engineering and Technology and Fourth International Conference on Marine Industry, 07-11 October 2002, Varna Bulgaria. Volume 1 2002 / p. 261-266 : ill