

### **Algorithm for restructuring of structurally synthesized BDDs**

**Jürimägi, Lembit; Ubar, Raimund-Johannes** 2019 IEEE 31st International Conference on Microelectronics : Niš, Serbia September 16th-18th, 2019 : proceedings 2019 / p. 239-242 : ill <https://doi.org/10.1109/MIEL.2019.8889578>

### **Application specific true critical paths identification in sequential circuits**

**Jürimägi, Lembit; Ubar, Raimund-Johannes; Jenihhin, Maksim; Raik, Jaan; Devadze, Sergei; Oyeniran, Adeboye Stephen** 2019 IEEE 25th International Symposium on On-Line Testing and Robust System Design (IOLTS 2019) : 1-3 July 2019, Greece 2019 / p. 299-304 : ill <https://doi.org/10.1109/IOLTS.2019.8854442>

### **Calculation of probabilistic testability measures for digital circuits with Structurally Synthesized BDDs**

**Jürimägi, Lembit; Ubar, Raimund-Johannes; Jenihhin, Maksim; Raik, Jaan** Microprocessors and microsystems 2020 / art. 103117, 12 p <https://doi.org/10.1016/j.micpro.2020.103117> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

### **Conditional fault collapsing in digital circuits with shared structurally synthesized BDDs [Online resource]**

**Jürimägi, Lembit; Ubar, Raimund-Johannes** BEC 2018 : 2018 16th Biennial Baltic Electronics Conference (BEC) : proceedings of the 16th Biennial Baltic Electronics Conference, October 8-10, 2018 2018 / 4 p. : ill <https://doi.org/10.1109/BEC.2018.8600967>

### **Double phase fault collapsing with linear complexity in digital circuits**

**Ubar, Raimund-Johannes; Jürimägi, Lembit; Orasson, Elmet; Josifovska, Galina; Oyeniran, Adeboye Stephen** DSD 2015 : 18th Euromicro Conference on Digital Systems Design : 26-28 August 2015, Funchal, Madeira, Portugal 2015 / p. 700-705 : ill

### **Equivalent transformations of structurally synthesized BDDs and applications**

**Jürimägi, Lembit; Ubar, Raimund-Johannes; Viies, Vladimir** 2019 8th Mediterranean Conference on Embedded Computing (MECO) 2019 / 6 p. : ill <https://doi.org/10.1109/MECO.2019.8760283>

### **Fast identification of true critical paths in sequential circuits**

**Ubar, Raimund-Johannes; Kostin, Sergei; Jenihhin, Maksim; Raik, Jaan; Jürimägi, Lembit** Microelectronics reliability 2018 / p. 252-261 : ill <https://doi.org/10.1016/j.microrel.2017.11.027> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

### **Fault collapsing in digital circuits using fast fault dominance and equivalence analysis with SSBDDs**

**Ubar, Raimund-Johannes; Jürimägi, Lembit; Orasson, Elmet; Raik, Jaan** VLSI-SoC : Design for Reliability, Security, and Low Power : 23rd IFIP WG 10.5/IEEE International Conference on Very Large Scale Integration, VLSI-SoC 2015 Daejeon, Korea, October 5-7, 2015 : revised selected papers 2016 / p. 23-45 : ill [https://doi.org/10.1007/978-3-319-46097-0\\_2](https://doi.org/10.1007/978-3-319-46097-0_2) [Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

### **Hierarchical timing-critical paths analysis in sequential circuits**

**Jürimägi, Lembit; Ubar, Raimund-Johannes; Jenihhin, Maksim; Raik, Jaan; Devadze, Sergei; Kostin, Sergei** 2018 IEEE 28th International Symposium on Power and Timing Modeling, Optimization and Simulation (PATMOS 2018) : 2 – 4 July 2018, Spain 2018 / 6 p. : ill <https://doi.org/10.1109/PATMOS.2018.8464176>

### **Introducing rapid web application development with Oracle APEX to students of higher education**

**Robal, Tarmo; Reinsalu, Uljana; Jürimägi, Lembit; Heinsar, Risto** New trends in computer sciences 2024 / p. 69-80 <https://doi.org/10.3846/ntcs.2024.21227>

### **Modeling and simulation of circuits with shared structurally synthesized BDDs**

**Ubar, Raimund-Johannes; Jürimägi, Lembit; Raik, Jaan; Viies, Vladimir** Microprocessors and microsystems 2017 / p. 56-61 : ill <https://doi.org/10.1016/j.micpro.2016.09.006> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

### **Scalable algorithm for structural fault collapsing in digital circuits**

**Ubar, Raimund-Johannes; Jürimägi, Lembit; Orasson, Elmet; Raik, Jaan** 2015 IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC) : October 5-7, 2015, Daejeon, Korea 2015 / p. 171-176 : ill

### **Shared Structurally Synthesized BDDs for speeding-up parallel pattern simulation in digital circuits**

**Ubar, Raimund-Johannes; Jürimägi, Lembit; Raik, Jaan** 2015 Nordic Circuits and Systems Conference (NORCAS) : NORCHIP & International Symposium on System-on-Chip (SoC) : 1st IEEE NORCAS Conference : 26-28 October 2015, Oslo, Norway 2015 / [4] p. : ill <http://dx.doi.org/10.1109/NORCHIP.2015.7364406>

### **Teaching rapid application development skills for digitalisation challenges**

**Robal, Tarmo; Reinsalu, Uljana; Leoste, Janika; Jürimägi, Lembit; Heinsar, Risto** Digital Business and Intelligent Systems : 16th International Baltic Conference, Baltic DB&IS 2024, Vilnius, Lithuania, June 30 – July 3, 2024, Proceedings 2024 / p. 177-192 : ill [https://doi.org/10.1007/978-3-031-63543-4\\_12](https://doi.org/10.1007/978-3-031-63543-4_12) [Conference proceedings at Scopus](#) [Article at Scopus](#)

### **Timing-critical path analysis with structurally synthesized BDDs**

**Ubar, Raimund-Johannes; Jürimägi, Lembit; Jenihhin, Maksim; Raik, Jaan; Olugbenga, Niyi-Leigh; Viies, Vladimir** 2018 7th

**True path tracing in structurally synthesized BDDs for testability analysis of digital circuits**

**Ubar, Raimund-Johannes; Jürimägi, Lembit; Oyeniran, Adeboye Stephen; Jenihhin, Maksim** Euromicro Conference on Digital System Design : DSD 2019 : 28 - 30 August 2019 Kallithea, Chalkidiki, Greece : proceedings 2019 / p. 492-499 : ill <https://doi.org/10.1109/DSD.2019.00077>