

Activity classification for real-time wearable systems : effect of window length, sampling frequency and number of features on classifier performance

Allik, Ardo; Pilt, Kristjan; Karai, Deniss; Fridolin, Ivo; Leier, Mairo; Jervan, Gert 2016 IEEE EMBS Conference on Biomedical Engineering and Sciences (IECBES) : Kuala Lumpur, 4-8 December 2016 / p. 460-464 : ill <https://doi.org/10.1109/IECBES.2016.7843493>

Advancing novel physical fatigue assessment and human activity monitoring methods towards personalized feedback with wearable sensors = Kantavatel seadmetel põhinevate füüsilise väsimuse hindamise ning inimese aktiivsuse monitoorimise meetodite arendamine personaalseks tagasisideks

Allik, Ardo 2022 <https://doi.org/10.23658/taltech.62/2022> <https://digikogu.taltech.ee/et/Item/230f302e-0cf1-4b86-a9d3-0927a51b36e2> https://www.ester.ee/record=b5525012*est

“Al ei mõtle, Al arvutab”. Konverentsil kohtusid tehisaru rakendajad ja entusiastid

Kald, Indrek ituudised.ee 2024 [“Al ei mõtle, Al arvutab”. Konverentsil kohtusid tehisaru rakendajad ja entusiastid](https://www.ester.ee/record=b5525012*est)

Analog integrated circuits and signal processing

Ellervee, Peeter; Jervan, Gert 2010

Anomalous File System Activity detection through Temporal Association rule mining

Reza, M.; Iman, H.; Chikul, Pavel; Jervan, Gert; Bahsi, Hayretidin; Ghasempouri, Tara Proceedings of the 9th International Conference on Information Systems Security and Privacy ICISSP. Vol. 1 2023 / p. 733-740 <https://doi.org/10.5220/0000168400003405>

An approach to system-level design for test

Jervan, Gert; Ubar, Raimund-Johannes; Peng, Z.; Eles, Petru System-level test and validation of hardware/software systems 2005 / p. 121-149 : ill

ARTmine : automatic association rule mining with temporal behavior for hardware verification

Heidari Iman, Mohammad Reza; Jervan, Gert; Ghasempouri, Tara 2024 Design, Automation & Test in Europe Conference & Exhibition (DATE) : Valencia, Spain, 25-27 March 2024 : proceedings 2024 / p. 1-6 : ill <https://ieeexplore.ieee.org/document/10546742>

Arvutite õpetamisest siinpool Läänemerd ja sealpool Idamerd : [õppetööst Rootsi ülikoolides]

Jervan, Gert A & A 2007 / lk. 5-10 : ill

Assembling low-level tests to high-level symbolic test frames

Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes Proceedings [of the] 15th NORCHIP Conference, Tallinn, 10-11 November 1997 1997 / p. 275-280: ill

Asutajadekaanid : eesmärk on anda inimestele võimalus oma tööd paremini teha

Oorn, Arvo; Jervan, Gert; Kanger, Tõnis; Listra, Enn Mente et Manu 2016 / lk. 18-23 : fot https://artiklid.elnet.ee/record=b2797132*est

Augmented coaching ecosystem for non-obtrusive adaptive personalized elderly care on the basis of cloud-fog-dew computing paradigm [Electronic resource]

Gordienko, Yuri; Stirenko, S.; Alienin, O.; Jervan, Gert 2017 40th International Convention on Information and Communication Technology, Electronics and Microelectronics (MIPRO), May 22 - 26, 2017, Opatija, Croatia : proceedings 2017 / p. 359-364 : ill. [CD-ROM] <https://doi.org/10.23919/MIPRO.2017.7973449>

Automated area and coverage optimization of minimal latency checkers

Azad, Siavoosh Payandeh; Niazmand, Behrad; Apneet Kaur; Raik, Jaan; Jervan, Gert; Hollstein, Thomas 2017 22nd IEEE European Test Symposium (ETS 2017), Limassol, Cyprus, 22 – 26 May 2017 : proceedings 2017 / p. 7-8 : ill <https://doi.org/10.1109/ETS.2017.7968211>

An automated method for mining high-quality assertion sets

Heidari Iman, Mohammad Reza; Raik, Jaan; Jenihhin, Maksim; Jervan, Gert; Ghasempouri, Tara Microprocessors and microsystems 2023 / art. 104773 <https://doi.org/10.1016/j.micpro.2023.104773> Journal metrics at Scopus Article at Scopus Journal metrics at WOS Article at WOS

Automated minimization of concurrent online checkers for network-on-chips

Saltarelli, Pietro; Niazmand, Behrad; Hariharan, Ranganathan; Raik, Jaan; Jervan, Gert; Hollstein, Thomas 10th International Symposium on Reconfigurable and Communication-centric Systems-on-Chip (ReCoSoC 2015) : Bremen, 29 June - 1 July 2015 2015 / [8] p. : ill <http://dx.doi.org/10.1109/ReCoSoC.2015.7238079>

Automatic test generation system for VLSI

Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the First Electronic Circuits and Systems Conference : Bratislava, Slovakia, September 4-5, 1997 1997 / p. 255-258

AWAIT : an ultra-lightweight soft-error mitigation mechanism for network-on-chip links

Janson, Karl; Pihlak, Rene; Azad, Siavoosh Payandeh; Niazmand, Behrad; Jervan, Gert; Raik, Jaan 2018 13th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC), Lille, France, July 9th-11th, 2018 / p. 1-6 : ill <https://doi.org/10.1109/ReCoSoC.2018.8449374>

Baltic Workboatsi tehasejuht Erik Aleksejev: Saaremaa laevaehitaja niipea roboteid rakendama ei hakka
toostusuudised.ee 2024 [Baltic Workboatsi tehasejuht Erik Aleksejev: Saaremaa laevaehitaja niipea roboteid rakendama ei hakka](#)

Black-box assessment of optical spectrum services

Kaeval, Kaida; Elbers, Jörg-Peter; Grobe, Klaus; Tikas, Marko; Fehenberger, Tobias; Griesser, Helmut; **Jervan, Gert** 2021 Optical Fiber Communications Conference and Exhibition (OFC) : 6-10 June 2021 : San Francisco, CA, USA 2021 / 3 p
<https://ieeexplore.ieee.org/document/9489700>

CAD software for digital test and diagnostics

Jervan, Gert; Markus, Antti; Paomets, Priidu; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of International Conference on Design and Diagnostics of Electronic Circuits and Systems, Ostrava, Czech Republik, May 12-14, 1997 1997 / p. 35-40

CAESAR-MPSoC : dynamic and efficient MPSoC security zones

Azad, Siavoosh Payandeh; Tempelmeier, Michael; **Jervan, Gert;** Sepulveda, Johanna 2019 IEEE Computer Society Annual Symposium on VLSI : ISVLSI 2019 : proceedings 2019 / p. 477-482 : ill <https://doi.org/10.1109/ISVLSI.2019.00092>

Channel performance estimations with extended channel probing

Kaeval, Kaida; Griesser, Helmut; Grobe, Klaus; Elbers, Jörg-Peter; Tikas, Marko; **Jervan, Gert** Photonic Networks : 21th ITG-Symposium : 24-25 November 2020 : online 2020 / p. 60-64 <http://d-nb.info/1223037061> <https://ieeexplore.ieee.org/document/9273755>

Characterization of the optical spectrum as a service

Kaeval, Kaida; Jansen, Sander Lars; Spinty, Florian; Grobe, Klaus; Griesser, Helmut; Fehenberger, Tobias; Tikas, Marko; **Jervan, Gert** Journal of Optical Communications and Networking 2022 / p. 398-410 <https://doi.org/10.1364/JOCN.454158> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Classification algorithm improvement for physical activity recognition in maritime environments

Allik, Ardo; Pilt, Kristjan; Karai, Deniss; Fridolin, Ivo; Leier, Mairo; Jervan, Gert World Congress on Medical Physics and Biomedical Engineering 2018 : June 3–8, 2018, Prague, Czech Republic (Vol. 3) 2019 / p. 13-17 https://doi.org/10.1007/978-981-10-9023-3_3 [Conference proceeding](#) [Article at Scopus](#) [Article at WOS](#)

Communication modelling and synthesis for NoC-based systems with real-time constraints

Tagel, Mihkel; Ellervee, Peeter; Hollstein, Thomas; Jervan, Gert Proceedings of the 2011 IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 13-15, 2011, Gottbus, Germany 2011 / p. 237-242 : ill
<https://www.semanticscholar.org/paper/Communication-modelling-and-synthesis-for-NoC-based-Tagel-Ellervee/71f9595d88ed06b63367b87188b218fe6da6bd97>

Comparison of predictive equations for basal metabolic rate

Allik, Ardo; Mägi, Siiri; Pilt, Kristjan; Karai, Deniss; Fridolin, Ivo; Leier, Mairo; Jervan, Gert Wireless Mobile Communication and Healthcare : 7th International Conference, MobiHealth 2017, Vienna, Austria, November 14–15, 2017 : proceedings 2018 / p. 261-264 : ill <https://doi.org/10.1007/978-3-319-98551-0> [Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Comprehensive performance and robustness analysis of 2D turn models for network-on-chips

Azad, Siavoosh Payandeh; Niazmand, Behrad; Janson, Karl; Kogge, Thilo; Raik, Jaan; Jervan, Gert; Hollstein, Thomas 2017 IEEE International Symposium on Circuits and Systems (ISCAS) 2017 / p. 1476-1479 : ill
<https://doi.org/10.1109/ISCAS.2017.8050634> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Concatenated GSNR profiles for end-to-end performance estimations in disaggregated networks

Kaeval, Kaida; Myyry, Jani; Grobe, Klaus; Griesser, Helmut; **Jervan, Gert** 2022 Optical Fiber Communications Conference and Exhibition (OFC) : San Diego, California, USA, 6 – 10 March 2022 : proceedings 2022 / 3 p
<https://ieeexplore.ieee.org/document/9748620/figures#figures>

A constraint-driven gate-level test generator

Raik, Jaan; Ubar, Raimund-Johannes; Jervan, Gert; Krupnova, Helena BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 237-240: ill

Contention aware scheduling for NoC-based real-time systems

Tagel, Mihkel; Ellervee, Peeter; Hollstein, Thomas; Jervan, Gert Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK viienda aastakonverentsi artiklite kogumik : 25.-26. novembril 2011, Nelijärve 2011 / p. 75-78 : ill

Contention aware scheduling for NoC-based real-time systems

Tagel, Mihkel; Ellervee, Peeter; Hollstein, Thomas; Jervan, Gert Norchip 2011 : 14-15 November 2011, Lund 2011 / [4] p.: ill

CR14 ja Tallinna Tehnikaülikool sõlmisid leppe Eesti noorte küberkaitsevõistluse edendamiseks

Elektriala 2023 / lk. 11-12 : foto https://www.ester.ee/record=b1240496*est

Cross-layer dependability management in network on chip based system on chip = Kiipvõrkudel põhinevate süsteemide kihtideülene usaldatavuse haldus

Azad, Siavoosh Payandeh 2018 <https://digi.lib.ttu.ee/i/?9948> https://www.ester.ee/record=b5056143*est

DATE 2007 konverentsi ülevaade : [ka TTÜ arvutitehnika instituudi osalemisest konverentsil]

Tagel, Mihkel; Reinsalu, Uljana; Jervan, Gert A & A 2007 / 3, lk. 59-61 https://artiklid.elnet.ee/record=b1020793*est

DECIDER : a decision diagram based hierarchical test generation system

Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the 2nd International Workshop on Design and Diagnostics of Electronic Circuits and Systems, Szczyrk, Poland, September 2-4, 1998 1998 / p. 269-273
<https://www.ida.liu.se/labs/eslab/publications/pap/db/DDECS98.pdf>

A decision diagram based hierarchical test pattern generator

Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 159-162: ill

Dependability evaluation in fault-tolerant systems with high-level decision diagrams

Ubar, Raimund-Johannes; Jervan, Gert; Raik, Jaan; Jenihhin, Maksim; Ellervee, Peeter Computer Science Meets Automation : 10-13 September 2007 : proceedings. Volume II 2007 / p. 147-152 : ill https://www.db-thueringen.de/receive/dbt_mods_00008864

Dependability improvements of NoC-based systems = Töökindluse parandamine kiipvõrkudel põhinevates süsteemides

Niazmand, Behrad 2018 <https://digi.lib.ttu.ee/i/?9879> https://www.ester.ee/record=b4907650*est

Dependable embedded systems : FP7 KhAI-ERA project experience

Kharchenko, Vyacheslav; Kulanov, Vitaliy; Jervan, Gert 10th European Workshop on Microelectronics Education : EWME 2014 : May 14-16, 2014, Tallinn, Estonia 2014 / p. 26-30 : ill

Design and verification of secure cache wrapper against access-driven side-channel attacks

Niazmand, Behrad; Azad, Siavoosh Payandeh; Jervan, Gert; Sepulveda, Johanna Euromicro Conference on Digital System Design : DSD 2019 : 28 - 30 August 2019 Kallithea, Chalkidiki, Greece : proceedings 2019 / p. 672-676 : ill
<https://doi.org/10.1109/DSD.2019.00108>

Design methodology for fault-tolerant heterogeneous MPSoC under real-time constraints

Amin, Mohsin; Tagel, Mihkel; Jervan, Gert; Hollstein, Thomas 7th International Workshop on Reconfigurable and Communication-Centric Systems-on-Chip : July 9-11, 2012 : York, United Kingdom : proceedings 2012 / [6 p.] : ill
<https://ieeexplore.ieee.org/document/6322901>

Design space exploration and optimisation for NoC-based timing sensitive systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert BEC 2010 : 2010 12th Biennial Baltic Electronics Conference : proceedings of the 12th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 4-6, 2010, Tallinn, Estonia 2010 / p. 177-180 : ill

Design space exploration and optimisation for NoC-based timing sensitive systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK neljanda aastakonverentsi artiklite kogumik : 26.-27. novembril 2010, Essu mõis 2010 / lk. 117-120 : ill

Deterministic traffic generator for network-on-chip simulator

Tagel, Mihkel; Jervan, Gert Workshop Digest. Diagnostic Services in Network-on-Chips - Test, Debug, and On-Line Monitoring : DATE 2007 : Nice, France, 16-20 April, 2007 2007 / p. 288-290

Deterministic traffic generator for network-on-chip simulator

Tagel, Mihkel; Jervan, Gert Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK teise aastakonverentsi artiklite kogumik : 11.-12. mai 2007, Viinistu kunstimuseum 2007 / lk. 72-75 : ill

Digikeskus GovAILab aitab riigil luua tõhusaid tehisintellekti põhiseid lahendusi

Mente et Manu 2019 / lk. 7 : portr https://www.ttu.ee/public/m/mente-et-manu/MM_05_2019/mobile/index.html
https://www.ester.ee/record=b1242496*est

Digital design flow with test activities

Diener, Karl-Heinz; Elst, G.; Ivask, Eero; Jervan, Gert; Peng, Z.; Raik, Jaan; Ubar, Raimund-Johannes VILAB User Forum 2000 / [11] p

Doktoriõppe arengud ja kitsaskohad

Jervan, Gert A & A 2010 / 4, lk. 6-9

Dynamic and distributed security management for NoC Based MPSoCs

Azad, Siavoosh Payandeh; Jervan, Gert; Sepulveda, Johanna Computational Science - ICCS 2019 : 19th International Conference, Faro, Portugal, June 12-14, 2019 : Proceedings, Part II 2019 / p. 649–662 : ill https://doi.org/10.1007/978-3-030-22741-8_4
[Conference proceeding at Scopus](#) [Article at Scopus](#) [Conference proceeding at WOS](#) [Article at WOS](#)

Edge intelligence resource consumption by UAV-based IR object detection

Polukhin, Andrii; Gordienko, Yuri; Leier, Mairo; Jervan, Gert; Rokovyi, Oleksandr; Alienin, Oleg; Stirenko, Sergii UAVM '23 : proceedings of the 2023 Workshop on UAVs in multimedia : capturing the world from a new perspective 2023 / p. 57-61
<https://doi.org/10.1145/3607834.3616566>

Eesti – innovaatiivne tiiger või hääbuv väikeriik [Võrguväljaanne]

Jervan, Gert postimees.ee 2022 ["Eesti – innovaatiivne tiiger või hääbuv väikeriik"](#)

Eesti - innovaatiivne tiiger või hääbuv väikeriik?

Jervan, Gert Mente et Manu 2022 / lk. 4-5 : portr https://www.ester.ee/record=b1242496*est

Eesti teaduspööre? Valitsus suurendab doktorikohti, ent mitte nooremteaduri palka

Lomp, Looa-Elisabet postimees.ee 2025 <https://www.postimees.ee/8247991/eesti-teaduspoores-valitsus-suurendab-doktorikohti-ent-mitte-nooremteaduri-palka>

Eesti vajab uusi teadlasi

Jervan, Gert Postimees 2021 / Lk. 13 <https://dea.digar.ee/article/postimees/2021/06/17/12.6>

Eestis langes tähtis otsus: 5G ei tapa [Võrguväljaanne]

Pau, Aivar tehnika.postimees.ee 2019 / fot [Eestis langes tähtis otsus: 5G ei tapa](#)

Embedded software solutions for development of marine navigation light system = Sardtarkvara lahendused valgusnavigatsiooni süsteemide arendusel

Moorits, Erkki 2016 <http://digi.lib.ttu.ee/i/?6383>

Employing channel probing to derive end-of-life service margins for optical spectrum services

Kaeval, Kaida; Slyne, Frank; Troia, Sebastian; Kenny, Eoin; Grobe, Klaus; Griesser, Helmut; Kilper, Daniel C.; Ruffini, Marco; Pedreno-Manresa, Jose-Juan; Patri, Sai Kireet; **Jervan, Gert** Journal of optical communications and networking 2023 / p. C63-C73 : ill <https://doi.org/10.1364/JOCN.480080> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Enabling secure MPSoC dynamic operation through protected communication

Azad, Siavoosh Payandeh; Niazmand, Behrad; Jervan, Gert; Sepulveda, Johanna 2018 25th IEEE International Conference on Electronics Circuits and Systems (ICECS), Bordeaux, France, December 9–12, 2018 2018 / p. 481-484 : ill <https://doi.org/10.1109/ICECS.2018.8617940>

Energy minimization for hybrid BIST in a system-on-chip test environment

Ubar, Raimund-Johannes; Shchenova, Tatjana; Jervan, Gert; Peng, Zebo European Test Symposium : ETS 2005 : 22-25 May 2005, Tallinn, Estonia : proceedings 2005 / p. 2-7 : ill

Enhancing assertion-based verification in hardware designs through data mining algorithms = Andmekaeve algoritmide kasutamine riistvarasüsteemide väidete-põhise verifitseerimise parendamiseks

Heidari Iman, Mohammad Reza 2024 <https://digikogu.taltech.ee/et/Item/622f3660-e890-454d-aa85-7cc505557f77>
<https://doi.org/10.23658/taltech.37/2024> https://www.ester.ee/record=b5694015*est

Evaluation of SysML software for teaching systems engineering basics

Kruus, Helena; Jervan, Gert Proceedings of the 25th International Conference on European Association for Education in Electrical and Information Engineering : 30 May-June 2014, Cesme, Türkiye 2014 / p. 29-32 : ill

Exploring service margins for optical spectrum services

Kaeval, Kaida; Slyne, Frank; Troia, Sebastian; Kenny, Eoin; Pedreno-Manresa, Jose-Juan; Patri, Sai Kireet; Grobe, Klaus; Kilper, Daniel C.; Ruffini, Marco; **Jervan, Gert** Proceedings European Conference on Optical Communication (ECOC 2022), 18-22 September 2022, Basel, Switzerland 2022 / art. Mo3B.4, 4 p <https://opg.optica.org/abstract.cfm?uri=ECOC-2022-Mo3B.4>

Extended checkers for control part of routers in network-on-chips

Hariharan, Ranganathan; Niazmand, Behrad; Hollstein, Thomas; Raik, Jaan; Jervan, Gert MEDIAN 2015 : the 4th Workshop on Manufacturable and Dependable Multicore Architectures at Nanoscale : March 13, 2015, Grenoble, France 2015 / p. 36-39 : ill

Extended checkers for logic-based distributed routing in network-on-chips

Niazmand, Behrad; Hariharan, Ranganathan; Govind, Vineeth; Jervan, Gert; Hollstein, Thomas; Raik, Jaan Proceedings of the 8th Annual Conference of the Estonian National Doctoral School in Information and Communication Technologies : December 5-6,

Extended checkers for logic-based distributed routing in network-on-chips

Niazmand, Behrad; Hariharan, Ranganathan; Govind, Vineeth; Jervan, Gert; Hollstein, Thomas; Raik, Jaan BEC 2014 : 2014 14th Biennial Baltic Electronics Conference : proceedings of the 14th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 6-8, 2014, Tallinn, Estonia 2014 / p. 77-80 : ill

Fall detection and activity recognition system for usage in smart work-wear [Online resource]

Leier, Mairo; Jervan, Gert; Allik, Ardo; Pilt, Kristjan; Karai, Deniss; Fridolin, Ivo BEC 2018 : 2018 16th Biennial Baltic Electronics Conference (BEC) : proceedings of the 16th Biennial Baltic Electronics Conference, October 8-10, 2018 2018 / 4 p.: ill
<https://doi.org/10.1109/BEC.2018.8600959>

Fast test cost calculation for hybrid BIST in digital systems

Orasson, Elmet; Raidma, Rein; Ubar, Raimund-Johannes; Jervan, Gert; Peng, Zebo Euromicro Symposium on Digital Systems Design : [Architectures, Methods and Tools : DSD 2001] : September 4-6, 2001, Warsaw, Poland : proceedings 2001 / p. 318-325 : ill
<https://www.semanticscholar.org/paper/Fast-test-cost-calculation-for-hybrid-BIST-in-Orasson-Raidma/5aafcda5a18c2aabf0ad20cac10af10727f3c58f>

Fault model and test synthesis for RISC-processors

Ubar, Raimund-Johannes; Markus, Antti; Jervan, Gert; Raik, Jaan BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 229-232: ill

Fault-resilient NoC router with transparent resource allocation

Putkaradze, Tsothe; Azad, Siavoosh Payandeh; Niazmand, Behrad; Raik, Jaan; Jervan, Gert 12th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC2017), July 12-14, 2017, Madrid, Spain : proceedings 2017 / 8 p. : ill
<https://doi.org/10.1109/ReCoSoC.2017.8016161> <https://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=8016161>

Fault-tolerant scheduling of mixed-critical applications on multi-processor platforms

Bagheri, Mehrdad; Jervan, Gert 2014 International Conference on Embedded and Ubiquitous Computing : EUC 2014 : 26-28 August 2014, Milano, Italy : proceedings 2014 / p. 25-32 : ill

FPGA based emulation environment

Jervan, Gert; Arhipov, Anton; Ellervee, Peeter 2nd International Workshop on Reconfigurable Communication Centric System-on-Chip (ReCoSoC'06) 2006

A framework for area-efficient concurrent online checkers design

Saltarelli, Pietro; Niazmand, Behrad; Hariharan, Ranganathan; Raik, Jaan; Jervan, Gert; Hollstein, Thomas MEDIAN Finale : Workshop on Manufacturable and Dependable Multicore Architectures at Nanoscale : November 10-11, 2015, Tallinn, Estonia 2015 / p. 64-69 : ill

A framework for combining concurrent checking and online embedded test for low-latency fault detection in NoC routers

Saltarelli, Pietro; Niazmand, Behrad; Raik, Jaan; Govind, Vineeth; Hollstein, Thomas; Jervan, Gert; Hariharan, Ranganathan NOCS '15 : International Symposium on Networks-on-Chip : Vancouver, BC, Canada, September 28-30, 2015 2015 / [8] p. : ill
<http://dx.doi.org/10.1145/2786572.2788713>

A framework for comprehensive automated evaluation of concurrent online checkers

Saltarelli, Pietro; Niazmand, Behrad; Raik, Jaan; Hariharan, Ranganathan; Jervan, Gert; Hollstein, Thomas Euromicro Conference on Digital System Design : DSD 2015 : 26-28 August 2015, Funchal, Madeira, Portugal : proceedings 2015 / p. 288-292 : ill
<http://dx.doi.org/10.1109/DSD.2015.15>

From online fault detection to fault management in network-on-chips : a ground-up approach

Azad, Siavoosh Payandeh; Niazmand, Behrad; Janson, Karl; Nevin, George; Oyeniran, Adeboye Stephen; Putkaradze, Tsothe; Apneet Kaur; Raik, Jaan; Jervan, Gert; Ubar, Raimund-Johannes; Hollstein, Thomas Proceedings 2017 IEEE 20th International Symposium on Design and Diagnostics of Electronic Circuit & Systems(DDECS) : April 19-21, 2017, Dresden, Germany 2017 / p. 48-53 : ill
<https://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=7934553>

Generalization of network-on-chip communication modelling

Ellervee, Peeter; Tagel, Mihkel; Jervan, Gert Workshop Digest. Diagnostic Services in Network-on-Chips - Test, Debug, and On-Line Monitoring : DATE 2009 : Nice, France, 20-24 April, 2009 2009 / ? p

Gert Jervan: AI Leap — benefits of early adoption

Jervan, Gert <https://news.err.ee/1609624118/gert-jervan-ai-leap-benefits-of-early-adoption> 2025

Gert Jervan: TalTechi uurimisrühma tulemusel on Eesti kui digiriigi eestvedaja paremini kaitstud

Kald, Indrek ituudised.ee 2023 [Gert Jervan: TalTechi uurimisrühma tulemusel on Eesti kui digiriigi eestvedaja paremini kaitstud](#)

Gert Jervan: tehisintellekti tark kasutamise võib inimkonnale palju kasu tuua

kultuur.err.ee 2023 [Gert Jervan: tehisintellekti tark kasutamise võib inimkonnale palju kasu tuua](#)

Gert Jervan: Tehisintellektiga on praegu nii nagu kunagi tuumarelvadega – tõestada, et me suudame seda teha

Tammepuu, Kadri; **Jervan, Gert** tv.postimees.ee 2023 [Gert Jervan: Tehisintellektiga on praegu nii nagu kunagi tuumarelvadega – tõestada, et me suudame seda teha](#)

Gert Jervan: TI õpib meie pealt ja meie jagatud infost

Jervan, Gert Õpetajate Leht 2025 / lk. 6-7 <https://dea.digar.ee/article/opetajateleht/2025/05/13/7.1> <https://www.opleht.ee/2025/05/gert-jervan-ti-opib-meie-pealt-ja-meie-jagatud-infost/>

Gert Jervan: TI-hüpe – varase kasutuselevõtu eelised

Jervan, Gert err.ee 2025 <https://www.err.ee/1609621601/gert-jervan-ti-hupe-varase-kasutuselevotu-eelised>

Graduate School in Information and Communication Technologies : experiences at Tallinn University of Technology

Jervan, Gert; Kruus, Margus; Rüstern, Ennu Proceedings MSE 2007 : 2007 IEEE International Conference on Microelectronic Systems Education : 3-4 June 2007, San Diego, CA 2007 / p. 25-26

Handling of SETs on NoC links by exploitation of inherent redundancy in circular input buffers [Online resource]

Janson, Karl; Pihlak, Rene; Azad, Siavoosh Payandeh; Niazmand, Behrad; Jervan, Gert; Raik, Jaan BEC 2018 : 2018 16th Biennial Baltic Electronics Conference (BEC) : proceedings of the 16th Biennial Baltic Electronics Conference, October 8-10, 2018 2018 / 4 p.: ill <https://doi.org/10.1109/BEC.2018.8600989>

Head lõpetajad! = Dear graduates!

Land, Tiit; Sergejev, Fjodor; Jervan, Gert; Salupere, Andrus; Listra, Enn; Leiger, Roomet Mente et Manu 2021 / lk. 30-50 : fot https://www.ester.ee/record=b1242496*est

A hierarchical approach for devising area efficient concurrent online checkers

Niazmand, Behrad; Azad, Siavoosh Payandeh; Ghasempouri, Tara; Raik, Jaan; Jervan, Gert Proceedings 2nd IEEE International Test Conference in Asia : ITC-Asia 2018, 15-17 August 2018, Harbin, China 2018 / p. 139-144 : ill <https://doi.org/10.1109/ITC-Asia.2018.00034>

A hierarchical automatic test pattern generator based on using alternative graphs

Brik, Marina; Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the 4th International Workshop Mixed Design of Integrated Circuits and Systems : MIXDES'97 : Poznan, Poland, 12-14 June 1997 1997 / p. 415-420

Hierarchical calculation of malicious faults for evaluating the fault-tolerance

Ubar, Raimund-Johannes; Devadze, Sergei; Jenihhin, Maksim; Raik, Jaan; Jervan, Gert; Ellervee, Peeter Proceedings : Fourth IEEE International Symposium on Electronic Design, Test and Applications : [DELTA 2008] : 23-25 January 2008, Hong Kong, SAR, China 2008 / p. 222-227 : ill <https://ieeexplore.ieee.org/document/4459544>

Hierarchical test generation for digital systems

Brik, Marina; Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes Mixed design of integrated circuits and systems 1998 / p. 131-136: ill https://link.springer.com/chapter/10.1007/978-1-4615-5651-0_20

Hierarchical test generation with multi-level decision diagram models

Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the 7th IEEE North Atlantic Test Workshop, West Greenwich RI, USA, May 28-29, 1998 1998 / p. 26-33 https://www.academia.edu/67811738/Hierarchical_Test_Generation_with_Multi_Level_Decision_Diagram_Models?hb-g-sw=7883185

High-level synthesis and test in the MOSCITO-based virtual laboratory

Schneider, Andre; Diener, Karl-Heinz; **Jervan, Gert**; Peng, Z.; **Raik, Jaan; Ubar, Raimund-Johannes**; Hollstein, Thomas; Glesner, M. BEC 2002 : proceedings of the 8th Biennial Baltic Electronics Conference : October 6-9, 2002, Tallinn, Estonia 2002 / p. 287-290 : ill

High-level test synthesis with hierarchical test generation

Jervan, Gert; Eles, Petru; Peng, Zebo; **Raik, Jaan; Ubar, Raimund-Johannes** 17th NORCHIP Conference : Oslo, Norway, 8-9 November 1999 : proceedings 1999 / p. 291-296

Hirm masinate ees: tehisintellekti liiga kiirele arengule tõmmatakse pidurit

Strandberg, Marek; Einama, Kaido Postimees 2023 / lk. 5 <https://dea.digar.ee/article/postimees/2023/03/31/3.11> [Hirm masinate ees: tehisintellekti liiga kiirele arengule tahetakse tõmmata pidurit](#)

Holistic approach for Fault-Tolerant Network-on-Chip based many-core systems [Online resource]

Azad, Siavoosh Payandeh; Niazmand, Behrad; Raik, Jaan; Jervan, Gert; Hollstein, Thomas arXiv.org 2016 / [8] p. : ill

How to emulate Network-on-Chip?

Ellervee, Peeter; Jervan, Gert Proceedings of the IEEE East-West Design & Test Workshop (EWDWTW'06) : Sochi, Russia, September 15-19, 2006 2006 / p. 282-286 : ill

A hybrid BIST architecture and its optimization for SoC testing

Jervan, Gert; Peng, Zebo; Ubar, Raimund-Johannes; Kruus, Helena Proceedings of the 3rd International Symposium on Quality Electronic Design : ISQED 2002, March 18-21, 2002, San Jose, California 2002 / p. 273-279 : ill
<https://ieeexplore.ieee.org/document/996750>

Hybrid BIST energy minimisation technique for system-on-chip testing

Jervan, Gert; Peng, Zebo; Shchenova, Tatjana; Ubar, Raimund-Johannes IEE proceedings computers & digital techniques 2006 / 4, p. 208-216 : ill <https://citeseerx.ist.psu.edu/document?repid=rep1&type=pdf&doi=5ae755d323ccba87f8ff886334e3dd6d33560874>

Hybrid BIST methodology for testing core-based systems

Jervan, Gert; Ubar, Raimund-Johannes; Peng, Zebo Proceedings of the Estonian Academy of Sciences. Engineering 2006 / 3-2, p. 300-322 : ill

Hybrid BIST optimization for core-based systems with test pattern broadcasting

Ubar, Raimund-Johannes; Jenihhin, Maksim; Jervan, Gert; Peng, Zebo DELTA 2004 : second IEEE International Workshop on Electronic Design, Test and Applications : 28-30 January 2004, Perth, Australia : proceedings 2004 / p. 3-8 : ill
<https://ieeexplore.ieee.org/document/1409808>

Hybrid BIST optimization using reseeding and test set compaction

Jervan, Gert; Orasson, Elmet; Kruus, Helena; Ubar, Raimund-Johannes 10th Euromicro Conference on Digital System Design Architectures, Methods and Tools, DSD 2007 : 29-31 August 2007, Lübeck, Germany : proceedings 2007 / p. 596-603 : ill
<http://dx.doi.org/10.1109/DSD.2007.4341529>

Hybrid BIST optimization using reseeding and test set compaction

Jervan, Gert; Orasson, Elmet; Kruus, Helena; Ubar, Raimund-Johannes Microprocessors and microsystems 2008 / 5/6, p. 254-262 : ill <https://www.sciencedirect.com/science/article/abs/pii/S0141933108000288>

Hybrid BIST scheduling for NoC-based SoCs

Jervan, Gert; Shchenova, Tatjana; Ubar, Raimund-Johannes Proceedings [of] 24th IEEE Norchip Conference : Linköping, Sweden, 20-21 November 2006 2006 / p. 141-144 : ill <https://ieeexplore.ieee.org/document/4126966>

Hybrid BIST time minimization for core-based systems with STUMPS architecture

Jervan, Gert; Eles, Petru; Peng, Zebo; Ubar, Raimund-Johannes; Jenihhin, Maksim 18th IEEE International Symposium on Defect and Fault Tolerance in VLSI Systems : 3-5 November 2003, Boston, Massachusetts : proceedings 2003 / p. 225-232 : ill
<https://ieeexplore.ieee.org/document/1250116>

Hybrid built-in self-test and test generation techniques for digital systems

Jervan, Gert 2005 https://www.ester.ee/record=b2177537*est

IKT haridus ülikoolides läbi TTÜ arvutitehnika instituudi vaateprisma

Kruus, Margus; Jervan, Gert Arvutimaailm 2010 / 5, lk. 8-9 : fot https://artiklid.elnet.ee/record=b2409375*est

IMMizer : an innovative cost-effective method for minimizing assertion sets

Heidari Iman, Mohammad Reza; Raik, Jaan; Jervan, Gert; Ghasempouri, Tara 2022 25th Euromicro Conference on Digital System Design (DSD) : Maspalomas, Spain 2022 : proceedings 2022 / p. 671 - 678 <https://doi.org/10.1109/DSD57027.2022.00095>
[Article at Scopus](#) [Article at WOS](#)

An improved estimation methodology for hybrid BIST cost calculation

Jervan, Gert; Peng, Zebo; Ubar, Raimund-Johannes; Korelina, Olga Proceedings [of] 22nd NORCHIP Conference : Oslo, Norway, 8-9 November 2004 2004 / p. 297-300 : ill <https://ieeexplore.ieee.org/document/1423882>

An improved estimation technique for hybrid BIST test set generation

Jervan, Gert; Peng, Zebo; Ubar, Raimund-Johannes; Korelina, Olga DDECS : 8th IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems : April 13-16, 2005, Sopron, Hungary : proceedings 2005 / p. 182-185 : ill
<https://www.ida.liu.se/labs/eslab/publications/pap/db/ddecs05.pdf>

Innovaatiline tiiger või hääbuv väikeriik?

Jervan, Gert Postimees 2022 / Lk. 13 <https://dea.digar.ee/article/postimees/2022/04/26/13.7>

Innovation and entrepreneurship in the computer systems curricula and Nordic Master School in innovative ICT

Jervan, Gert; Ellervee, Peeter; Kruus, Margus 22nd EAEEIE annual conference : June, 13-15, 2011, Maribor, Slovenija : conference book 2011 / p. 9 <https://ieeexplore.ieee.org/document/6165723>

IT Akadeemia - hea näide, kuidas süstemaatiliselt koostööd teha

Jervan, Gert Mente et Manu 2022 / lk. 30-31 : portr https://www.ester.ee/record=b1242496*est

An iterative approach to test time minimization for parallel hybrid BIST architecture

Ubar, Raimund-Johannes; Jenihhin, Maksim; Jervan, Gert; Peng, Z. 5th IEEE Latin-American Test Workshop - LATW 2004 : Cartagena, Colombia, 2004 : digest of papers 2004 / p. 98-103 : ill <https://www.ida.liu.se/labs/eslab/publications/pap/db/latw04.pdf>

An iterative approach to test time minimization for parallel hybrid BIST architectures

Ubar, Raimund-Johannes; Jenihhin, Maksim; Jervan, Gert; Peng, Z. System-on-Chip Conference 2004 : Bastad, Sweden 2004 / p. ? <https://www.ida.liu.se/labs/eslab/publications/pap/db/latw04.pdf>

IT-hariduse skeletiks on nüüd inseneriks saamine

Jervan, Gert; Lorents, Peeter Mente et Manu 2017 / lk. 39-41 : fot http://www.ester.ee/record=b1242496*est
https://artiklid.elnet.ee/record=b2830876*est

IT-teaduskond tähistab sügisel TalTechi sünnipäeva 102 loenguga Eesti koolidele

Mente et Manu 2020 / lk. 14 https://www.ester.ee/record=b1242496*est

IT-valdkond vajab tudengeid välisriikidest

Jervan, Gert Eesti Päevaleht 2014 / Ärioleht, lk. 16-17

Jaak Aaviksoo tuleb rektoriks kinnitada

Jervan, Gert Eesti Päevaleht 2015 / lk. 11 <https://epl.delfi.ee/artikkel/71821559/jaak-aaviksoo-tuleb-rektoriks-kinnitada>

Keerukate arvutisüsteemide uurimine Tallinna Tehnikaülikoolis

Jervan, Gert; Ellervee, Peeter; Ubar, Raimund-Johannes Tallinna Tehnikaülikooli aastaraamat 2007 2008 / lk. 42-60 : ill

Kolm professorit versus ChatGPT

Russak, Ülo Harju Elu 2023 / Lk. 3 https://dea.digar.ee/article/harjuelu/2023/04/14/4.1_Kolm_professorit_versus_ChatGPT

Kui internet saab täis

Jervan, Gert Postimees 2020 / Lk. 13 <https://dea.digar.ee/article/postimees/2020/07/30/11.3> <https://leht.postimees.ee/7028359/gert-jervan-kui-eesti-internet-saab-tais>

10 põhjust, miks astuda TalTechi IT-teaduskonda

arileht.delfi.ee 2023 [10 põhjust, miks astuda TalTechi IT-teaduskonda](https://arileht.delfi.ee/10-pohjust-miks-astuda-taltech-i-teaduskonda)

Logic-based implementation of fault-tolerant routing in 3D Network-on-Chips

Niazmand, Behrad; Azad, Siavoosh Payandeh; Flich, Jose; Raik, Jaan; Jervan, Gert; Hollstein, Thomas 2016 Tenth IEEE/ACM International Symposium on Networks-on-Chip (NOCS) : Nara, Japan, 31 August - 2 September 2016 2016 / [8] p. : ill <https://doi.org/10.1109/NOCS.2016.7579317>

Low resource demanding FOTA method for remote AtoN site equipment

Moorits, Erkki; Jervan, Gert OCEANS 2010 : Seattle, Washington, USA, September 20-23, 2010 2010 / [5] p <https://ieeexplore.ieee.org/document/5664107>

Low-cost CAD system for teaching digital test

Ubar, Raimund-Johannes; Raik, Jaan; Paomets, Priidu; Ivask, Eero; Jervan, Gert; Markus, Antti Microelectronics education : proceedings of the European Workshop, Grenoble, France, 5-6 Feb 1996 1996 / p. 185-188

[Lõpetajad = Graduates]

Land, Tiit; Sergejev, Fjodor; Jervan, Gert; Salupere, Andrus; Listra, Enn; Leiger, Roomet Mente et Manu 2022 / lk. 24-43 : fot https://www.ester.ee/record=b1242496*est

[Lõpetajad = Graduates]

Land, Tiit; Sergejev, Fjodor; Jervan, Gert; Salupere, Andrus; Avarmaa, Mari; Leiger, Roomet Mente et Manu 2023 / lk. 30-49 : fot https://www.ester.ee/record=b1242496*est

[Lõpetajad 2018 : rektori ja dekaanide kõned]

Aaviksoo, Jaak; Kanger, Tõnis; Jervan, Gert; Oorn, Arvo; Listra, Enn; Leiger, Roomet Mente et Manu 2018 / lk. 16-31 : portr https://ttu.ee/public/m/mente-et-manu/MM_02_2018/mobile/index.html#p=6 http://www.ester.ee/record=b1242496*est
<http://dea.digar.ee/publication/AKmenteetmanu>

[Lõpetajad 2019 : rektori ja dekaanide soovid]

Aaviksoo, Jaak; Jervan, Gert; Kanger, Tõnis; Listra, Enn; Oorn, Arvo; Leiger, Roomet Mente et Manu 2019 / lk. 18-35 : fot https://www.ester.ee/record=b1242496*est https://www.ttu.ee/public/m/mente-et-manu/MM_03_2019/mobile/index.html

[Lõpetajad = Graduates]

Land, Tiit; Sergejev, Fjodor; Jervan, Gert; Salupere, Andrus; Avarmaa, Mari; Leiger, Roomet Mente et Manu 2024 / lk. 28-46 :

fot https://www.ester.ee/record=b1242496*est

Majandusele oluline kiibikeskus võib 1 miljoni pärast käest libiseda: MKM kahtleb, EISA on poolt

Puusild, Harro aripaev.ee 2024 [Majandusele oluline kiibikeskus võib 1 miljoni pärast käest libiseda: MKM kahtleb, EISA on poolt](#)

Meditsiinitehnika lipulaeva Karl Storzi ja TalTechi koostöös sünnivad maailmamuutvad teadus- ja uurimisprojektid

Raag, Toomas digi.geenius.ee 2023 [Meditsiinitehnika lipulaeva Karl Storzi ja TalTechi koostöös sünnivad maailmamuutvad teadus- ja uurimisprojektid](#) [Meditsiinitehnika lipulaeva Karl Storzi ja TalTechi koostöös sünnivad maailmamuutvad teadus- ja uurimisprojektid](#)

A methodology for automated mining of compact and accurate assertion sets

Heidari Iman, Mohammad Reza; Raik, Jaan; Jenihhin, Maksim; Jervan, Gert; Ghasempouri, Tara 2021 IEEE Nordic Circuits and Systems Conference (NorCAS) : Oslo, Norway, October 26-27 2021 / 7 p. : ill <https://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=9599865> <https://doi.org/10.1109/NorCAS53631.2021.9599865>

Microprocessors and microsystems : MICPRO : embedded hardware design

2010

Mida on õppida Rootsist? : [Linköpingi Ülikooli doktorandi tähelepanekuid : võrdlusi TTÜga]

Jervan, Gert Mente et Manu 2001 / 22. mai, lk. 2 : fot https://www.ester.ee/record=b1242496*est

Mida vinget tõi lõppev aasta tehnikailma? : [2016. aasta olulisematest uuendustest tehnoloogiamailmas : ekspertidena vastavad Gert Jervan, Argo Virkebau, ... Maarja Kruusmaa jt.]

Pau, Aivar Postimees 2016 / lk. 24-25 : ill

Miniaturized wireless monitor for long-term monitoring of newborns

Leier, Mairo; Jervan, Gert BEC 2014 : 2014 14th Biennial Baltic Electronics Conference : proceedings of the 14th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 6-8, 2014, Tallinn, Estonia 2014 / p. 193-196 : ill

Missioonikriitiliste sardsüsteemide arendamise nimel : [Integreeritud Elektroonikasüsteemide ja Biomeditsiinitehnika Tippkeskusest : intervjuu Raimund Ubari ja Gert Jervaniga]

Ubar, Raimund-Johannes; Jervan, Gert; Ummelas, Mart Mente et Manu 2009 / 30. jaan., lk. 1, 3 : ill ; 16. veebr., lk. 5 : ill https://www.ester.ee/record=b1242496*est

Mixed-level deterministic-random test generation for digital systems

Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the 5th International Conference on Mixed Design of Integrated Circuits and Systems, Lodz, Poland, June 18-20, 1998 1998 / p. 335-340

Mixed-level test generator for digital systems

Brik, Marina; Jervan, Gert; Markus, Antti; Paomets, Priidu; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the Estonian Academy of Sciences. Engineering 1997 / 4, p. 271-282 : ill

A novel physical fatigue assessment method utilizing heart rate variability and pulse arrival time towards personalized feedback with wearable sensors

Allik, Ardo; Pilt, Kristjan; Viigimäe, Moonika; Fridolin, Ivo; Jervan, Gert Sensors 2022 / art. 1680 <https://doi.org/10.3390/s22041680> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Object detection for rescue operations by high-altitude infrared thermal imaging collected by unmanned aerial vehicles

Polukhin, Andrii; Gordienko, Yuri; Jervan, Gert; Stirenko, Sergii Pattern Recognition and Image Analysis : 11th Iberian Conference, IbPRIA 2023, Alicante, Spain, June 27-30, 2023 : proceedings 2023 / p. 490-504 https://doi.org/10.1007/978-3-031-36616-1_39 [Conference proceedings at Scopus](#) [Article at Scopus](#)

Optical spectrum services in Open Disaggregated Transport Networks = Optilise spektri teenused avatud laine-pikkustihendus võrkudes

Kaeval, Kaida 2023 <https://doi.org/10.23658/taltech.13/2023> <https://digikogu.taltech.ee/et/Item/aeb5432f-9aff-4683-bcd6-1bc5b04432d0> https://www.ester.ee/record=b5557127*est

Optimization of memory-constrained hybrid BIST for testing core-based systems

Jervan, Gert; Kruus, Helena; Orasson, Elmet; Ubar, Raimund-Johannes Proceedings of the IEEE 2nd International Symposium on Industrial Embedded Systems : SIES'2007 : Lisbon, Portugal, 4-6 July 2007 2007 / p. 71-77 <https://ieeexplore.ieee.org/document/4297319>

Optimization of memory-constrained hybrid BIST for testing core-based systems

Jervan, Gert; Kruus, Helena; Orasson, Elmet; Ubar, Raimund-Johannes Info- ja kommunikatsioonitehnoloogia doktorikooli

Optimization of physical activity recognition for real-time wearable systems : effect of window length, sampling frequency and number of features

Allik, Ardo; Pilt, Kristjan; Karai, Deniss; Fridolin, Ivo; Leier, Mairo; Jervan, Gert Applied sciences 2019 / art. 4833, 14 p. : ill
<https://doi.org/10.3390/app9224833> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Optimization of the store-and-generate based built-in self-test

Ubar, Raimund-Johannes; Jervan, Gert; Kruus, Helena; Orasson, Elmet; Aleksejev, Igor BEC 2006 : 2006 International Baltic Electronics Conference : Tallinn University of Technology, October 2-4, 2006, Tallinn, Estonia : proceedings of the 10th Biennial Baltic Electronics Conference 2006 / p. 199-202 : ill

Persoon : Gert Jervan

Mänd, Krööt Grete; **Jervan, Gert** Studioosus 2019 / lk. 6-9 https://www.ester.ee/record=b1558644*est

Power-constrained hybrid BIST test scheduling in an abort-on-first-fail test environment

He, Zhiyuan; **Jervan, Gert**; Peng, Zebo; Eles, Petru Proceedings : DSD'2005 : 8th Euromicro Conference on Digital System Design : Architectures, Methods and Tools : Porto, Portugal, August 30 - September 3, 2005 2005 / p. 83-86 : ill
<https://ieeexplore.ieee.org/document/1559782>

Professor TI-Hüppest: eesti keelest saab suur väljakutse

Jervan, Gert err.ee 2025 <https://www.err.ee/1609615346/professor-ti-huppest-eesti-keelest-saab-suur-valjakutse>

Profiling in deeply embedded systems

Moorits, Erkki; Jervan, Gert BEC 2012 : 2012 13th Biennial Baltic Electronics Conference : proceedings of the 13th Biennial Baltic Electronics Conference : October 3-5, 2012, Tallinn, Estonia 2012 / p. 127-230

Pühapäeval käivitatakse küberkuu suure võistlus- ja messipäevaga

pealinn.ee 2023 [Pühapäeval käivitatakse küberkuu suure võistlus- ja messipäevaga](#)

QoT assessment of the optical spectrum as a service in disaggregated network scenarios

Kaeval, Kaida; Fehenberger, Tobias; Zou, Jim; Jansen, Sander Lars; Grobe, Klaus; Griesser, Helmut; Elbers, Jörg-Peter; **Tikas, Marko; Jervan, Gert** Journal of Optical Communications and Networking 2021 / art. 9436771, p. E1-E12
<https://doi.org/10.1364/JOCN.423530> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Research in digital design and test at Tallinn University of Technology

Ubar, Raimund-Johannes; Jervan, Gert; Jutman, Artur; Raik, Jaan; Ellervee, Peeter; Kruus, Margus Radioelectronics & informatics 2008 / p. 4-12 : ill <http://www.ewdtest.com/ri/%E2%84%96-1-40-january-march-2008/>

Research on digital system design and test at Tallinn University of Technology

Ubar, Raimund-Johannes; Ellervee, Peeter; Hollstein, Thomas; Jervan, Gert; Jutman, Artur; Kruus, Margus; Raik, Jaan Research in Estonia : present and future 2011 / p. 184-205 : ill

Respiration signal extraction from photoplethysmogram using pulse wave amplitude variation

Leier, Mairo; Jervan, Gert; Stork, Wilhelm 2014 IEEE International Conference on Communications (ICC) : Sydney, Australia, June 10-14, 2014 2014 / p. 3535-3540 : ill

Riigi AI-eksperdid : „Tehisaru aitaks arsti hoiatada ravimite koosmõju eest.“

Leitmaa, Dannar ohtuleht.ee 2024 [Riigi AI-eksperdid : „Tehisaru aitaks arsti hoiatada ravimite koosmõju eest.“](#)

Sardsüsteemide õppe- ja teaduslabor Tallinna Tehnikaülikooli arvutitehnika instituudis

Jervan, Gert E-õppe uudiskiri 2011 / lk. 9 : fot <http://uudiskiri.e-ope.ee>

Scalable open platform for reliable medical sensorics = Laiendatav avatud platvorm usaldusväärsete meditsiiniliste sensorite jaoks

Leier, Mairo 2016 https://www.ester.ee/record=b4575148*est

Scheduling framework for dependable NoC-based systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert Workshop Digest. Diagnostic Services in Network-on-Chips - Test, Debug, and On-Line Monitoring : DATE 2009 : Nice, France, 20-24 April, 2009 2009 / ? p <https://ieeexplore.ieee.org/document/5335670>

Scheduling framework for real-time dependable NoC-based systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert Proceedings of the 11th International Conference on System-on-chip : Tampere, Finland, October 05-07, 2009 2009 / p. 95-99 <https://ieeexplore.ieee.org/document/5335670/keywords#keywords>

Service margins for wide-band optical spectrum services implemented in long-haul Raman-enabled networks

Kaeval, Kaida; Patri, Sai Kireet; Pedreno-Manresa, Jose-Juan; Grobe, Klaus; Elbers, Jörg-Peter; Griesser, Helmut; Tikas, Marko; **Jervan, Gert** Optical Fiber Communications Conference and Exhibition, OFC 2023 : Proceedings 2023 / art. W3H.6
<https://doi.org/10.1364/OFC.2023.W3H.6>

A set of tools for estimating quality of built-in self-test in digital circuits

Jervan, Gert; Markus, Antti; Paomets, Priidu; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the International Symposium on Signals, Circuits and Systems, Iasi (Romania), October 2-3, 1997 1997 / p. 362-365

Sleep apnea pre-screening on neonates and children with shoe integrated sensors [Electronic resource]

Leier, Mairo; Jervan, Gert 31st Norchip Conference : Vilnius, Lithuania, 11-12 November 2013 : conference program and papers 2013 / [4] p. : ill [USB]

A small-size sleep apnea pre-screening prototype for neonates and children

Leier, Mairo; Jervan, Gert Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK seitsmenda aastakonverentsi artiklite kogumik : 15.-16. novembril 2013, Haapsalu 2013 / p. 49-52 : ill

Smart photoplethysmographic sensor for pulse wave registration at different vascular depths

Leier, Mairo; Jervan, Gert; Pilt, Kristjan Proceedings of the 8th Annual Conference of the Estonian National Doctoral School in Information and Communication Technologies : December 5-6, 2014, Rakvere 2014 / p. 67-70 : ill

Smart photoplethysmographic sensor for pulse wave registration at different vascular depths

Leier, Mairo; Pilt, Kristjan; Karai, Deniss; Jervan, Gert Conference proceedings : 37th Annual International Conference of the IEEE Engineering in Medicine and Biology Society : Milan, Italy, August 25-29 2015 2015 / p. 1849-1852 : ill
<https://doi.org/10.1109/EMBC.2015.7318741> [Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

SoCDep2 : a framework for dependable task deployment on many-core systems under mixed-criticality constraints

Azad, Siavoosh Payandeh; Niazmand, Behrad; Ellervee, Peeter; Raik, Jaan; Jervan, Gert; Hollstein, Thomas 11th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC) : June 27-29, 2016, Tallinn, Estonia 2016 / [6] p. : ill <https://doi.org/10.1109/ReCoSoC.2016.7533903>

SysML in systems engineering course

Kruus, Helena; Robal, Tarmo; Jervan, Gert 10th European Workshop on Microelectronics Education : EWME 2014 : May 14-16, 2014, Tallinn, Estonia 2014 / p. 177-181 : ill

System level design for dependability and reliability

Jervan, Gert CREDES Summer School : Dependable Systems Design : handouts 2011 / p. 5-20 : ill

System level design of dependable real-time Systems-on-Chip

Tagel, Mihkel; Jervan, Gert Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK kolmanda aastakonverentsi artiklite kogumik : 25.-26. aprill 2008, Voore külalistemaja 2008 / p. 77-80 : ill

System-level communication synthesis and dependability improvements for Network-on-Chip based systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert Estonian journal of engineering 2010 / 1, p. 23-38 : ill
https://artiklid.elnet.ee/record=b1964968*est

System-level design of NoC-based dependable embedded systems

Tagel, Mihkel; Ellervee, Peeter; Jervan, Gert Design and test technology for dependable systems-on-chip 2011 / p. 1-36 : ill
https://pld.ttu.ee/~raiub/BOOK_content/Section_1/Ch11_jervan/Ch11_jervan_.pdf

System-level design of timing-sensitive network-on-chip based dependable systems = Kiipvõrkudel põhinevate ajakriitiliste ja töökindlate süsteemide kõrgtaseme disain

Tagel, Mihkel 2012 https://www.ester.ee/record=b2778263*est

System-level optimization of NoC-based timing sensitive systems

Tagel, Mihkel; Ellervee, Peeter; Hollstein, Thomas; Jervan, Gert Estonian journal of engineering 2011 / 2, p. 158-168 : ill
https://artiklid.elnet.ee/record=b2422982*est

Systems engineering course as a backbone of the computer engineering curricula

Jervan, Gert Proceedings of the 24th International Conference on European Association for Education in Electrical and Information Engineering : 30-31 May 2013, Chania, Greece 2013 / p. 163-166 : ill

Tallinna tehnikaülikooli uus nägu

Oorn, Arvo; Jervan, Gert; Listra, Enn; Kanger, Tõnis Sirp 2017 / lk. 33-35 : portr <http://www.sirp.ee/s1-artiklid/c21-teadus/tallinna-tehnikaülikooli-uus-nagu/>

TalTech hakkab tegema koostööd juhtiva tehnoloogiaettevõttega

digi.geenius.ee 2024 [TalTech hakkab tegema koostööd juhtiva tehnoloogiaettevõttega](#)

TalTechDigital sai diginõukoja

Aaviksoo, Jaak; Gil, Gerlin; Helm, Marek; **Jervan, Gert; Krimmer, Robert Johannes;** Lackman, Seth; **Otto, Tauno;** Saar, Kirke; Sulling, Anne; **Tammet, Tanel** Mente et Manu 2017 / lk. 26-30 http://www.ester.ee/record=b1242496*est
https://artiklid.elnet.ee/record=b2830872*est

TalTechi aasta vilistlane, Testlio asutaja Kristel Kruustük: hirmutavad hetked suunavad õigele rajale

digi.geenius.ee 2023 [TalTechi aasta vilistlane, Testlio asutaja Kristel Kruustük: hirmutavad hetked suunavad õigele rajale](#)

TalTechi IT magistrikavad on mõeldud nii eksperdile, täiendõppijale kui ka karjääripöörjale

postimees.ee 2025 <https://www.postimees.ee/8214129/taltech-i-magistrikavad-on-moeldud-nii-ekspertile-taiendopijale-kui-ka-karjaari-poorajale>

Teaching embedded systems as a part of a computer engineering curricula

Jervan, Gert; Gorev, Maksim; Pesonen, Vadim 23rd EAEEIE annual conference, Cagliari, Italy, February 26-27, 2012 2012 / p. 1-4

Teaching modeling in SysML/UML and problems encountered

Kruus, Helena; Robal, Tarmo; Jervan, Gert Proceedings of the 25th International Conference on European Association for Education in Electrical and Information Engineering : 30 May-June 2014, Cesme, Türkiye 2014 / p. 33-36 : ill

Teaching test and design for testability with TURBO-TESTER software

Jervan, Gert; Markus, Antti; Paomets, Priidu; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the 3rd Workshop on Mixed Design of Integrated Circuits and Systems, Lodz, May 1996 1996 / p. 589-594

Teaduses ja äris on samad põhimõtted

Jervan, Gert Äripäev 2019 / lk. 12

Tehisintellekt - millest kogu see kära?

Jervan, Gert Mente et Manu 2023 / lk. 57-58 : portr https://www.ester.ee/record=b1242496*est

Tehisintellekt – millest kogu see kära?

Jervan, Gert digi.geenius.ee 2023 [Tehisintellekt – millest kogu see kära?](#)

Tehisintellekt on petturite elu veelgi lihtsamaks muutunud

Saarniit, Aleksandra Postimees 2023 / Lk. 6 ["Tehisintellekt on petturite elu veelgi lihtsamaks muutunud"](#)

Tehnikahuvilisi noori võiks rohkem olla : [intervjuu TTÜ 2008. a. parima noorteadlase Gert Jervaniga]

Jervan, Gert; Priilinn, Ketlin Inseneeria 2009 / lk. 36-37 : fot

Test cost minimization for hybrid BIST

Jervan, Gert; Peng, Zebo; Ubar, Raimund-Johannes IEEE International Symposium on Defect and Fault Tolerance in VLSI Systems : 25-27 October 2000, Yamanashi, Japan : proceedings 2000 / p. 283-298 : ill
<https://ieeexplore.ieee.org/abstract/document/887168>

Test generation : a hierarchical approach

Jervan, Gert; Ubar, Raimund-Johannes; Peng, Z.; Eles, Petru System-level test and validation of hardware/software systems 2005 / p. 67-81 : ill

Test time minimization for hybrid BIST of core-based systems

Jervan, Gert; Eles, Petru; Peng, Zebo; Ubar, Raimund-Johannes; Jenihhin, Maksim 12th Asian Test Symposium (ATS 2003) : 17-19 November 2003, Xian, China 2003 / p. 318-325 : ill <https://link.springer.com/article/10.1007/s11390-006-0907-x>

Test time minimization for hybrid BIST of core-based systems

Jervan, Gert; Eles, Petru; Peng, Zebo; Ubar, Raimund-Johannes; Jenihhin, Maksim Journal of computer science and technology 2006 / 6, p. 907-912 : ill <https://link.springer.com/article/10.1007/s11390-006-0907-x>

Test time minimization for hybrid BIST with test pattern broadcasting

Ubar, Raimund-Johannes; Jenihhin, Maksim; Jervan, Gert; Peng, Zebo IEEE NORCHIP 2003 : 21 Norchip Conference : Riga, Latvia, 10-11 November 2003 : proceedings 2003 / p. 112-116 : ill <https://www.ida.liu.se/labs/eslab/publications/pap/db/norchip03.pdf>

Towards formal verification of cache access-based side-channel attacks

Niazmand, Behrad; Reinbrecht, Cezar; Raik, Jaan; Jervan, Gert; Sepulveda, Johanna Testmethoden und Zuverlässigkeit von Schaltungen und Systemen, TUZ 2019 2019 / 2 p. : tab <http://www.informatik.uni-bremen.de/tuz/2019>

TTÜ aastal 2020 : töögrupi vahearuanne

Ernits, Juhan-Peep; Goi, Anna; Jervan, Gert; Leibak, Alar; Randma-Liiv, Tiina; Otto, Tauno; Rosin, Argo; Sarmiento Guerin, Maria Cecilia Mente et Manu 2009 / 16. veebr., lk. 4 : fot https://www.ester.ee/record=b1242496*est

TTÜ arengukonverents 8. juuni 2011 : ettekannete põhiteesid

Truve, Erkki; Tammemäe, Kalle; Varrak, Tea; Öpik, Andres; Jervan, Gert; Lopp, Margus; Elken, Jüri; Kattel, Rainer; Keerberg, Anne Mente et Manu 2011 / lk. 5 https://www.ester.ee/record=b1242496*est

TTÜ professor: võidujooks parema tehisintellekti nimel on varjutanud eetika

Oidermaa, Jaan-Juhan novaator.err.ee 2023 [TTÜ professor: võidujooks parema tehisintellekti nimel on varjutanud eetika](https://www.ester.ee/record=b1242496*est)

Tuhat eurot palgale juurde! Just nii palju rohkem teenid, kui astud selle ühe sammu!

Jervan, Gert arileht.delfi.ee 2024 [Tuhat eurot palgale juurde! Just nii palju rohkem teenid, kui astud selle ühe sammu!](https://www.ester.ee/record=b1242496*est)

Tunned huvi IT-erialade vastu, kuid pole valikutes veel kindel? Tee väike test!

delfi.ee 2024 [Tunned huvi IT-erialade vastu, kuid pole valikutes veel kindel? Tee väike test!](https://www.ester.ee/record=b1242496*est)

Turbo tester : a CAD system for teaching digital test

Jervan, Gert; Markus, Antti; Paomets, Priidu; Raik, Jaan; Ubar, Raimund-Johannes Microelectronics education : proceedings of the 2nd European Workshop held in Noordwijkerhout, The Netherlands, 14-15 May 1998 1998 / p. 287-290: ill https://link.springer.com/chapter/10.1007/978-94-011-5110-8_66

Üitmõtteid akadeemilisest juhtimisest : [arvamuslugu]

Jervan, Gert Mente et Manu 2013 / lk. 15 : fot https://artiklid.elnet.ee/record=b2619496*est

Understanding MPSoCs : exploiting memory microarchitectural vulnerabilities of high performance NoC-based MPSoCs

Sepulveda, Johanna; **Azad, Siavoosh Payandeh; Niazmand, Behrad; Jervan, Gert** SAMOS '18 : Proceedings of the 18th International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation, Pythagorion, Greece, July 15-19, 2018 2018 / p. 162-166 <https://doi.org/10.1145/3229631.3239367> [Conference proceedings at Scopus](https://www.scopus.com/article/record/10.1145/3229631.3239367) [Article at Scopus](https://www.scopus.com/article/record/10.1145/3229631.3239367) [Article at WOS](https://www.scopus.com/article/record/10.1145/3229631.3239367)

Using Tabu Search for optimization of memory-constrained hybrid BIST

Kruus, Helena; Jervan, Gert; Ubar, Raimund-Johannes Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK kolmanda aastakonverentsi artiklite kogumik : 25.-26. aprill 2008, Voore külalistemaja 2008 / p. 85-88 : ill

Using Tabu Search for optimization of memory-constrained hybrid BIST

Kruus, Helena; Jervan, Gert; Ubar, Raimund-Johannes BEC 2008 : 2008 International Biennial Baltic Electronics Conference : proceedings of the 11th Biennial Baltic Electronics Conference : Tallinn University of Technology : October 6-8, 2008, Tallinn, Estonia 2008 / p. 155-158 : ill

Using Tabu search method for optimizing the cost of hybrid BIST

Kruus, Helena; **Ubar, Raimund-Johannes; Jervan, Gert**; Peng, Z. XVI Conference on Design of Circuits and Integrated Systems : Porto, Portugal, 2001 2001 / p. 445-450 <https://citeseerx.ist.psu.edu/document?repid=rep1&type=pdf&doi=e97bb394ff71aa0affcc5fb372404bbc246888a8>

Valgetähe saanud Gert Jervan : tehisaru muudab õpioskused üha tähtsamaks

Jervan, Gert novaator.err.ee 2025 [Valgetähe saanud Gert Jervan : tehisaru muudab õpioskused üha tähtsamaks](https://www.ester.ee/record=b5306225*est)

VHDL based test generation system

Jervan, Gert; Markus, Antti; Raik, Jaan; Ubar, Raimund-Johannes Proceedings of the 5th Electronic Devices and Systems Conference, Brno, June 11-12, 1998 1998 / p. 145-148

Villiko Nurmoja selgitab, miks Eesti vajab AI-d ja relvi: oleme väikeriik, kelle hoovis "õunavargil" ei käida

geenius.ee 2024 [Villiko Nurmoja selgitab, miks Eesti vajab AI-d ja relvi: oleme väikeriik, kelle hoovis "õunavargil" ei käida](https://www.ester.ee/record=b5306225*est)

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Эстонский Экспресс 2024 / С. 22-26 https://www.ester.ee/record=b5306225*est