

Evaluating NTT/INTT implementation styles for post-quantum cryptography

Imran, Malik; Khan, Safiullah; Khalid, Ayesha; Rafferty, Ciara; Ali Shah, Yasir; Pagliarini, Samuel; Rashid, Muhammad; O'Neill, Maire
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Fast iterative circuits and RAM-based mergers to accelerate data sort in software/hardware systems

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2017 / p. 323-335 : ill <https://doi.org/10.3176/proc.2017.3.07> http://www.eester.ee/record=b2355998*est [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)