

Optimal design of electrodynamic delay lines

Gurskas, Antanas; Kirvaitis, Raimundas; Šileikis, Arturas BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 373-376: ill

Pulse generation in a superconducting microstrip line

Costa, Luis R.J.; Seppä, Heikki; **Valtonen, Martti** BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 443-446: ill

Realization of nonlinear time-delay input–output equations

Kaldmäe, Arvo; Kotta, Ülle; Tönso, Maris IEEE Control Systems Letters 2018 / p. 369-374

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