

**Network-based hardware accelerators for parallel data processing = Võrgupõhised riistvarakirendid paralleelseks andmetöötuseks**

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**Parallel processing in FPGA-based digital circuits and systems**

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**Pipelined execution of data-parallel algorithms**

Gorev, Maksim; Ubar, Raimund-Johannes BEC 2014 : 2014 14th Biennial Baltic Electronics Conference : proceedings of the 14th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 6-8, 2014, Tallinn, Estonia 2014 / p. 109-112 : ill

**Relation learning with bar charts**

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**See mitmetahuline rööpsus**

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**3D parallel fault simulation**

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