

A new measure for calculating multiple fault coverage of microprocessor self-test

Oyeniran, Adeboye Stephen; Odozi, Uzochukwu Eddie; Ubar, Raimund-Johannes BEC 2016 : 2016 15th Biennial Baltic Electronics Conference : proceedings of the 15th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 3-5, 2016, Tallinn, Estonia 2016 / p. 75-78 : ill http://www.ester.ee/record=b2150914*est

Aasta 2000 on probleem ka Eestis

Kirt, Toomas; Tepandi, Jaak Infotehnoloogia haldusjuhtimises : aastaraamat 1998 1999 / lk. 18-22

Aasta 2000 probleemist ja ühilduvusest

Kirt, Toomas; Tepandi, Jaak A & A 1999 / 1, lk. 46-50

Acceleration of recursive data sorting over tree-based structures

Mihhailov, Dmitri; Sudnitsõn, Aleksander; Sklyarov, Valery; Skliarova, Iouliia Elektronika ir elektrotehnika = Electronics and electrical engineering 2011 / p. 51-56 : ill <https://eejournal.ktu.lt/index.php/elt/article/view/612>

Adapting the auto to a new tune

Helander, Johannes; Preden, Jürjo-Sören 1st Workshop on Models and Analysis for Automotive Systems : RTSS 2006 : The 27th IEEE Real-Time Systems Symposium : 5-8 December 2006, Rio de Janeiro, Brazil 2006 / p. 21-24 <https://www.microsoft.com/en-us/research/publication/adapting-the-auto-to-a-new-tune/>

Address-based data processing over N-ary trees

Sklyarov, Valery; Skliarova, Iouliia; Kruus, Margus; Mihhailov, Dmitri; Sudnitsõn, Aleksander EuroCon 2013 : 01-04 July 2013, Zagreb, Croatia 2013 / p. 1790-1797 : ill

Advanced Hardware Protection Mechanisms : A Study on Logic Locking and Circuit Obfuscation Techniques

[Võrguteavik] = Täiustatud riistvara kaitsemehhanismid : uuring loogikalukustamise ja hägustamise tehnikate kohta
Costa de Almeida, Antonio Felipe 2025 https://www.ester.ee/record=b5752171*est <https://digikogu.taltech.ee/et/Item/9840306f-39a2-4ac7-b275-9a841a75857b> <https://doi.org/10.23658/taltech.42/2025>

Advances in Model and Data Engineering in the Digitalization Era : MEDI 2021 International Workshops : DETECT, SIAS, CSMML, BIOC, HEDA, Tallinn, Estonia, June 21–23, 2021 : proceedings

2021 <https://doi.org/10.1007/978-3-030-87657-9>

An optimization framework for dynamic pipeline management in computing systems

Naqvi, Syed Rameez; Zahid, Anjum; Sawalha, Lina; Jenihhin, Maksim Computers & electrical engineering 2019 / p. 242-258 : ill <https://doi.org/10.1016/j.compeleceng.2019.07.013> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Andmetöötuse riistvara

Agur, Ustus 1976 https://www.ester.ee/record=b1287490*est

Application of high-level decision diagrams for simulation-based verification tasks

Jenihhin, Maksim; Raik, Jaan; Tšepurov, Anton; Ubar, Raimund-Johannes Estonian journal of engineering 2010 / 1, p. 56-77 : ill

Application-specific hardware accelerator for implementing recursive sorting algorithms

Mihhailov, Dmitri; Sklyarov, Valery; Skliarova, Iouliia; **Sudnitsõn, Aleksander** Proceedings of the IEEE International Conference on Field Programmable Technology (FPT'10) : Beijing, China Dec. 8-10, 2010 2010 / p. 269-272 : ill <https://ieeexplore.ieee.org/document/5681486>

APPRAISER : DNN fault resilience analysis employing approximation errors

Taheri, Mahdi; Ahmadilivani, Mohammad Hasan; Jenihhin, Maksim; Daneshtalab, Masoud; Raik, Jaan 2023 26th International Symposium on Design and Diagnostics of Electronic Circuits and Systems (DDECS) 2023 / p. 124–127 : ill <https://ddecs2023.taltech.ee/> <https://doi.org/10.1109/DDECS57882.2023.10139468>

Approaches to extra-functional verification of security and reliability aspects in hardware designs = Riistvaraprojektide turva- ja töökindlusaspektide ekstrasfunktsionaalse verifitseerimise lähenemisviisid

Lai, Xinhui 2022 <https://doi.org/10.23658/taltech.29/2022> <https://digikogu.taltech.ee/et/Item/cff1aeb9-b0b2-49ce-b81a-bfb9dc25fd56> https://www.ester.ee/record=b5502807*est

Area efficient design and implementation of a novel divider circuit block = Uudne efektiivne jagamistehte riistvaraline realiseerimine

Patankar, Udayan Sunil 2025 https://www.ester.ee/record=b5746970*est <https://digikogu.taltech.ee/et/Item/bada7a0d-f54f-48bb-844d-37e23ad8d029> <https://doi.org/10.23658/taltech.29/2025>

Areeba : an area efficient binary huff-curve architecture

Sajid, Asher; Rashid, Muhammad; Jamal, Sajjad Shaukat; Imran, Malik; Alotaibi, Saud S.; Sinky, Mohammed H. Electronics

(Switzerland) 2021 / art. 1490 <https://doi.org/10.3390/electronics10121490> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

ARTmine : automatic association rule mining with temporal behavior for hardware verification

Heidari Iman, Mohammad Reza; Jervan, Gert; Ghasempouri, Tara 2024 Design, Automation & Test in Europe Conference & Exhibition (DATE) : Valencia, Spain, 25-27 March 2024 : proceedings 2024 / p. 1-6 : ill <https://ieeexplore.ieee.org/document/10546742>

Arvuti riistvarasse peidetud troojalane pakub häkkeritele hõlbuelu [Võrguväljaanne]

Oidermaa, Jaan-Juhan novaator.err.ee 2022 ["Arvuti riistvarasse peidetud troojalane pakub häkkeritele hõlbuelu"](#)

Arvutid nööpi

Sinivee, Veljo Praktiline Arvutikasutaja 2006 / 3, lk. 33-35 : ill https://artiklid.elnet.ee/record=b1053463*est

Arvutitehnika riistvara : õpik kõrgkoolidele

Evertson, Teet 2013 https://www.ester.ee/record=b2967116*est

Assessment and Enhancement of Hardware Reliability for Deep Neural Networks = Riistvara töökindluse hindamine ja täiustamine süvanärvivõrkude jaoks

Ahmadilivani, Mohammad Hasan 2025 https://www.ester.ee/record=b5739227*est <https://digikogu.taltech.ee/et/item/652d50e3-773f-4de5-897d-86531feb0d56> <https://doi.org/10.23658/taltech.19/2025>

Autonoomsed andmelogierid

Toomsalu, Arvo A & A 2008 / 1, lk. 11-28 : ill https://artiklid.elnet.ee/record=b1021439*est

Avatud lähtekood ja riistvara

Rebane, Jaan A & A 2009 / 6, lk. 9-17

Benchmarking advanced security closure of physical layouts

Eslami, Mohammad; Knechtel, Johann; Sinanoglu, Ozgur; Karri, Ramesh; Pagliarini, Samuel Nascimento ISPD '23 : proceedings of the 2023 International Symposium on Physical Design 2023 / p. 256-264 <https://doi.org/10.1145/3569052.3578924> <https://dl.acm.org/doi/pdf/10.1145/3569052.3578924>

Bottlenecks in hardware design and design automation [Electronic resource] : [slides]

Ellervee, Peeter Design and Test Technology for Dependable Hardware/Software Systems : DEDIS/DAAD Summer Academy : BTU Cottbus, Sept. 1st-12th, 2008 2008 / [24] p. : ill. [CD-ROM]

Bottlenecks in hardware design and design automation (Hardware synthesis: no pain, no gain)

Ellervee, Peeter CREDES Summer School : Dependable Systems Design : handouts 2011 / p. 49-58 : ill

Build-a-Thon Energy ehk Riistvarahäkaton, mis panustab paremasse homsesse

Mossin, Eneli Studioosus 2024 / lk. 26-27 : fot https://www.ester.ee/record=b1558644*est

CAC 2.0 : a corrupt and correct logic locking technique resilient to structural analysis attacks

Aksoy, Levent; Yasin, Muhammad; Pagliarini, Samuel Nascimento arXiv.org 2024 / 6 p. : ill <https://doi.org/10.48550/arXiv.2401.07142>

CAC 2.0 : a corrupt and correct logic locking technique resilient to structural analysis attacks

Aksoy, Levent; Yasin, Muhammad; Pagliarini, Samuel Nascimento 2024 IEEE 25th Latin American Test Symposium (LATS) : 9-12 April 2024 (2024) 2024 <https://doi.org/10.1109/LATS62223.2024.10534592> [Article at Scopus](#)

Calculation of LFSR seed and polynomial pair for BIST applications

Jutman, Artur; Tšertov, Anton; Ubar, Raimund-Johannes Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK kolmanda aastakonverentsi artiklite kogumik : 25.-26. aprill 2008, Voore külalistemaja 2008 / p. 105-108 : ill

Can 3D printing bring droplet microfluidics to every lab? - A systematic review

Gyimah, Nafisat; Scheler, Ott; Rang, Toomas; Pardy, Tamas Micromachines 2021 / art. 339 <https://doi.org/10.3390/mi12030339> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Can a HW development and research environment be convenient, scalable and free? zamiaCAD : open-source platform for hardware design and analysis : [invited talk]

Jenihhin, Maksim Proceedings of IEEE East-West Design & Test Symposium (EWDTS'2012) : Kharkov, Ukraine, September 14-17, 2012 2012 / p. 548-549

Capillary electrophoresis as a monitoring tool for flow composition determination

Kaljurand, Mihkel; Saar-Reismaa, Piret; Vaher, Merike; Gorbatsšova, Jelena; Mazina-Šinkar, Jekaterina Molecules 2021 / art.

4918, 12 p. : ill <https://doi.org/10.3390/molecules26164918> Journal metrics at Scopus Article at Scopus Journal metrics at WOS Article at WOS

Celeron

Toomsalu, Arvo Arvutimaailm 1998 / 7, lk. 47-48 https://artiklid.elnet.ee/record=b2003517*est

Choosing an expert system shell

Tepandi, Jaak; Parmakson, Priit Data processing, compiler writing, problems of programming 1989 / p. 28-32

Clock manipulation for heterogeneous emulation environment

Ellervee, Peeter; Arhipov, Anton; Tammemäe, Kalle Proceedings [of] 24th IEEE Norchip Conference : Linköping, Sweden, 20-21 November 2006 2006 / p. 213-216 : ill <https://ieeexplore.ieee.org/abstract/document/4126984>

Compact smart home systems : design and verification of cost effective hardware solutions

Kuusik, Alar 2001 http://www.ester.ee/record=b1622134*est

Compact smart home systems : design and verification of cost effective hardware solutions

Kuusik, Alar 2001 http://www.ester.ee/record=b1578112*est

Control intensive digital system synthesis

Tammemäe, Kalle 1997 http://www.ester.ee/record=b1060033*est

Cost-effective concurrent hardware checkers for network on chip based system on chip = Kulutõhusad süsteemiga paralleelsed rikkemonitorid kiipvõrkudel põhinevatele kiipsüsteemidele

Hariharan, Ranganathan 2019 <https://digi.lib.ttu.ee/i/?12854> https://www.ester.ee/record=b5243161*est

Cost-effective fault tolerance for CNNs using parameter vulnerability based hardening and pruning

Ahmadilivani, Mohammad Hasan; Mousavi, Seyedhamidreza; Raik, Jaan; Daneshtalab, Masoud; Jenihhin, Maksim 2024 IEEE 30th International Symposium on On-line Testing and Robust System Design (IOLTS) : IOLTS 2024 : July 03rd-05th 2024, Rennes, Brittany, France 2024 / 7 p <https://doi.org/10.1109/IOLTS60994.2024.10616072> Article at Scopus Article at WOS

Cross-layer dependability management in network on chip based system on chip = Kiipvõrkudel põhinevate süsteemide kihtideülene usaldatavuse haldus

Azad, Siavoosh Payandeh 2018 <https://digi.lib.ttu.ee/i/?9948> https://www.ester.ee/record=b5056143*est

Databases and Information Systems : 14th International Baltic Conference, DB&IS 2020, Tallinn, Estonia, June 16-19, 2020 : Proceedings

2020 <https://doi.org/10.1007/978-3-030-57672-1>

Data-driven cross-layer fault management architecture for sensor networks

Vihman, Lauri; Kruusmaa, Maarja; Raik, Jaan 16th European Dependable Computing Conference : EDCC 2020 : Virtual Conference, Munich, Germany, 7-10 September 2020 : proceedings 2020 / art. 20094188, p. 33-40 <https://doi.org/10.1109/EDCC51268.2020.00015>

DeepVigor: Vulnerability Value RanGes and FactORs for DNNs' Reliability Assessment

Ahmadilivani, Mohammad Hasan; Taheri, Mahdi; Raik, Jaan; Daneshtalab, Masoud; Jenihhin, Maksim IEEE European Test Symposium (ETS) : Venice, Italy, 22-26 May 2023 : proceedings 2023 / 6 p. : ill <https://doi.org/10.1109/ETS56758.2023.10174133>

Determined-safe faults identification : a step towards ISO26262 hardware compliant designs

Augusto da Silva, Felipe; Bagbaba, Ahmet Cagri; Sartoni, Sandro; Cantoro, Riccardo; Sonza Reorda, Matteo; Hamdioui, Said; Sauer, Christian 2020 25th IEEE European Test Symposium (ETS) 2020 / 6 p. : ill <https://doi.org/10.1109/ETS48528.2020.9131568>

Development of a power electronics controller with RISC-V based core for security-critical applications

Swakath, S. U.; Kshirsagar, Abhijit; Kondepu, Koteswararao; Banavath, Satish Naik; Chub, Andrii; Vinnikov, Dmitri 2022 IEEE 63th International Scientific Conference on Power and Electrical Engineering of Riga Technical University (RTUCON): conference proceedings 2022 / p. 1-5 <https://doi.org/10.1109/RTUCON56726.2022.9978737>

DICE - an interactive approach to hardware/software co-design of heterogeneous real-time systems

Hollstein, Thomas; Becker, J.; Kirschbaum, A.; Renner, F.-M.; Glesner, M. BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 97-100

Digitaalrõutite operatsioonelemendid ja aritmeetikaseadmed : õppevahend

Mägi, Harri 1978 https://www.ester.ee/record=b1274927*est

Digitaalsüsteemide diagnostika

Ubar, Raimund-Johannes 2005 http://www.ester.ee/record=b2097071*est

Digital hardware organization course for SoC program

Ellervee, Peeter; Tenhunen, Hannu 2001 International Conference on Microelectronic Systems Education : June 17-18, 2001, Las Vegas, Nevada, USA : proceedings 2001 / p. 26-27 <https://ieeexplore.ieee.org/document/932402>

Division Method And Circuit

Patankar, Udayan Sunil; Koel, Ants; Patankar, Sunil; Flores, Miguel E. 2022 <https://www.wipo.int>

Eesti aparaadiehituse osa seismoloogia riistvara arengus

Hendre, Enn Insenerikultuur Eestis. 3 1997 / lk. 19-27: ill https://www.ester.ee/record=b1063622*est

Eestis ainulaadne TalTechi õppekava liidab kokku riistvara arenduse ja programmeerimise

delfi.ee 2024 [Eestis ainulaadne TalTechi õppekava liidab kokku riistvara arenduse ja programmeerimise](#)

An efficient analog convolutional neural network hardware accelerator enabled by a novel memoryless architecture for insect-sized robots

Dadras, Iman; Ahmadilivani, Mohammad Hasan; Banerji, Saoni; Raik, Jaan; Abloo, Alvo 2022 11th International Conference on Modern Circuits and Systems Technologies (MOCASST) : Bremen, Germany : 08-10 June 2022 2022 / p. 1-6 <https://doi.org/10.1109/MOCASST54814.2022.9837551>

Efficient Deep Learning Model Optimization for Resource Constrained Devices = Tõhus süvaõppe mudeli optimeerimine piiratud ressurssidega seadmete jaoks

Ademola, Olutosin Ajibola 2025 https://www.ester.ee/record=b5750529*est <https://digikogu.taltech.ee/et/Item/183f6305-2924-4e30-a7cc-d06ac86581b8> <https://doi.org/10.23658/taltech.37/2025>

An efficient non-inverting buck-boost converter with improved step up/down ability

Abdelrahim Abdelghafour, Omar Mohamed; Chub, Andrii; Blinov, Andrei; Vinnikov, Dmitri; Pefitsis, Dimosthenis Energies 2022 / art. 4550 <https://doi.org/10.3390/en15134550> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

EFIC-ME : a fast emulation based fault injection control and monitoring enhancement

Abideen, Zain Ul; Rashid, Muhammad Haroon IEEE Access 2020 / p. 207705-207716 <https://doi.org/10.1109/ACCESS.2020.3038198> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Elektroonikaettevõtte premeerib kahte IT-tudengit 2500-eurose stipendiumiga [Võrguväljaanne]

goodnews.ee 2021 ["Elektroonikaettevõtte premeerib kahte IT-tudengit 2500-eurose stipendiumiga"](#)

Elliptic-curve crypto processor for RFID applications

Rashid, Muhammad; Jamal, Sajjad Shaukat; Khan, Sikandar Zulqarnain; Alharbi, Adel R.; Aljaedi, Amer; Imran, Malik Applied Sciences (Switzerland) 2021 / art. 7079 <https://doi.org/10.3390/app11157079> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Enhancing assertion-based verification in hardware designs through data mining algorithms = Andmekaeve algoritmide kasutamine riistvarasüsteemide väidete-põhise verifitseerimise parendamiseks

Heidari Iman, Mohammad Reza 2024 <https://digikogu.taltech.ee/et/Item/622f3660-e890-454d-aa85-7cc505557f77> <https://doi.org/10.23658/taltech.37/2024> https://www.ester.ee/record=b5694015*est

Environmentally adaptive fish or no-fish classification for river video fish counters using high-performance desktop and embedded hardware

Soom, Jürgen; Pattanaik, Vishwajeet; Leier, Mairo; Tuhtan, Jeffrey Andrew Ecological Informatics 2022 / art. 101817, 14 p. : ill <https://doi.org/10.1016/j.ecoinf.2022.101817> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Evaluating architectural, redundancy, and implementation strategies for radiation hardening of FinFET integrated circuits

Pagliarini, Samuel Nascimento; Benites, Luis; Martins, Mayler; Rech, Paolo; Kastensmidt, Fernanda IEEE transactions on nuclear science 2021 / p. 1045-1053 <https://doi.org/10.1109/TNS.2021.3070643> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Evaluating fault emulation on FPGA

Ellervee, Peeter; Raik, Jaan; Tihomirov, Valentin; Tammemäe, Kalle Field-Programmable Logic and Applications : 14th International Conference, FPL 2004 : Antwerp, Belgium, August 30-September 1, 2004 : proceedings 2004 / p. 354-363 : ill https://link.springer.com/chapter/10.1007/978-3-540-30117-2_37

Evaluating NTT/INTT implementation styles for post-quantum cryptography

Imran, Malik; Khan, Safiullah; Khalid, Ayesha; Rafferty, Ciara; Ali Shah, Yasir; Pagliarini, Samuel; Rashid, Muhammad; O'Neill, Maire IEEE Embedded Systems Letters 2024 / p. 485 - 488 <https://doi.org/10.1109/LES.2024.3410516> [Journal metrics at Scopus](#) [Article at](#)

[Scopus Journal metrics at WOS](#) [Article at WOS](#)

Evaluation of deep neural network compression methods for edge devices using weighted score-based ranking scheme
Ademola, Olutosin Ajibola; Leier, Mairo; Petlenkov, Eduard Sensors 2021 / art. 7529 <https://doi.org/10.3390/s21227529> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Experience in increase of practical hours for HDL course

Reinsalu, Uljana; Ellervee, Peeter 2011 International Conference on Microelectronic Systems Education (MSE '11), 5-6 June 2011, San Diego, California 2011 / p. 102-105 <https://ieeexplore.ieee.org/document/5937104>

Exploiting data transfer locality in memory mapping

Ellervee, Peeter; Miranda, Miguel; Cattoor, Francky; Hemani, Ahmed 25th EUROMICRO conference : Informatics : Theory and Practice for the New Millennium : Milan, Italy, September 8-10, 1999 : proceedings. Volume I 1999 / p. 14-21 : ill <https://ieeexplore.ieee.org/document/793132>

Exploration of Activation Fault Reliability in Quantized Systolic Array-Based DNN Accelerators

Taheri, Mahdi; Cherezova, Natalia; Ansari, Mohammad Saeed; **Jenihhin, Maksim**; Mahani, Ali; **Daneshtalab, Masoud; Raik, Jaan** 25th International Symposium on Quality Electronic Design (ISQED) 2025 / 8 p. : ill <https://ieeexplore.ieee.org/stamp/stamp.jsp?arnumber=10528372>

Fast iterative circuits and RAM-based mergers to accelerate data sort in software/hardware systems

Sklyarov, Valery; Skliarova, Iuliia; **Rjabov, Artjom; Sudnitsön, Aleksander** Proceedings of the Estonian Academy of Sciences 2017 / p. 323-335 : ill <https://doi.org/10.3176/proc.2017.3.07> http://www.ester.ee/record=b2355998*est [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Fault detection and protection system for the power converters with high-voltage IGBTs [Electronic resource]

Vinnikov, Dmitri; Roasto, Indrek; Lehtla, Tõnu ICECS 2008 : The 15th IEEE International Conference on Electronics, Circuits and Systems : August 31 - September 3, 2008, Malta 2008 / p. 922-925 : ill. [CD-ROM] <https://ieeexplore.ieee.org/document/4675005>

Fault simulation and code coverage analysis of RTL designs using high-level decision diagrams = Rikete simuleerimine ja koodikatte analüüs register-siirde tasemel kasutades kõrgtaseme otsustusdiagramme

Reinsalu, Uljana 2013 https://www.ester.ee/record=b2963595*est

A fault-resistant architecture for AES S-box architecture

Taheri, Mahdi; Sheikhpour, Saeideh; Ansari, Mohammad Saeed; Mahani, Ali Journal of Applied Research in Electrical Engineering 2021 / p. 86-92 <https://doi.org/10.22055/jaree.2021.36230.1020>

Feature article: Firmware updating systems for nanosatellites

Sünter, Indrek; Slavinskis, Andris; Kvell, Urmas; **Vahter, Andres**; Kuuste, Henri; Noorma, Mart; Kutt, Johan; Vendt, Riho; Tarbe, Karl; Pajusalu, Mihkel; Veske, Mihkel; Ilves, Taavi IEEE Aerospace and Electronic Systems Magazine 2016 / p. 36 - 44 <https://doi.org/10.1109/MAES.2016.150162> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Four years of System-on-Chip curricula

Kruus, Margus; Ellervee, Peeter EWME 2006 proceedings : 6th International Workshop on Microelectronics Education : 8-9 June, 2006, Stockholm, Sweden 2006 / p. 88-91

FPGA-based fault emulation of synchronous sequential circuits

Ellervee, Peeter; Raik, Jaan; Tammemäe, Kalle; Ubar, Raimund-Johannes IET computers and digital techniques 2007 / 2, p. 70-76 : ill <https://ieeexplore.ieee.org/abstract/document/1423822>

From RTL liveness assertions to cost-effective hardware checkers

Hariharan, Ranganathan; Ghasempouri, Tara; Niazmand, Behrad; Raik, Jaan XXXIII Conference on Design of Circuits and Integrated Systems (DCIS) : proceedings 2018 / 6 p. : ill <https://doi.org/10.1109/DCIS.2018.8681487>

Fully-fusible convolutional neural networks for end-to-end fused architecture with FPGA implementation

Dadras, Iman; Seydi, Sakineh; **Ahmadilivani, Mohammad Hasan; Raik, Jaan**; Salehi, Mostafa E. 2023 30th IEEE International Conference on Electronics, Circuits and Systems (ICECS) 2023 / 5 p. <https://doi.org/10.1109/ICECS58634.2023.10382831>

Gait analysis based approach for Parkinson's disease modeling with decision tree classifiers

Krajuškina, Anna; Nömm, Sven; Toomela, Aaro 2018 IEEE International Conference on Systems, Man, and Cybernetics : SMC 2018, 7-10 October 2018 Miyazaki, Japan 2018 / p. 3720-3725 : ill <https://doi.org/10.1109/SMC.2018.00630>

A generic scheme for communication representation and mapping

Meincke, Thomas; Jantsch, Axel; **Ellervee, Peeter**; Hemani, Ahmed; Tenhunen, Hannu 17th NORCHIP Conference : Oslo, Norway, 8-9 November 1999 : proceedings 1999 / p. 334-339 : ill https://www.researchgate.net/publication/2598862_A_Generic_Scheme_for_Communication_Representation_and_Mapping

Gert Jervan: TalTechi uurimisrühma tulemusel on Eesti kui digiriigi eestvedaja paremini kaitstud

Kald, Indrek ituudised.ee 2023 [Gert Jervan: TalTechi uurimisrühma tulemusel on Eesti kui digiriigi eestvedaja paremini kaitstud](#)

Google'i klasteri arhitektuur

Sullin, Anti A & A 2006 / 6, lk. 9-17 https://artiklid.elnet.ee/record=b1019894*est

Hardware and software for expert and predictive estimations of ecological loads

Shyshkin, A.I.; Zhukov, K.G.; Sytin, V.P. Theses of the reports of the VIII Symposium Concerning the Problems of Waterbodies Water Quality, Tallinn, Oct. 23-25, 1990 1990 / p. 71-72

Hardware implementation of face recognition using low precision representation

Dwivedi, Sai Kumar; **Azad, Siavoosh Payandeh; Ellervee, Peeter**; Dash, Ratnakar BEC 2016 : 2016 15th Biennial Baltic Electronics Conference : proceedings of the 15th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 3-5, 2016, Tallinn, Estonia 2016 / p. 63-66 : ill http://www.ester.ee/record=b2150914*est

Hardware implementation of recursive algorithms

Mihhailov, Dmitri; Sklyarov, Valery; Skliarova, Iuliia; **Sudnitsõn, Aleksander** 53rd IEEE International Midwest Symposium on Circuits and Systems : Seattle, Washington, USA, August 1-4, 2010 : proceedings 2010 / p. 225-228 <https://ieeexplore.ieee.org/document/5548674>

Hardware implementation of recursive sorting algorithms

Mihhailov, Dmitri; Sklyarov, Valery; Skliarova, Iuliia; **Sudnitsõn, Aleksander** 2011 International Conference on Electronic Devices, Systems and Applications (ICEDSA) : Kuala Lumpur, Malaysia, April 25-27, 2011 : [proceedings] 2011 / p. 33-38 : ill <https://ieeexplore.ieee.org/document/5959040>

Hardware implementation of recursive sorting algorithms using tree-like structures and HFSM models = Rekursiivsete sortimisalgoritmide riistvaraline realiseerimine kasutades puulaadseid struktuure ja HFSM mudeleid

Mihhailov, Dmitri 2011 https://www.ester.ee/record=b2748823*est

Hardware in Estonia

Kilvits, Kaarel Industrial restructuring and economic recovery in the Baltic Countries - Lithuania, Latvia, Estonia : infrastructure policies for sustained growth in the Baltic Countries : final report 1998 / p. 650-665

Hardware modeling for design verification and debug = Riistvara modelleerimine disaini verifitseerimise ja silumise jaoks

Tšepurov, Anton 2013 https://www.ester.ee/record=b2963501*est

Hardware obfuscation of digital FIR filters

Aksoy, Levent; Hepp, Alexander; Baehr, Johanna; **Pagliarini, Samuel Nascimento** 2022 25th International Symposium on Design and Diagnostics of Electronic Circuits and Systems (DDECS) : Prague, Czech Republic : April 6-8, 2022 : proceedings 2022 / p. 68-73 <https://doi.org/10.48550/arXiv.2202.10022> <https://doi.org/10.1109/DDECS54261.2022.9770141>

Hardware realization of lattice-based post-quantum cryptography = Võrel põhinev post-kvant-krüptograafia riistvaraline realiseerimine

Imran, Malik 2023 https://www.ester.ee/record=b5571216*est <https://doi.org/10.23658/taltech.33/2023> <https://digikogu.taltech.ee/et/Item/75aeb070-cb8b-4511-beaf-cbea3fca147d> https://www.ester.ee/record=b5571216*est

Hardware trojan insertion in finalized layouts : from methodology to a silicon demonstration

Perez, Tiago Diadami; **Pagliarini, Samuel Nascimento** IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems 2023 / p. 2094-2107 <https://doi.org/10.1109/TCAD.2022.3223846> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Hardware Trojans for confidence reduction and misclassifications on neural networks

Grailoo, Mahdieh; **Leier, Mairo**; **Pagliarini, Samuel Nascimento** Proceedings Of The Twenty Third International Symposium On Quality Electronic Design (ISQED 2022) 2022 / art. 180541, p. 230-235 <https://doi.org/10.1109/ISQED54688.2022.9806246>

Hardware/software co-design : state-of-the-art and future directions

Glesner, Manfred; Hollstein, Thomas; Gasteier, Michael; Münch, Michael BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 9-16: ill

Hardware/Software co-design in practice : MEMOCODE'08 contest experience

Reinsalu, Uljana; **Devadze, Sergei**; **Jutman, Artur**; **Tšertov, Anton**; **Ellervee, Peeter** Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK kolmanda aastakonverentsi artiklite kogumik : 25.-26. aprill 2008, Voore külalistemaja 2008 / p. 55-58 : ill

Hardware-based systems for partial sorting of streaming data

Rjabov, Artjom BEC 2016 : 2016 15th Biennial Baltic Electronics Conference : proceedings of the 15th Biennial Baltic Electronics

Conference : Tallinn University of Technology, October 3-5, 2016, Tallinn, Estonia 2016 / p. 59-62 : ill
http://www.ester.ee/record=b2150914*est

HDL-s for students with different background

Reinsalu, Uljana; Arhipov, Anton; Evertson, Teet; Ellervee, Peeter Proceedings MSE 2007 : 2007 IEEE International Conference on Microelectronic Systems Education : 3-4 June 2007, San Diego, CA 2007 / p. 69-70
<https://ieeexplore.ieee.org/document/4231454>

Heterogeneous Approximation of DNN HW Accelerators based on Channels Vulnerability

Cherezova, Natalia; Pappalardo, Salvatore; Taheri, Mahdi; Ahmadilivani, Mohammad Hasan; Deveautour, Bastien; Bosio, Alberto; Raik, Jaan; Jenihhin, Maksim 2024 IFIP/IEEE 32nd International Conference on Very Large Scale Integration (VLSI-SoC) 2024 / 4 p. : ill <https://doi.org/10.1109/VLSI-SoC62099.2024.10767798>

High-level decision diagram simulation for diagnosis and soft-error analysis

Raik, Jaan; Repinski, Urmaz; Jenihhin, Maksim; Chepurov, Anton Design and test technology for dependable systems-on-chip 2011 / p. 294-309 : ill <https://www.igi-global.com/chapter/high-level-decision-diagram-simulation/51406>

High-level fault diagnosis in RISC processors with Implementation-Independent Functional Test

Oyeniran, Adeboye Stephen; Jenihhin, Maksim; Raik, Jaan; Ubar, Raimund-Johannes 2022 IEEE Computer Society Annual Symposium on VLSI (ISVLSI) : Nicosia, Cyprus : 04-06 July 2022 2022 / p. 32-37 <https://doi.org/10.1109/ISVLSI54635.2022.00019>

High-level intellectual property obfuscation via decoy constants

Aksoy, Levent; Nguyen, Quang-Linh; Almeida, Felipe; Raik, Jaan; Flottes, Marie-Lise; Dupuis, Sophie; Pagliarini, Samuel Nascimento 2021 IEEE 27th International Symposium on On-Line Testing and Robust System Design (IOLTS) : Torino, Italy, 28-30 June 2021 2021 / p. 1-7 <https://doi.org/10.1109/IOLTS52814.2021.9486714>

High-performance hardware accelerators for sorting and managing priorities

Sklyarov, Valery; Skliarova, Iuliia; Mihhailov, Dmitri; Sudnitsõn, Aleksander Proceedings of the 2011 IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 13-15, 2011, Gottbus, Germany 2011 / p. 313-318 : ill
<https://ieeexplore.ieee.org/document/5783103>

High-speed design of postquantum cryptography with optimized hashing and multiplication

Imran, Malik; Aikata, Aikata; Roy, Sujoy Sinha; Pagliarini, Samuel Nascimento IEEE Transactions on Circuits and Systems II : Express Briefs 2023 / p. 847-851 : ill <https://doi.org/10.1109/TCSII.2023.3273821>

A HW/SW partitioning technique based on hierarchical canitate selection scheme

Jantsch, Axel; Ellervee, Peeter; Öberg, Johnny; Hemani, Ahmed 7th International Workshop on High-level Synthesis, Niagara-on-the-Lake, Ontario, Canada, May 18-20, 1994 1994

A hybrid BIST architecture and its optimization for SoC testing

Jervan, Gert; Peng, Zebo; Ubar, Raimund-Johannes; Kruus, Helena Proceedings of the 3rd International Symposium on Quality Electronic Design : ISQED 2002, March 18-21, 2002, San Jose, California 2002 / p. 273-279 : ill
<https://ieeexplore.ieee.org/document/996750>

Hybrid protection of digital FIR filters

Aksoy, Levent; Nguyen, Quang-Linh; Almeida, Felipe; Raik, Jaan; Flottes, Marie-Lise; Dupuis, Sophie; Pagliarini, Samuel Nascimento IEEE transactions on Very Large Scale Integration (VLSI) Systems 2023 / p. 812-825 : ill
<https://doi.org/10.1109/TVLSI.2023.3253641> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

IBM kinkis TTÜle tarkvara, Coca-Cola serveri

Mente et Manu 2005 / lk. 4 https://www.ester.ee/record=b1242496*est

IMMizer : an innovative cost-effective method for minimizing assertion sets

Heidari Iman, Mohammad Reza; Raik, Jaan; Jervan, Gert; Ghasempouri, Tara 2022 25th Euromicro Conference on Digital System Design (DSD) : Maspalomas, Spain 2022 : proceedings 2022 / p. 671 - 678 <https://doi.org/10.1109/DSD57027.2022.00095>
[Article at Scopus](#) [Article at WOS](#)

Impact of orientation on the bias of SRAM-based PUFs

Abideen, Zain Ul; Wang, Rui; Perez, Tiago Diadami; Schrijen, Geert-Jan; Pagliarini, Samuel Nascimento IEEE design & test 2024 / p. 14-20 <https://doi.org/10.1109/MDAT.2023.3322621> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Implementation of sorting algorithms in reconfigurable hardware

Skliarova, Iuliia; Sklyarov, Valery; Mihhailov, Dmitri; Sudnitsõn, Aleksander 2012 IEEE Mediterranean Electrotechnical Conference (MELECON 2012) : Yasmine Hammamet, Tunisia, March 25-28, 2012 2012 / p. 107-110 : ill
<https://ieeexplore.ieee.org/document/6196391>

Implementation-independent test generation for a large class of faults in RISC processor modules

Jenihhin, Maksim; Oyeniran, Adeboye Stephen; Raik, Jaan; Ubar, Raimund-Johannes 24th Euromicro Conference on Digital System Design (DSD) 2021 <https://doi.org/10.1109/DSD53832.2021.00090>

An improved implementation of shift displacement method on hardware —comprehensive evaluation of emerging bi-pedal techniques

Abideen, Zain UI; Tariq, Hassan; Hafeez, Muhammad Asfand; Subhani, Zahid Mehmood 2020 4th International Conference on Automation, Control and Robots : Rome, Italy, 11-13 October 2020 2020 / p. 7-12 : ill <https://doi.org/10.1109/ICACR51161.2020.9265496>

Infotehnoloogia. Sõnastik. Osa 3, Aparatuuritehnika

Hanson, Vello; Agur, Ustus; Kalja, Ahto; Võhandu, Leo 1999 https://www.ester.ee/record=b1708557*est

Infotehnoloogia. Sõnastik. Osa 11, Töötlusseadmed

Hanson, Vello; Agur, Ustus; Kalja, Ahto; Võhandu, Leo 1999 https://www.ester.ee/record=b1211017*est

Introduction to the IOT (Internet of Things) : coursebook

Sell, Raivo; Puks, Rim; Kingsepp, Mallor; Nikitenko, Agris; Berkolds, Karlis; Vagale, Anete; Rumba, Rudolfs; Czekalski, Piotr; Tokarz, Krzysztof; Läll, Karl 2025 <https://ebooks.rtu.lv/product/introduction-to-the-iot-second-edition/?lang=en#tab-id-1>

Jini tehnoloogias

Küngas, Peep Arvutimaailm 2000 / 8, lk. 45 https://artiklid.elnet.ee/record=b1004987*est

Kaks uut unikaalset TalTechi magistrikava hakkavad valmistama ette tippspetsialiste strateegiliselt tähtsale IKT valdkonnale

digi.geenius.ee 2023 [Kaks uut unikaalset TalTechi magistrikava hakkavad valmistama ette tippspetsialiste strateegiliselt tähtsale IKT valdkonnale](https://digi.geenius.ee/blogi/teadus-ja-tulevik/kas-sind-huvitab-mis-toimub-kapoti-all-taltech-magistriope-avab-tehnoloogia-telgitagused/)

Kas sind huvitab, mis toimub “kapoti all”? TalTechi magistriõpe avab tehnoloogia telgitagused

digi.geenius.ee 2025 <https://digi.geenius.ee/blogi/teadus-ja-tulevik/kas-sind-huvitab-mis-toimub-kapoti-all-taltech-magistriope-avab-tehnoloogia-telgitagused/>

Keynote: cost-efficient reliability for Edge-AI chips

Jenihhin, Maksim; Taheri, Mahdi; Cherezova, Natalia; Ahmadilivani, Mohammad Hasan; Selg, Hardi; Jutman, Artur; Shibin, Konstantin; Tsertov, Anton; Devadze, Sergei; Kodamanchili, Rama Mounika; Rafiq, Ahsan; Raik, Jaan; Daneshtalab, Masoud 2024 IEEE 25th Latin American Test Symposium (LATS) 2024 / 2 p <https://doi.org/10.1109/LATS62223.2024.10534610> Article at Scopus

Kontekstilülilus tark- ja riistvaras

Tammemäe, Kalle A & A 2002 / 3, lk. 14-21 : ill

Kontseptuaalse programmeerimise vahenditest

Eemois, Peep Arvutustehnika ja Andmetöötlus : A&A 1989 / lk. 1-8 : joon., tab https://www.ester.ee/record=b1071940*est

Kuidas osta personaalarvutit? : ostujuhiseid

Laiatarbe tarkvarapaketid personaalarvutitele. Elementaarkasutusõpetus ja personaalse info korraldajad 1989 / lk. 92-101 https://www.ester.ee/record=b1294951*est

Latest trends in hardware security and privacy

Di Natale, Giorgio; Regazzoni, Francesco; Albanese, Vincent; Lhermet, Frank; Loisel, Yann; Sensaoui, Abderrahmane; Pagliarini, Samuel Nascimento 33rd IEEE International Symposium on Defect and Fault Tolerance in VLSI and Nanotechnology Systems (DFT) : ESA-ESRIN, Italy (On-line Virtual Event), October 19–21, 2020 2020 / 4 p. : ill <https://doi.org/10.1109/DFT50435.2020.9250816>

Leveraging FPGA Reconfigurability as an Obfuscation Asset = FPGA ümberkonfigureeritavuse rakendamise häägustamise vahendina

Abideen, Zain UI 2024 <https://digikogu.taltech.ee/et/Item/660d923b-44d2-4993-898f-324ab2088199>
https://www.ester.ee/record=b5649944*est <https://doi.org/10.23658/taltech.1/2024>

Leveraging layout-based effects for locking analog ICs

Aljafar, Muayad J.; Azais, Florence; Flottes, Marie-Lise; Pagliarini, Samuel Nascimento ASHES'22: Proceedings of the 2022 Workshop on Attacks and Solutions in Hardware Security 2022 / p. 5-13 <https://doi.org/10.1145/3560834.3563826>

Low-cost open-source flow velocity sensor for droplet generators

Prabatama, Nicky Andre; Jõemaa, Rauno; Hegedus, Kristof; Pardy, Tamas 2022 18th Biennial Baltic Electronics Conference (BEC) 2022 / p. 1-4 <https://doi.org/10.1109/BEC56180.2022.9935606>

Machine learning clustering techniques for selective mitigation of critical design features

Lange, Thomas; **Balakrishnan, Aneesh**; Glorieux, Maximilien; Alexandrescu, Dan; Sterpone, Luca Proceedings : 2020 26th IEEE International Symposium on On-Line Testing and Robust System Design : IOLTS 2020, Napoli, Italy, July 13-16, 2020 : virtual edition 2020 / 7 p. : ill <https://doi.org/10.1109/IOLTS50870.2020.9159751>

Mapping the types of modularity in open-source hardware

Gavras, Kosmas; **Kostakis, Vasileios** Design Science 2021 / e13 <https://doi.org/10.1017/dsj.2021.11> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Methods for reliability assessment and enhancement of deep neural networks hardware accelerators =

Süvanärvivõrkude riistvara kiirendite töökindluse hindamine ja täiustamine

Taheri, Mahdi 2025 https://www.ester.ee/record=b5728368*est <https://digikogu.taltech.ee/et/Item/9cf79768-17bc-44ec-a828-e4ccf6cf93f1>
<https://doi.org/10.23658/taltech.4/2025>

Microprocessors and microsystems : MICPRO : embedded hardware design

2010

Mikrokontrollerite M68HC11 ja Satt Con 05 riist- ja tarkvara : metoodiline materjal

Tammemäe, Kalle; Toomsalu, Arvo 1995 https://www.ester.ee/record=b1069449*est

Mikrokontrollerite riistvara turbest

Toomsalu, Arvo Arvutustehnika ja Andmetöötlus 1997 / 7/8, lk. 7-12

Miniatuurne märgvara [Võrguteavik]

Tammemäe, Kalle Arvutimaailm 2018 / 14 p <https://www.am.ee/margvara>

Mis on Dhrystone ja Whetstone?

Toomsalu, Arvo Arvutustehnika ja Andmetöötlus 1992 / 10, lk. 11-15

Model and Data Engineering : 10th international conference, MEDI 2021, Tallinn, Estonia, June 21–23, 2021 : proceedings

2021 <https://doi.org/10.1007/978-3-030-78428-7>

Model synthesis from VHDL for the functional test generation system

Krupnova, Helena 1993 https://www.ester.ee/record=b2090509*est

Modular approach to training hardware design for modular teaching [Electronic resource]

Galkin, Ilja; Laugis, Juhan EPE-PEMC 2006 : 12th International Power Electronics and Motion Control Conference : Portorož, Slovenia, August 30 - September 1, 2006 : proceedings 2006 / p. 1715-1720 : ill. [CD-ROM]
<https://ieeexplore.ieee.org/document/4778652>

Moore'i seadus ja tehnoloogia areng

Toomsalu, Arvo Arvutustehnika ja Andmetöötlus 1997 / 9, lk. 3-10

Multimeedia ülekanne piiratud ressursside puhul

Kulmar, Marika Telekommunikatsioon 2002 : IX rahvusvahelise telekommunikatsioonipäeva materjalid 2002 / lk. 36-45 : ill

Multimeediumisüsteemide riistvara

Toomsalu, Arvo Arvutustehnika ja Andmetöötlus 1997 / 10, lk. 7-16

Mutation analysis for systemC designs at TLM

Guarnieri, Valerio; Bombieri, Nicola; Pravadelli, Graziano; Fummi, Franco; **Hantson, Hanno**; **Raik, Jaan**; **Jenihhin, Maksim**; **Ubar, Raimund-Johannes** 12th IEEE Latin American Test Workshop (LATW) : Porto de Galinhas, Brasil, 27-30 March 2011 2011 / [6] p
<https://ieeexplore.ieee.org/document/5985925>

Mutation-based verification and error correction in high-level designs = Mutatsioonidel põhinev verifitseerimine ja vigade parandamine kõrgtaseme skeemides

Hantson, Hanno 2015 https://www.ester.ee/record=b4518212*est

Mutations for testing hardware and correcting design errors

Hantson, Hanno Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK viienda aastakonverentsi artiklite kogumik : 25.-26. novembril 2011, Nelijärve 2011 / p. 105-108 : ill

NATO kursus riist- ja tarkvara koosdisainist

Tammemäe, Kalle Arvutustehnika ja Andmetöötlus 1995 / 9, lk. 23-24; 10, lk. 2-8; 11, lk. 2-7; 12, lk. 8-15

NeuroPIM: flexible neural accelerator for processing-in-memory architectures

Bidgoli, Ali Monavari; Fattahi, Sepideh; Rezaei, Seyyed Hossein Seyyedaghaei; Modarressi, Mehdi; **Daneshtalab, Masoud** 2023 26th International Symposium on Design and Diagnostics of Electronic Circuits and Systems (DDECS) : May 3-5, 2023, Tallin, Estonia : proceedings 2023 / p. 51-56 <https://doi.org/10.1109/DDECS57882.2023.10139567>

New built-in self-test scheme for SoC interconnect

Jutman, Artur; Ubar, Raimund-Johannes; Raik, Jaan The 9th World Multi-Conference on Systemics, Cybernetics and Informatics : WMSCI 2005 : July 10-13, 2005, Orlando, Florida, USA. Vol. IV 2005 / p. 19-24 : ill https://www.researchgate.net/publication/237375234_New_Built-In_Self-Test_Scheme_for_SoC_Interconnect

Novel architectures for contractive autoencoders with embedded learning

Kerner, Madis; Tammemäe, Kalle; Raik, Jaan; Hollstein, Thomas 2020 17th Biennial Baltic electronics conference, Tallinn, Estonia, October 6-8, 2020 : proceedings 2020 / 6 p. : ill <https://doi.org/10.1109/BEC49624.2020.9277246>

Novel Neural Network accelerator architectures for FPGAs = Uudsed närvivõrkude kiirendite arhitektuurid FPGAdele

Kerner, Madis 2024 https://www.ester.ee/record=b5675484*est <https://digikogu.taltech.ee/et/Item/3568fe35-19c3-43e6-9525-73c79371ab13> <https://doi.org/10.23658/taltech.16/2024>

On the reuse of TLM mutation analysis at RTL

Guarnieri, Valerio; **Hantson, Hanno; Raik, Jaan; Jenihhin, Maksim; Ubar, Raimund-Johannes** Journal of electronic testing : theory and applications 2012 / p. 435-448 : ill <https://link.springer.com/article/10.1007/s10836-012-5303-6>

On the use of defensive schemes for hardware security = Kaitseskeemid riistvara turvalisuse tagamiseks

Eslami, Mohammad 2024 https://www.ester.ee/record=b5701420*est <https://doi.org/10.23658/taltech.53/2024> <https://digikogu.taltech.ee/et/Item/068530be-4810-4489-9604-fb838d298b45>

Ontology design for automatic evaluation of web user interface usability [Electronic resource]

Robal, Tarmo; Marenkov, Jevgeni; Kalja, Ahto PICMET '17 : Portland International Conference on Management of Engineering and Technology : Technology Management for the Interconnected World : proceedings 2017 / p. 1-8 : ill. [USB] <https://doi.org/10.23919/PICMET.2017.8125425>

Open source hardware cost-effective imaging sensors for high-throughput droplet microfluidic systems

Pärnamets, Kaiser; Koel, Ants; Pardy, Tamas; Rang, Toomas Proceedings of 26th International Conference : ELECTRONICS 2022 2022 / 6 p <https://doi.org/10.1109/IEEECONF55059.2022.9810383>

An open surface drifter for river flow field characterization

Fuentes-Pérez, Juan Francisco; Sanz-Ronda, Francisco Javier; **Tuhtan, Jeffrey Andrew** Sensors 2022 / art. nr. 9918 <https://doi.org/10.3390/s22249918> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Open-source framework and practical considerations for translating RTL VHDL to SystemC

Saif Abrar, Syed; Jenihhin, Maksim; Raik, Jaan IP-SoC 2012 : IP Embedded System Conference & Exhibition : Grenoble, France, Dec. 4-5, 2012 2012 <https://www.design-reuse.com/articles/32685/translating-rtl-vhdl-to-systemc.html>

Open-source JTAG simulator bundle for labs

Shibin, Konstantin; Devadze, Sergei; Rosin, Vjatšeslav; Jutman, Artur; Ubar, Raimund-Johannes International journal of electronics and telecommunications 2012 / p. 233-239 : ill <https://journals.pan.pl/Content/87192/PDF/32.pdf>

Optimization of recursive sorting algorithms for implementation in hardware

Mihhailov, Dmitri; Sklyarov, Valery; Skliarova, Iuliia; Sudnitsõn, Aleksander Proceedings of 22nd International Conference on Microelectronics (ICM 2010) : Cairo, Egypt, Dec. 19-22, 2010 2010 / p. 471-474 : ill https://www.researchgate.net/publication/224213497_Optimization_of_recursive_sorting_algorithms_for_implementation_in_hardware

An overview of FPGA-inspired obfuscation techniques

Abideen, Zain Ul; Gokulanathan, Sumathi; Aljafar, Muayad J.; Pagliarini, Samuel Nascimento arXiv.org 2023 / 30 p. : ill <https://doi.org/10.48550/arXiv.2305.15999>

Parallel pseudo-exhaustive testing of array multipliers with data-controlled segmentation

Oyeniran, Adeboye Stephen; Azad, Siavoosh Payandeh; Ubar, Raimund-Johannes 2018 IEEE International Symposium on Circuits and Systems (ISCAS) : 27-30 May 2018, Florence, Italy : proceedings 2018 / 5 p.: ill <https://doi.org/10.1109/ISCAS.2018.8350936> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

PASCAL : timing SCA resistant design and verification flow

Lai, Xinhui; Jenihhin, Maksim; Raik, Jaan; Paul, Kolin 2019 IEEE 25th International Symposium on On-Line Testing and Robust System Design (IOLTS 2019) : 1-3 July 2019, Greece 2019 / p. 239-242 : ill <https://doi.org/10.1109/IOLTS.2019.8854458>

Performance evaluation for FPGA-based processing of tree-like structures

Sklyarov, Valery; Skliarova, Iuliia; **Mihhailov, Dmitri; Sudnitsõn, Aleksander** 19th IEEE International Conference on Electronics, Circuits, and Systems (IEEE ICECS), Sevilla, Spain, December 9-12, 2012 2012 / p. 217-220 : ill

A pragmatic methodology for blind hardware trojan insertion in finalized layouts

Hepp, Alexander; **Perez, Tiago Diadami; Pagliarini, Samuel Nascimento**; Sigl, Georg ICCAD '22: Proceedings of the 41st IEEE/ACM International Conference on Computer-Aided Design 2022 / art. 69, p. 1-9 : ill <https://doi.org/10.1145/3508352.3549452>
[Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Preface

Bellatreche, Ladjel; Chernishev, George; Corral, Antonio; Ouchani, Samir; **Vain, Jüri** Advances in Model and Data Engineering in the Digitalization Era : MEDI 2021 International Workshops : DETECT, SIAS, CSMML, BIOC, HEDA, Tallinn, Estonia, June 21–23, 2021 : proceedings 2021 / p. v-vii <https://link.springer.com/content/pdf/bfm:978-3-030-87657-9/1?pdf=chapter%20toc> [Article collection metrics at Scopus](#) [Article at Scopus](#)

Preface

Attiogbe, Christian; **Ben Yahia, Sadok** Model and Data Engineering : 10th International Conference, MEDI 2021, Tallinn, Estonia, June 21–23, 2021 : proceedings 2021 / p. v-vi <https://doi.org/10.1007/978-3-030-78428-7> [Conference proceeding at Scopus](#) [Article at Scopus](#)

Preface

Hollstein, Thomas; Raik, Jaan; Kostin, Sergei; Tšertov, Anton; O'Connor, Ian; Reis, Ricardo VLSI-SoC: System-on-Chip in the Nanoscale Era – Design, Verification and Reliability, 24th IFIP WG 10.5/IEEE International Conference on Very Large Scale Integration, VLSI-SoC 2016, Tallinn, Estonia, September 26-28, 2016 : Revised Selected Papers 2017 / p. V-VI
<https://link.springer.com/book/10.1007/978-3-319-67104-8> [Conference proceedings at Scopus](#) [Article at Scopus](#)

Processing N-ary trees in hardware circuits

Sklyarov, Valery; Skliarova, Iuliia; **Mihhailov, Dmitri; Sudnitsõn, Aleksander** 13th International Symposium on Integrated Circuits (ISIC) : Singapore, 12-14 December 2011 : proceedings 2011 / p. 262-265 : ill <https://ieeexplore.ieee.org/document/6131946>

Processing tree-like data structures for sorting and managing priorities

Sklyarov, Valery; Skliarova, Iuliia; **Mihhailov, Dmitri; Sudnitsõn, Aleksander** 2011 IEEE Symposium on Computers & Informatics : ISCI 2011 : Kuala Lumpur, Malaysia, 20-23 March 2011 2011 / p. 322-327
https://www.researchgate.net/publication/252020117_Processing_tree-like_data_structures_for_sorting_and_managing_priorities

Processing tree-like data structures in different computing platforms

Sklyarov, Valery; Skliarova, Iuliia; Oliveira, Ramiro; **Mihhailov, Dmitri; Sudnitsõn, Aleksander** 2011 International Conference on Information and Computer Applications (ICICA 2011) : Dubai, United Arab Emirates, March 18-20, 2011 2011 / p. 112-116 : ill
https://sweet.ua.pt/iouliia/Papers/2011/rp025_ICICA2011-A067.pdf

Processor vulnerability detection with the aid of assertions : RISC-V case study

Heidari Iman, Mohammad Reza; Ahmadi-Pour, Sallar; Drechsler, Rolf; **Ghasempouri, Tara** techrxiv.org 2024 / p. 1-8 : ill
<https://doi.org/10.36227/techrxiv.172101134.45466090/v1>

Product Assurance in the Age of Artificial Intelligence

2025 <https://doi.org/10.4271/EPR2025011> <https://saemobilus.sae.org/reports/product-assurance-age-artificial-intelligence-epr2025011>

PV-battery assisted three-level T-Type inverter for AC residential nanogrid realized with small-scale HIL units

Gutierrez-Escalona, Javier; **Roncero-Clemente, Carlos**; Gonzalez-Romera, Eva; Milanes-Montero, Maria Isabel; Husev, Oleksandr; Romero-Cadaval, Enrique IEEE Access 2023 / p. 48007 - 48021 <https://doi.org/10.1109/ACCESS.2023.3276235> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Ransomware attack as Hardware Trojan : a feasibility and demonstration study

Almeida, Felipe; Imran, Malik; Raik, Jaan; Pagliarini, Samuel Nascimento IEEE Access 2022 / p. 44827-44839
<https://doi.org/10.1109/ACCESS.2022.3168991> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Rejuvenation of nanoscale logic at NBTI-critical paths using evolutionary TPG

Palermo, N.; **Tihhomirov, Valentin**; Copetti, Thiago; **Jenihhin, Maksim; Raik, Jaan; Kostin, Sergei** 2015 16th Latin American Test Symposium (LATS 2015) : Puerto Vallarta, Mexico, 25-27 March 2015 2015 / [6] p. : ill
<http://dx.doi.org/10.1109/LATW.2015.7102405>

Replication-based deterministic testing of 2-dimensional arrays with highly interrelated cells

Azad, Siavoosh Payandeh; Oyeniran, Adeboye Stephen; Ubar, Raimund-Johannes 21st IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems : DDECS 2018 : Budapest, Hungary 25-27 April, 2018 : proceedings 2018 / p. 21-26 : ill <https://doi.org/10.1109/DDECS.2018.00011>

A rescue demonstrator for interdependent aspects of reliability, security and quality towards a complete EDA flow

Raik, Jaan; Jenihhin, Maksim 2020 Design, Automation & Test in Europe Conference & Exhibition (DATE) : proceedings 2020 / p. 58 <https://ieeexplore.ieee.org/stamp/stamp.jsp?arnumber=9116424>

Research and training environment for digital design and test

Ubar, Raimund-Johannes; Wuttke, Heinz-Dietrich Proceedings of the Eighth IASTED International Conference on Computers and Advanced Technology in Education : August 29-31, 2005, Oranjestad, Aruba 2005 / p. 232-237 : ill
<https://ieeexplore.ieee.org/document/1408779>

Resource-aware scene text recognition using learned features, quantization, and contour-based character extraction

Ademola, Olutosin Ajibola; Petlenkov, Eduard; Leier, Mairo IEEE Access 2023 / p. 56865 - 56874
<https://doi.org/10.1109/ACCESS.2023.3283931> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Resynthesis-based attacks against logic locking

Almeida, Felipe; Aksoy, Levent; Nguyen, Quang-Linh; Dupuis, Sophie; Flottes, Marie-Lise; **Pagliarini, Samuel Nascimento** 2023 24th International Symposium on Quality Electronic Design (ISQED) : San Francisco, 5-7 April 2023 2023 / 8 p. : ill
<https://doi.org/10.1109/ISQED57927.2023.10129403> [Article at Scopus](#) [Article at WOS](#)

Reusing verification assertions as security checkers for Hardware Trojan detection

Eslami, Mohammad; Ghasempouri, Tara; Pagliarini, Samuel Nascimento 2022 23rd International Symposium on Quality Electronic Design (ISQED), Santa Clara, CA, USA : 06-07 April 2022 2022 / p. 1-6 : ill
<https://doi.org/10.1109/ISQED54688.2022.9806292>

Revolver : a high-performance MIMD architecture for collision free computing

Öberg, I.; **Ellervee, Peeter** Euromicro Conference : proceedings : 24th Euromicro Conference : Västerås, Sweden, August 25-27, 1998. Vol. 1 1998 / p. 301-308 <https://ieeexplore.ieee.org/document/711814>

Riist- ja tarkvara hackathon Tehnohack tuleb taas!

Inseneeria 2016 / lk. [56] http://www.ester.ee/record=b2336521*est

Riist- ja tarkvara koosdisain ja -simuleerimine

Tammemäe, Kalle Arvutustehnika ja Andmetöötlus 1997 / 2, lk. 1-3; 3, lk. 1-6; 4, lk. 1-4; 5, lk. 1-6: ill

Riistvara kirjeldamise keel VHDL

Tammemäe, Kalle Arvutustehnika ja Andmetöötlus 1992 / 4, lk. 1-11: ill

Riistvara kirjeldamiskeel - VHDL : metoodiline materjal

1992 https://www.ester.ee/record=b1062926*est

Riistvara kirjeldamiskeel - VHDL : metoodiline materjal

Tammemäe, Kalle 2003 http://www.ester.ee/record=b1605950*est

Riistvara kirjeldamiskeel VHDL : metoodiline materjal

Tammemäe, Kalle 2002 http://www.ester.ee/record=b1605950*est

Riistvara kirjelduskeel Verilog

Ellervee, Peeter A & A 1998 / 1, lk. 6-10

Riistvara kompileerimine

Tammemäe, Kalle A & A 1998 / 5, lk. 11-15: ill

Riistvara suhtlemiseks

Jaanus, Martin Praktiline Arvutikasutaja 2006 / 6, lk. 35-37 : ill https://artiklid.elnet.ee/record=b1054613*est

Riistvara Trooja hobuste rünnak võib ohustada nii koduarvuti kasutajat kui ka riigi digitaalset julgeolekut [Võrguväljaanne]

arileht.delfi.ee 2022 [Riistvara Trooja hobuste rünnak võib ohustada nii koduarvuti kasutajat kui ka riigi digitaalset julgeolekut](#)

SALSy : security-aware layout synthesis

Eslami, Mohammad; Perez, Tiago Diadami; Pagliarini, Samuel Nascimento arXiv.org 2024 / 13 p. : ill
<https://doi.org/10.48550/arXiv.2308.06201>

SCARF : securing chips with a robust framework against fabrication-time hardware Trojans : preprint

Eslami, Mohammad; Ghasempouri, Tara; Pagliarini, Samuel Nascimento arXiv.org 2024 / 14 p. : ill
<https://doi.org/10.48550/arXiv.2402.12162>

A security-aware and LUT-based CAD flow for the physical synthesis of hASICs

Abideen, Zain Ul; Perez, Tiago Diadami; Martins, Mayler; Pagliarini, Samuel Nascimento IEEE transactions on computer-aided design of integrated circuits and systems 2023 / p. 3157-3170 : ill <https://doi.org/10.1109/TCAD.2023.3244879> [Journal metrics at Scopus](#)

Security-aware physical synthesis of integrated circuits = Integraallülituste turvateadlik füüsiline süntees

Perez, Tiago Diadami 2023 <https://doi.org/10.23658/taltech.4/2023> <https://digikogu.taltech.ee/et/Item/440f41fd-0950-4b5c-8e47-4f75a754cdae> https://www.ester.ee/record=b5536743*est

Sequential test set compaction in LFSR reseeding

Jutman, Artur; Aleksejev, Igor; Raik, Jaan Design and test technology for dependable systems-on-chip 2011 / p. 476-493 : ill <https://ieeexplore.ieee.org/document/4738292>

A side-channel hardware trojan in 65nm CMOS with 2 μ W precision and multi-bit leakage capability

Perez, Tiago Diadami; Pagliarini, Samuel Nascimento 2022 27th Asia and South Pacific Design Automation Conference (ASP-DAC) : 17-20 January 2022 : Taipei, Taiwan 2022 / p. 9-10 : ill <https://doi.org/10.1109/ASP-DAC52403.2022.9712490>

Side-channel Trojan insertion - a practical foundry-side attack via ECO

Perez, Tiago Diadami; Imran, Malik; Vaz, Pablo; Pagliarini, Samuel Nascimento 2021 IEEE International Symposium on Circuits and Systems (ISCAS), Daegu, Korea, May 22-28, 2021 : proceedings 2021 / 5 p. : ill <https://doi.org/10.1109/ISCAS51556.2021.9401481>
[Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Simulation

Parmakson, Priit; Tepandi, Jaak Concise encyclopedia of software engineering 1993 / p. 303-306

Simulation-based hardware verification with high-level decision diagrams = Simuleerimisel põhinev riistvara verifitseerimine kõrgtaseme otsustusdiagrammidel

Jenihhin, Maksim 2008 https://www.ester.ee/record=b2431332*est

A small-scale, low-budget semi-immersive virtual environment for scientific visualization and research

Pastorelli, Emiliano; Herrmann, Heiko 2013 International Conference on Virtual and Augmented Reality in Education 2013 / p. 14-22 <https://doi.org/10.1016/j.procs.2013.11.003> [Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Software parser and analyser for hardware performance estimations

Ruberg, Priit; Meinberg, Erki; Ellervee, Peeter 2022 International Conference on Electrical, Computer and Energy Technologies (ICECET), Prague, Czech Republic, 20-22 July 2022 2022 / p. 1-6 <https://doi.org/10.1109/ICECET55527.2022.9872951>

Software-based mitigation for memory address decoder aging

Kraak, D. H. P.; Gürsoy, Cemil Cem; Jenihhin, Maksim; Raik, Jaan LATS 2019 : 20th IEEE Latin American Test Symposium : Santiago, Chile, March 11th - 13th 2019 2019 / 6 p. : ill <https://doi.org/10.1109/LATW.2019.8704595>

Solving computationally intensive problems in reconfigurable hardware : a case study

Skliarova, Ioulia; Vallejo, Tiago; Sklyarov, Valery; Sudnitsõn, Aleksander; Kruus, Margus Journal of convergence information technology (JCIT) : an international journal 2013 / p. 601-609 : ill

Structural decision diagrams in digital test : theory and applications

Ubar, Raimund-Johannes; Raik, Jaan; Jenihhin, Maksim; Jutman, Artur 2024 <https://doi.org/10.1007/978-3-031-44734-1>
https://www.ester.ee/record=b5734441*est

Survey on architectural attacks : a unified classification and attack model

Ghasempouri, Tara; Raik, Jaan; Reinbrecht, Cezar; Hamdioui, Said; Hamdioui, Said ACM Computing Surveys 2023 / art. 42 <https://doi.org/10.1145/3604803> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

A survey on split manufacturing : attacks, defenses, and challenges

Perez, Tiago Diadami; Pagliarini, Samuel Nascimento IEEE Access 2020 / p. 184013-184035
<https://doi.org/10.1109/ACCESS.2020.3029339> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Survey on the benefits of using memristors for PUFs

Aljafar, Muayad J.; Acken, John M. International Journal of Parallel, Emergent and Distributed Systems 2022 / p. 40-67
<https://doi.org/10.1080/17445760.2021.1972295>

A survey on UAV computing platforms : a hardware reliability perspective

Ahmed, Foisal; Jenihhin, Maksim Sensors 2022 / art. 6286 <https://doi.org/10.3390/s22166286> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

A systematic literature review on hardware reliability assessment methods for deep neural networks

Ahmadiilivani, Mohammad Hasan; Taheri, Mahdi; Raik, Jaan; Daneshtalab, Masoud; Jenihhin, Maksim ACM Computing Surveys 2024 / art. 141, 39 p. : ill <https://doi.org/10.1145/3638242> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

A systematic study of lattice-based NIST PQC algorithms : from reference implementations to hardware accelerators
Imran, Malik; Abideen, Zain Ul; Pagliarini, Samuel Nascimento arXiv.org 2020 / 36 p. : ill

zamiaCAD : open source platform for advanced hardware design

Tšepurov, Anton; Jenihhin, Maksim; Raik, Jaan DATE 2011 University Booth : Design Automation and Test in Europe : Grenoble, France, March 14-18, 2011 2011 / [2] p.: ill

zamiaCAD : understand, develop and debug hardware designs

Jenihhin, Maksim; Tihomirov, Valentin; Saif Abrar, Syed; Raik, Jaan; Bartsch, Günter DUHDe : 1st Workshop on Design Automation for Understanding Hardware Designs : March 28, 2014 : Friday Workshop at DATE 2014, Dresden, Germany 2014 / p. 1-6

Zero-memory-overhead clipping-based fault tolerance for LSTM deep neural networks

Parchekani, Bahram; Nazari, Samira; Ahmadilivani, Mohammad Hasan; Azarpeyvand, Ali; Raik, Jaan; Ghasempouri, Tara; Daneshtalab, Masoud 37th IEEE International Symposium on Defect and Fault Tolerance in VLSI and Nanotechnology Systems, Harwell, Oxfordshire, Didcot, UK, October 8th - 10th, 2024 2024 / 4 p. : ill <https://doi.org/10.1109/DFT63277.2024.10753533> [Conference proceedings at Scopus](#) [Article at Scopus](#)

TalTechi riistvara turvalisuse keskuse doktorandid saavutasid rahvusvahelisel konkurentsitihedal võistlusel kolmanda koha [Võrguväljaanne]

digi.geenius.ee 2022 "[TalTechi riistvara turvalisuse keskuse doktorandid saavutasid rahvusvahelisel konkurentsitihedal võistlusel kolmanda koha](#)"

TalTechi uus magistrikava avab lõpetajatele ukse looma uut nutikat maailma tarkadest majadest rohepöördeni

arileht.delfi.ee 2023 [TalTechi uus magistrikava avab lõpetajatele ukse looma uut nutikat maailma tarkadest majadest rohepöördeni](#)

Tehnohack 2016 : ideid jagus plaastrilaadsest tootest bumerangini

Inseneeria 2016 / lk. [42]-43 : fot https://artiklid.elnet.ee/record=b2806776*est

TehnoHack 2017 : pissiandurist nullgravitatsioonini

Mühlbach, Madle Mente et Manu 2017 / lk. 54-55 : fot http://www.ester.ee/record=b1242496*est
https://artiklid.elnet.ee/record=b2830887*est

Temporally extended high-level decision diagrams for PSL assertions simulation

Jenihhin, Maksim; Raik, Jaan; Tšepurov, Anton; Ubar, Raimund-Johannes Proceedings : Thirteenth IEEE European Test Symposium : ETS 2008 : 25-29 May 2008, Verbania, Italy 2008 / p. 61-68 : ill <https://ieeexplore.ieee.org/document/4556029>

Terminali riist- ja tarkvarakompleks

Palmas, M.; Niinsalu, Udo XXXII üliõpilaste teaduslik-tehnilise konverentsi ettekannete teesid : pühendatud V. I. Lenini 110. sünniaastapäevale : 16.-18. aprill 1980 1981 / lk. 96 https://www.ester.ee/record=b1322611*est

The hardware for artificial nose

Misans, Peteris; Dzilums, Guntars; Shirs, Juris BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 425-428: ill

The substation local PC workstation. Present and future

Dmitrenko, Igor 9th International Symposium "Topical problems in the field of electrical and power engineering". Doctoral school of energy and geotechnology. II : Pärnu, Estonia, June 14-19, 2010 2010 / p. 186-189 : ill

A 3-D crossbar architecture for both pipeline and parallel computations

Aljafar, Muayad J.; Acken, John M. IEEE Transactions on Circuits and Systems I : regular papers 2021 / p. 4456-4469
<https://doi.org/10.1109/TCSI.2021.3108564> https://pdxscholar.library.pdx.edu/cgi/viewcontent.cgi?article=1680&context=ece_fac

Toimiva digiühiskonna tagavad usaldusväärne tarkvara, turvaline riistvara ning energiasäästlikud ja nutikad asjad

Hämat, Karin Mente et Manu 2022 / lk. 32-33 https://www.ester.ee/record=b1242496*est

Towards multidimensional verification : where functional meets non-functional

Jenihhin, Maksim; Lai, Xinhui; Ghasempouri, Tara; Raik, Jaan 2018 IEEE Nordic Circuits and Systems Conference (NORCAS) : NORCHIP and International Symposium of System-on-Chip (SoC) : 30-31 October 2018, Tallinn, Estonia : proceedings in IEEE Xplore 2018 / 7 p. : ill <https://doi.org/10.1109/NORCHIP.2018.8573495>

Trainer 1149 : a boundary scan simulation bundle with hardware support for labs

Shibin, Konstantin; Jutman, Artur Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK viienda aastakonverentsi artiklite kogumik : 25.-26. novembril 2011, Nelijärve 2011 / p. 135-138 : ill

Trainer 1149: a boundary scan simulation bundle for labs

Jutman, Artur; Ubar, Raimund-Johannes; Devadze, Sergei; Shibin, Konstantin; Rosin, Vjatšeslav MIXDES 2011 : 18th International Conference "Mixed Design of Integrated Circuits and Systems" : June 16-18, 2011, Gliwice, Poland 2011 / p. 520-525

Translating behavioral VHDL for emulation

Ellervee, Peeter; Reinsalu, Uljana; Arhipov, Anton 25th IEEE NORCHIP Conference : Aalborg, Denmark, 19-20 November 2007 2007 / ? p <https://ieeexplore.ieee.org/document/4481073>

Transpuutrid

Toomsalu, Arvo Arvutustehnika ja Andmetöötlus 1997 / 4, lk. 14-16; 5, lk. 7-11; 6, lk. 6-9; 7/8, lk. 1-6: ill

Triple fixed-point MAC unit for deep learning

Kerner, Madis; Tammemäe, Kalle; Raik, Jaan; Hollstein, Thomas Proceedings of the 2021 Design, Automation & Test in Europe (DATE 2021), 1-5 February 2021 : Virtual Conference 2021 / p. 1404-1407 <https://doi.org/10.23919/DATE51398.2021.9474020>

Tuleviku ootel

Võhandu, Leo Arvutimaailm 1993 / 3, lk. 4

Turning JTAG inside out for fast extended test access

Devadze, Sergei; Jutman, Artur; Aleksejev, Igor; Ubar, Raimund-Johannes 10th IEEE Latin American Test Workshop : 2-5 March 2009, Brazil 2009 / [6] p. : ill <https://ieeexplore.ieee.org/document/4813799>

Töökindla arvutusriistvara keskuse juht Maksim Jenihhin

Jenihhin, Maksim forte.delfi.ee 2024 [Töökindla arvutusriistvara keskuse juht Maksim Jenihhin](#)

Understanding multidimensional verification : where functional meets non-functional

Lai, Xinhui; Balakrishnan, Aneesh; Lange, Thomas; **Jenihhin, Maksim; Ghasempouri, Tara; Raik, Jaan;** Alexandrescu, Dan Microprocessors and microsystems 2019 / art. 102867, 13 p. : ill <https://doi.org/10.1016/j.micpro.2019.102867> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

UPS arvuti elektritoitesüsteemis

Toomsalu, Arvo Arvutimaailm 1998 / 6, lk. 18-22, 63: ill https://artiklid.elnet.ee/record=b2003508*est

Using of industry automation software and hardware in education and training

Rosin, Argo; Laugis, Juhan; Lehtla, Tõnu CAx Techniques : proceedings of the 4th International Scientific Colloquium : Bielefeld, Germany, September 13-15, 1999 1999 / p. 119-124 : ill

Using simulation statistics for bug localization in RTL designs

Tihhomirov, Valentin; Jenihhin, Maksim; Raik, Jaan Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK seitsmenda aastakonverentsi artiklite kogumik : 15.-16. novembril 2013, Haapsalu 2013 / p. 107-110 : ill

Using soft-core processors and FPGA development boards for hardware emulation

Arhipov, Anton; Ellervee, Peeter Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK teise aastakonverentsi artiklite kogumik : 11.-12. mai 2007, Viinistu kunstimuuseum 2007 / lk. 155-158 : ill

Using Tabu Search for optimization of memory-constrained hybrid BIST

Kruus, Helena; Jervan, Gert; Ubar, Raimund-Johannes Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK kolmanda aastakonverentsi artiklite kogumik : 25.-26. aprill 2008, Voore külalistemaja 2008 / p. 85-88 : ill

Using weighted graphs for fast architecture exploration

Ellervee, Peeter; Klaar, Tarmo; Kruus, Margus; Tammemäe, Kalle BEC 2002 : proceedings of the 8th Biennial Baltic Electronics Conference : October 6-9, 2002, Tallinn, Estonia 2002 / p. 111-114 : ill

Uus magistrikava avab lõpetajatele ukse nutikasse maailma tarkadest majadest rohepöördeni

postimees.ee 2023 [Uus magistrikava avab lõpetajatele ukse nutikasse maailma tarkadest majadest rohepöördeni](#) [TalTech avab elektroonikainseneridele suunatud magistrikava](#)

Vaba mees ratastoolis

Kikkas, Kaido Arvutimaailm 1997 / 1, lk. 44-46 https://artiklid.elnet.ee/record=b2002891*est

VEDA - moVE DATA to balance the grid : research directions and recommendations for exploiting data centers flexibility within the power system

Ziaghah Ahwazi, Amin; Bordin, Chiara; **Mishra, Sambeet;** Ha, Phuong H.; Horsch, Alexander ICSCC '21: Proceedings of the 2021 6th International Conference on Systems, Control and Communications 2021 / p. 13-18 <https://doi.org/10.1145/3510362.3510365> [Conference Proceedings at Scopus](#) [Article at Scopus](#)

Why quantum state verification cannot be both efficient and secure : a categorical approach

Wiesner, Fabian; Chaoui, Ziad; Kessler, Diana-Maria; Pappa, Anna; Karvonen, Martti arXiv.org 2024 / 36 p

<https://doi.org/10.48550/arXiv.2411.04767>

Virtual academy platform supported by a semantic knowledge base

Seiler, Sven; **Sell, Raivo** Proceedings of the 7th International Conference of DAAAM Baltic Industrial Engineering : 22-24th April 2010, Tallinn, Estonia. [III] 2010 / p. 566-571 : ill

Võitlus kiipides varitsevate troojalastega tõstab Eesti teadlased kilbile

Härmat, Karin err.ee 2023 [Võitlus kiipides varitsevate troojalastega tõstab Eesti teadlased kilbile](#)

X-road data traffic open for the second year

Kalja, Ahto Information technology in public administration of Estonia : yearbook 2003 2004 / p. 32-37

Методическое пособие к лабораторным работам и курсовому проектированию по дисциплине "Схемотехника ЭВМ"

1987 https://www.ester.ee/record=b1354263*est

Система макетного обмена MAO

Bernštein, Jelena Обработка данных. Построение трансляторов. Вопросы программирования 1987 / с. 114-117