

LiD-CAT: A lightweight detector for cache ATtacks

Reinbrecht, Cezar; Hamdioui, Said; Taouil, Mottaqiallah; **Niazmand, Behrad**; **Ghasempouri, Tara**; **Raik, Jaan**; Sepulveda, Johanna 2020 IEEE European Test Symposium (ETS) : ETS 2020, May 25-29, 2020 Tallinn, Estonia : proceedings 2020 / 6 p. : ill <https://doi.org/10.1109/ETS48528.2020.9131603>

A security verification template to assess cache architecture vulnerabilities

Ghasempouri, Tara; **Raik, Jaan**; **Paul, Kolin**; Reinbrecht, Cezar; Hamdioui, Said; Taouil, M. 2020 23rd International Symposium on Design and Diagnostics of Electronic Circuits and Systems (DDECS), April 22nd – 24th 2020 Novi Sad, Serbia : Proceedings 2020 / art. 9095707, 6 p <https://doi.org/10.1109/DDECS50862.2020.9095707>

Survey on architectural attacks : a unified classification and attack model

Ghasempouri, Tara; **Raik, Jaan**; Reinbrecht, Cezar; Hamdioui, Said; Hamdioui, Said ACM Computing Surveys 2023 / art. 42 <https://doi.org/10.1145/3604803> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Towards formal verification of cache access-based side-channel attacks

Niazmand, Behrad; Reinbrecht, Cezar; **Raik, Jaan**; **Jervan, Gert**; Sepulveda, Johanna Testmethoden und Zuverlässigkeit von Schaltungen und Systemen, TUZ 2019 2019 / 2 p. : tab <http://www.informatik.uni-bremen.de/tuz/2019>

Verifying cache architecture vulnerabilities using a formal security verification flow

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