

AKKA : a coderign environment

Tammemäe, Kalle; O'Nils, Mattias; Hemani, Ahmed; Jantsch, Axel NORCHIP '95, poster session 1995 / p. 249

A comparison of six languages for system level description of telecom applications

Jantsch, Axel; Kumar, Shashi; Sander, Ingo; Svantesson, Bengt; **Öberg, Johnny;** Hemani, Ahmed; **Ellervee, Peeter;** O'Nils, Mattias
Electronic chips & systems design languages 2001 / p. 181-192 : ill https://link.springer.com/chapter/10.1007/978-1-4757-3326-6_15

Controller synthesis in control and memory centric high level synthesis system

Ellervee, Peeter; Hemani, Ahmed; Kumar, Anshul; Svantesson, Bengt; Öberg, Johnny; Tenhunen, Hannu BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 393-396: ill

Customization methodology of a Coarse Grained Reconfigurable Architecture

Azad, Siavoosh Payandeh; Farahini, Nasim; Hemani, Ahmed Norchip : 32nd NORCHIP Conference, 27-28 October 2014, Tampere, Finland 2014 / [4] p. : ill

Design of D-AMPS channel decoder with codesign methodologies

O'Nils, Mattias; **Tammemäe, Kalle;** Jantsch, Axel; Hemani, Ahmed BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 397-400: ill

DyMeP : an infrastructure to support dynamic memory binding for runtime mapping in CGRAs

Tajammul, Muhammad Adeel; Jafri, Syed Mohammad Asad Hassan; **Ellervee, Peeter;** Hemani, Ahmed; Tenhunen, Hannu; Plosila, Juha 28th International Conference on VLSI Design : held concurrently with 14th International Conference on Embedded Systems : 3-7 January 2015, Bangalore, India : proceedings 2015 / p. 547-552 : ill <https://doi.org/10.1109/VLSID.2015.98> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

DyMeP : an infrastructure to support dynamic memory binding for runtime mapping in CGRAs

Tajammul, Muhammad Adeel; Jafri, Syed Mohammad Asad Hassan; **Ellervee, Peeter;** Hemani, Ahmed; Tenhunen, Hannu; Plosila, Juha Doctoral School in Information and Communication Technology : proceedings of doctoral session of BEC 2014 : October 6-8 2014, Laulasmaa 2014 / lk. 19-22 : ill

Exploiting data transfer locality in memory mapping

Ellervee, Peeter; Miranda, Miguel; Cathoor, Francky; Hemani, Ahmed 25th EUROMICRO conference : Informatics : Theory and Practice for the New Millennium : Milan, Italy, September 8-10, 1999 : proceedings. Volume I 1999 / p. 14-21 : ill
<https://ieeexplore.ieee.org/document/793132>

A generic scheme for communication representation and mapping

Meincke, Thomas; Jantsch, Axel; **Ellervee, Peeter;** Hemani, Ahmed; Tenhunen, Hannu 17th NORCHIP Conference : Oslo, Norway, 8-9 November 1999 : proceedings 1999 / p. 334-339 : ill
https://www.researchgate.net/publication/2598862_A_Generic_Scheme_for_Communication_Representation_and_Mapping

High-level synthesis of control and memory intensive applications : thesis submitted to the Royal Institute of Technology in partial fulfillment of the requirements for the degree of Doctor of Technology

Ellervee, Peeter 2000

A HW/SW partitioning technique based on hierarchical canitate selection scheme

Jantsch, Axel; **Ellervee, Peeter;** Öberg, Johnny; Hemani, Ahmed 7th International Workshop on High-level Synthesis, Niagara-on-the Lake, Ontario, Canada, May 18-20, 1994 1994

Implementation-independent macro-library for telecommunication in VHDL

Khan, Mozammel H.; Hemani, Ahmed; Tenhunen, Hannu BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 291-294: ill

NESTIMATOR - a neural network based estimator

Ellervee, Peeter; Jantsch, Axel; Öberg, Johnny; Hemani, Ahmed 7th International Workshop on High-Level Synthesis, Niagara-on-the Lake, Ontario, Canada, May 18-20, 1994 1994

Neural network based estimator to explore the design space at system level

Ellervee, Peeter; Öberg, Johnny; Jantsch, Axel; Hemani, Ahmed BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 391-396: ill https://www.ester.ee/record=b2150914*est

System-level data format exploration for dynamically allocated data structures

Ellervee, Peeter; Miranda, Miguel; Cathoor, Francky; Hemani, Ahmed IEEE transactions on computer-aided design of integrated circuits and systems 2001 / 12, p. 1469-1472 : ill <https://ieeexplore.ieee.org/abstract/document/969440>

System-level data format exploration for dynamically allocated data structures

Ellervee, Peeter; Miranda, Miguel; Cathoor, Francky; Hemani, Ahmed 37th Design Automation Conference : Los Angeles, CA, June

5-9, 2000 : proceedings 2000 / p. 556-559 : ill <https://ieeexplore.ieee.org/document/855373>

TransMem : a memory architecture to support dynamic remapping and parallelism in low power high performance CGRAs

Tajammul, Muhammad Adeel; Jafri, Syed Mohammad Asad Hassan; Hemani, Ahmed; **Ellervee, Peeter** 2016 26th International Workshop on Power and Timing Modeling, Optimization and Simulation : PATMOS 2016 : September, 21st to 23th 2016, Bremen, Germany : proceedings 2016 / p. 92-99 : ill <https://doi.org/10.1109/PATMOS.2016.7833431>