

From FPGAs to obfuscated eASICs : design and security trade-offs

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Hardware obfuscation of digital FIR filters

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Hardware Trojans for confidence reduction and misclassifications on neural networks

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High-level intellectual property obfuscation via decoy constants

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A pragmatic methodology for blind hardware trojan insertion in finalized layouts

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A survey on split manufacturing : attacks, defenses, and challenges

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