

Towards a healthcare cybersecurity certification scheme

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Verifying cache architecture vulnerabilities using a formal security verification flow

Ghasempouri, Tara; **Raik, Jaan**; **Paul, Kolin**; Reinbrecht, Cezar; Hamdioui, Said; Taouil, Mottaqiallah Microelectronics reliability 2021 / art. 114085 <https://doi.org/10.1016/j.microrel.2021.114085> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)