

CAD in the education of microelectronics at the Technical University of Budapest

Szekely, V.; Tarnay, K. Automation, simulation & measurement : ASM'91 : 3rd biennial conference, Tallinn, October 7-11, 1991. Section S / Tallinn Technical University 1992 / p. 146-151

Circuit simulation program oriented physical modeling of integrated circuit elements

Rang, Toomas; Tarnay, K.; Szekely, V. Periodica polytechnica. Electrical engineering = Электротехника 1980 / p. 37-45
https://www.ester.ee/record=b1198855*est

Design-aid programs for LSI integrated circuits

Tarnay, K.; Szekely, V.; Baji, P.; Farkas, G.; Masszi, F.; Kerecsen-Rencz, M. Проблемы моделирования и измерения в электронике 1986 / p. 3-11

Integrált áramköri elemek fizikai modellezése áramkölanalizös program segítségével

Rang, Toomas; Tarnay, K.; Szekely, V. Híradástechnika = Journal on communications, computers, convergence, contents, companies 1980 / p. 322-326

Modeling in semiconductor electronics

Javor, A.; **Rang, Toomas**; Szekely, V.; Tarnay, K.; **Velmre, Enn** 1993 https://www.ester.ee/record=b1031052*est

Электротехника и автоматика

Kukk, Vello; Tarnay, K.; Szekely, V.; Baji, P.; Farkas, G.; Masszi, F.; Kerecsen-Rencz, M.; **Rang, Toomas**; **Plakk, Paul**; **Plakk, Peeter**; Plakk, T.; **Gurjanov, Boris**; **Männama, Vello**; **Laansoo, Ants**; **Pikkov, Otto**; Rudski, V.A.; **Köverik, Kait** 1986
https://www.ester.ee/record=b1296007*est