

### **Heterogeneous Approximation of DNN HW Accelerators based on Channels Vulnerability**

**Cherezova, Natalia**; Pappalardo, Salvatore; **Taheri, Mahdi**; **Ahmadilivani, Mohammad Hasan**; Deveautour, Bastien; Bosio, Alberto; **Raik, Jaan**; **Jenihhin, Maksim** 2024 IFIP/IEEE 32nd International Conference on Very Large Scale Integration (VLSI-SoC) 2024 / 4 p. : ill <https://doi.org/10.1109/VLSI-SoC62099.2024.10767798> [Conference proceedings at Scopus](#) [Article at Scopus](#)

### **SAFFIRA: a Framework for Assessing the Reliability of Systolic-Array-Based DNN Accelerators**

**Taheri, Mahdi**; **Daneshtalab, Masoud**; **Raik, Jaan**; **Jenihhin, Maksim**; Pappalardo, Salvatore; Jimenez, Paul; Deveautour, Bastien; Bosio, Alberto 2024 27th International Symposium on Design & Diagnostics of Electronic Circuits & Systems (DDECS) : 03-05 April 2024, Kielce, Poland 2024 / p. 19–24 : ill <https://doi.org/10.1109/DDECS60919.2024.10508925>

### **Special session : approximation and fault resiliency of DNN accelerators**

**Ahmadilivani, Mohammad Hasan**; Barbareschi, Mario; Barone, Salvatore; Bosio, Alberto; **Daneshtalab, Masoud**; Torca, Salvatore Della; Gavarini, Gabriele; **Jenihhin, Maksim**; **Raik, Jaan**; **Taheri, Mahdi** 2023 IEEE 41st VLSI Test Symposium (VTS) : proceedings 2023 / 10 p. : ill <https://doi.org/10.1109/VTS56346.2023.10140043> [Conference proceeding at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

### **Special session: reliability assessment recipes for DNN accelerators**

**Ahmadilivani, Mohammad Hasan**; Bosio, Alberto; Deveautour, Bastien; Dos Santos, Fernando Fernandes; Guerrero-Balaguera, Juan-David; **Jenihhin, Maksim**; Kritikakou, Angeliki; Sierra, Robert Limas; **Raik, Jaan**; **Taheri, Mahdi** 42nd IEEE VLSI Test Symposium, VTS 2024 2024 / 11 p. : ill <https://doi.org/10.1109/VTS60656.2024.10538707> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)