

**Deadlock-free generic routing algorithms for 3-dimensional Networks-on-Chip with reduced vertical link density topologies**

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**Dynamic quadrant partitioning adaptive routing algorithm for irregular reduced vertical link density topology 3-dimensional network-on-chips**

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**Fast and optimized task allocation method for low vertical link density 3-Dimensional Networks-on-Chip based many core systems**

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**GSNoC — the comprehensive design platform for 3-dimensional Networks-on-Chip based many core embedded systems**

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**A hardware/software co-design reconfigurable network-on-chip FPGA emulation method**

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