

An interesting and effective code for error-correcting in mass-storage devices

Szepessy, Andras BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 1 1994 / p. 331-337

Application of model checking in memory arbiter synthesis

Ernits, Juhan-Peep Ninth Estonian Winter School in Computer Science (EWSCS'04) : Lahemaa, Estonia, February 29-March 5, 2004 2004 / [1 p.]. (Student talks and posters 2004 (abstracts))

Arvutid nõöpi

Sinivee, Veljo Praktiline Arvutikasutaja 2006 / 3, lk. 33-35 : ill https://artiklid.elnet.ee/record=b1053463*est

Code compaction within CGRAs

Tajammul, Muhammad Adeel; Jafri, Syed Mohammad Asad Hassan; **Ellervee, Peeter** Proceedings of the 8th Annual Conference of the Estonian National Doctoral School in Information and Communication Technologies : December 5-6, 2014, Rakvere 2014 / p. 133-136 : ill

Digiprügi – peamised leiukohad ja 15 viisi, kuidas sellest hoiduda

digi.geenius.ee 2023 [Digiprügi – peamised leiukohad ja 15 viisi, kuidas sellest hoiduda](https://artiklid.elnet.ee/record=b1053463*est)

Dünaamilised mälud : metoodiline materjal

1989 https://www.ester.ee/record=b1215453*est

Fully-fusible convolutional neural networks for end-to-end fused architecture with FPGA implementation

Dadras, Iman; Seydi, Sakineh; **Ahmadilivani, Mohammad Hasan; Raik, Jaan; Salehi, Mostafa E.** 2023 30th IEEE International Conference on Electronics, Circuits and Systems (ICECS) 2023 / 5 p. <https://doi.org/10.1109/ICECS58634.2023.10382831>

Hierarhilis-temporaalse mälu printsiip

Tammemäe, Kalle A & A 2007 / 3, lk. 9-19 : ill https://artiklid.elnet.ee/record=b1020789*est

Hindamatud mälestused ei pruugi säilida moodsatel andmekandjatel kuigi kaua

Piir, Rait novaator.err.ee 2024 [Hindamatud mälestused ei pruugi säilida moodsatel andmekandjatel kuigi kaua](https://artiklid.elnet.ee/record=b1020789*est)

Hybrid BIST time minimization for core-based systems with STUMPS architecture

Jervan, Gert; Eles, Petru; Peng, Zebo; Ubar, Raimund-Johannes; Jenihhin, Maksim 18th IEEE International Symposium on Defect and Fault Tolerance in VLSI Systems : 3-5 November 2003, Boston, Massachusetts : proceedings 2003 / p. 225-232 : ill <https://ieeexplore.ieee.org/document/1250116>

Improving statistical approach for memory leak detection using machine learning

Šor, Vladimir; **Treier, Tarvo**; Srirama, Satish Narayana; Oü, Plumb 2013 IEEE International Conference on Software Maintenance : 22-28 September 2013, Eindhoven, The Netherlands : proceedings 2013 / p. 544-547 <https://doi.org/10.1109/ICSM.2013.92>
[Conference Proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

In-system programming of non-volatile memories on microprocessor-centric boards

Tšertov, Anton; Devadze, Sergei; Jutman, Artur; Jasnetski, Artjom International journal of microelectronics and computer science 2014 / p. 25-34 : ill

Integraalsete mäluelementide kasutamine kujutise salvestamisel

Niinsalu, Udo Side. Raadio. Televisioon : infoseeria 10 1980 / lk. 13-15 : joon https://www.ester.ee/record=b1232303*est

Intel 1103 - esimene DRAM-kiip

Toomsalu, Arvo A & A 2006 / 5, lk. 27-32 https://artiklid.elnet.ee/record=b1019636*est

I-nööp PIC'i kastmes

Sinivee, Veljo Praktiline Arvutikasutaja 2006 / 10, lk. 28-30 : ill https://artiklid.elnet.ee/record=b1055399*est

Jäljevahemälu

Toomsalu, Arvo A & A 2002 / 2, lk. 8-19 : ill https://artiklid.elnet.ee/record=b1009485*est

Magnetmäluseadmed

Mägi, Harri 1978 https://www.ester.ee/record=b1274933*est

Marginal PCB assembly defect detection on DDR3/4 memory bus

Odintsov, Sergei; Jutman, Artur; Devadze, Sergei 2017 IEEE International Test Conference (ITC 2017) : Forth Worth, Texas, USA, 31 October - 2 November 2017 2017 / p. 238-247 : ill <https://doi.org/10.1109/TEST.2017.8242070> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

Mikroprotsessorsüsteemid : kursuseprojekti metoodiline juhend

Toomsalu, Arvo 1996 https://www.ester.ee/record=b1619347*est

Mitmekihiliste struktuuride väljatöötamine magnetoptilise mälu jaoks

Antsov, U.; Lott, Kalju XXXII üliõpilaste teaduslik-tehnilise konverentsi ettekannete teesid : pühendatud V. I. Lenini 110. sünniaastapäevale : 16.-18. aprill 1980 1981 / lk. 126 https://www.ester.ee/record=b1322611*est

Mälu pneumo- ja elek[t]ropneumoautomaatikas

Ševtšenko, Larissa; Parik, Ellen Elektriala 2009 / 4, lk. 16-18 : ill

Märkmikmälu sardsüsteemis

Toomsalu, Arvo A & A 2011 / 1, lk. 9-18 : ill https://artiklid.elnet.ee/record=b2401506*est

Network-on-ReRAM for scalable processing-in-memory architecture design

Dabiri, Bitā; Modarressi, Mehdi; **Daneshtalab, Masoud** 24th Euromicro Conference on Digital System Design (DSD) 2021 <https://doi.org/10.1109/DSD53832.2021.00031>

NV-SP: A new high performance and low energy NVM-Based scratch pad

Shalabi, Ameer; Paul, Kolin; Ghasempouri, Tara; Raik, Jaan 2020 IEEE Computer Society Annual Symposium on VLSI : ISVLSI 2020, 6–8 July 2020, Limassol, Cyprus 2020 / art. 19876866, p. 54–59 <https://doi.org/10.1109/ISVLSI49217.2020.00020>

Nööpmälu iButton

Toomsalu, Arvo A & A 2007 / 5, lk. 8-17 : ill https://artiklid.elnet.ee/record=b1021105*est

On BTI aging rejuvenation in memory address decoders

Gürsoy, Cemil Cem; Kraak, Daniel; Ahmed, Foisal; Taouil, Mottaqiallah; **Jenihhin, Maksim**; Hamdioui, Said 2022 IEEE 23rd Latin American Test Symposium, LATS 2022 2022 / Code 184360 <https://doi.org/10.1109/LATS57337.2022.9936940>

On in-system programming of non-volatile memories

Tšertov, Anton; Devadze, Sergei; Jutman, Artur; Jasnetski, Artjom International journal of microelectronics and computer science 2013 / p. 72-78 : ill

On in-system programming of non-volatile memories

Tšertov, Anton; Devadze, Sergei; Jutman, Artur; Jasnetski, Artjom Proceedings of the 20th International Conference Mixed Design of Integrated Circuits and Systems : MIXDES 2013, Gdynia, Poland, June 20-22, 2013 2013 / p. 408-413 : ill

Optimal scheduler synthesis using cost optimal reachability analysis

Ernits, Juhan-Peep Tenth Estonian Winter School in Computer Science (EWSCS'05) : Lahemaa, Estonia, February 27-March 4, 2005 2005 / [1 p.]. (Student talks and posters 2005 (abstracts))

Polymorphic configuration architecture for CGRAs

Jafri, Syed Mohammad Asad Hassan; **Tajammul, Muhammad Adeel**; Hermani, Ahmed; Paul, Kolin; Plosila, Juha; **Ellervee, Peeter**; Tenhunen, Hannu IEEE transactions on Very Large Scale Integration (VLSI) Systems 2016 / p. 403-407 : ill <http://dx.doi.org/10.1109/TVLSI.2015.2402392>

Pooljuhtmälu seadmed : õppevahend

Mägi, Harri 1978 https://www.ester.ee/record=b1313957*est

Prootonmälu

Toomsalu, Arvo A & A 2008 / lk. 8-15 : ill https://artiklid.elnet.ee/record=b1022653*est

Prospective research topics towards preserving electronic health records in decentralised content-addressable storage networks

Klementi, Toomas; Kankainen, Kristian Juha Ismo; Piho, Gunnar; Ross, Peeter HEDA 2022 : The International Health Data Workshop HEDA 2022 2022 / code 184051 https://ceur-ws.org/Vol-3264/HEDA22_paper_7.pdf [Conference Proceedings at Scopus](#) [Article at Scopus](#)

Protsessor mälus

Toomsalu, Arvo A & A 2001 / 4, lk. 9-16 https://artiklid.elnet.ee/record=b1007646*est

RISC-mikroprotsessorite arhitektuur

Toomsalu, Arvo 1995 https://www.ester.ee/record=b1069536*est

Salapärased akronüümid

Toomsalu, Arvo Arvutimaailm 1997 / 6, lk. 9-11

Security in ad-hoc sensor networks with pre-loaded time limited memory keys

Kimlaychuk, Vadim 2011 4th International Conference on Biomedical Engineering and Informatics (BMEI) : 15-17 October 2011 : proceedings. 4 2011 / p. 2067-2071 : ill <https://ieeexplore.ieee.org/stamp/stamp.jsp?arnumber=6098723>

Simulation of advanced super-speed BiCMOS static memory cells based on the reverse base current effect of avalanche transistors

Bubennikov, Alexander; Zykov, A. BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 63-66: ill

Software-based mitigation for memory address decoder aging

Kraak, D. H. P.; Gürsoy, Cemil Cem; Jenihhin, Maksim; Raik, Jaan LATS 2019 : 20th IEEE Latin American Test Symposium : Santiago, Chile, March 11th - 13th 2019 2019 / 6 p. : ill <https://doi.org/10.1109/LATW.2019.8704595>

Storage Area Networks

Leis, Paul A & A 2000 / 6, lk. 3-4

Sõltumatute ketaste liiasmassiiv (RAID)

Vapper, Virgo A & A 2007 / 5, lk. 28-37 : ill https://artiklid.elnet.ee/record=b1021107*est

Z-RAM-mälu

Toomsalu, Arvo A & A 2008 / 3, lk. 10-19 https://artiklid.elnet.ee/record=b1022321*est

Transpuutrid

Toomsalu, Arvo Arvutustehnika ja Andmetöötlus 1997 / 4, lk. 14-16; 5, lk. 7-11; 6, lk. 6-9; 7/8, lk. 1-6: ill

Unustatud mälad

Toomsalu, Arvo Arvutustehnika ja Andmetöötlus 1996 / 1, lk. 32-40: ill

USB väikmälu

Toomsalu, Arvo A & A 2005 / 6, lk. 7-14 https://artiklid.elnet.ee/record=b1018681*est

Utilising processor cache for speeding up reachability checks in model checking

Ernits, Juhan-Peep Eleventh Estonian Winter School in Computer Science (EWSCS'06) : Park Hotel Palmse, Lahemaa, Estonia : March 5-10, 2006 2006 / [1] p

Valkmälu

Toomsalu, Arvo Arvutimaailm 1995 / 7, lk. 42-44: ill

xTractor: an academic higg-level synthesis tool for control and memory intensive applications

Ellervee, Peeter 20th IEEE Conference NORCHIP'2002, Copenhagen, Denmark 2002 / p. 253-258
<https://citeseerx.ist.psu.edu/document?repid=rep1&type=pdf&doi=6db1d71884b75b47fb27ae86b91b61894e57fd18>

XXI sajandi mälu kiip talletab tohutult infot

Krustok, Jüri Eesti Päevaleht 1999 / 9. sept., lk. 11

О минимизации требуемой памяти ЦВМ при частичном решении уравнений цепи

Ronk, Ants Тезисы докладов республиканской научно-технической конференции, посвященной Дню радио, Таллин, 1977 1977 / с. 78-79 https://www.ester.ee/record=b1313776*est

Отображение структур данных в памяти ЭВМ

Allik, Kaarel; Mikli, Toomas Обработка информации. Функциональный анализ 1974 / с. 23-30
https://www.ester.ee/record=b1347059*est <https://digikogu.taltech.ee/et/Item/0dd16355-1e9a-4ed9-b616-b6d162fa44a5>

Полная оптимизация памяти анализаторов предшествования

Vooglaid, Aare; Liib, Donald Построение трансляторов, обработка данных, вопросы программирования 1980 / с. 15-29 : илл
https://www.ester.ee/record=b1264940*est <https://digikogu.taltech.ee/et/Item/c45a8e3c-2112-4737-8026-a158196b9d12>

Современные носители данных не гарантируют сохранности фотографий и видео

Piir, Rait nauka.err.ee 2024 [Современные носители данных не гарантируют сохранности фотографий и видео](https://nauka.err.ee/2024/01/24/sovremennye-nositeli-dannyyh-ne-garantiruyut-sohrannosti-fotografii-i-vidео)

Схема немагазинного распределения оперативной памяти ЭВМ "Минск-32"

Allik, Kaarel; Mikli, Toomas; Tedersoo, Kalju Обработка информации. Функциональный анализ 1974 / с. 31-36 : илл
https://www.ester.ee/record=b1347059*est <https://digikogu.taltech.ee/et/Item/0dd16355-1e9a-4ed9-b616-b6d162fa44a5>

Тонкие магнитные пленки как элементы ассоциативного запоминающего устройства : автореферат ... кандидата технических наук (252)

Pihlau, Jaak 1969 https://www.ester.ee/record=b2333088*est