

A general approach to synthesis of the finite-state machine logical structure in the sum-of-products form

Chapenko, V.; Fritsnovich, G.; Kalberzin, A.; Lange, E. BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 385-390 https://www.ester.ee/record=b2150914*est